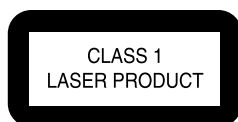


Service
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Service Manual



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PHILIPS

1. Technical Specifications and Connection Facilities

Specifications

PLAYBACK SYSTEM

DVD Video
SACD multi channel and SACD stereo
Video CD & SVCD
CD (CD-Recordable and CD-Rewritable)
MP3 CD

TV STANDARD (PAL/50Hz) (NTSC/60Hz)

Number of lines	625	525
Playback	Multistandard	(PAL/NTSC)

VIDEO PERFORMANCE

RGB output	0.7Vpp into 75 ohm
YUV output	Y: 1Vpp into 75 ohm Pr/Cr Pb/Cb: 0.7Vpp into 75 ohm
S-Video output	Y: 1Vpp into 75 ohm C: 0.3Vpp into 75 ohm
Video output	1 Vpp into 75 ohm
Black Level Shift	On/Off
Video Shift	Left/Right

AUDIO FORMAT

DSD	Multichannel and Stereo
MPEG/	Compressed Digital
Dolby Digital	16, 20, 24 bits
DTS/PCM	fs, 44.1, 48, 96 kHz
MP3	96, 112, 128, 256 kbps and
(ISO 9660)	variable bit rate fs 32, 44.1, 48kHz
Full decoding of Dolby Digital and DTS multi channel sound	
Analogue Stereo Sound	
Dolby Surround-compatible downmix from Dolby Digital multi-channel sound	
3D Sound for virtual 5.1 channel sound on 2 speakers	

SACD AUDIO PERFORMANCE

D/A Converter	DSD
SACD	fs 2.8224MHz DC - 100kHz
Max. output voltage (0dB)	2V rms
Channel unbalance	<0.5 dB
Cut-off frequency	50kHz (Front) 40kHz (Surround, Centre, Subwoofer)
Signal-Noise (1kHz)	-125 dB
Dynamic Range (1kHz)	-105 dB
Crosstalk (1kHz)	-120 dB
Total Harmonic Distortion (1kHz)	-100 dB

AUDIO PERFORMANCE (Without upsampling)

DA Converter	24 bits
DVD	fs 96 kHz 4 Hz - 44 kHz
CD/Video CD	fs 44.1 kHz 4 Hz - 20 kHz
S-Video CD	fs 48 kHz 4 Hz - 22 kHz
	fs 44.1 kHz 4 Hz - 20 kHz
Signal-Noise (1kHz)	< -120 dB
Dynamic Range (1kHz)	< -95 dB
Crosstalk (1kHz)	< -110 dB
Total Harmonic Distortion (1kHz)	< -95 dB
MPEG MP3	MPEG Audio L3

CONNECTIONS

SCART	2x Euroconnector
Y Pb/Cb Pr/Cr (480i/p)	Cinch 3x (green, blue, red)
S-Video Output	Mini DIN, 4 pins
Video Output	Cinch (yellow) 2x
Audio L+R output	Cinch (white/red)
Digital Output	1 coaxial, 1 optical IEC958 for CDDA / LPCM IEC1937 for MPEG1/2, Dolby Digital, DTS

6 channel analogue output

Audio Front L/R	Cinch (white/red)
Audio Surround L/R	Cinch (white/red)
Audio Centre	Cinch (blue)
Audio Subwoofer	Cinch (black)

CABINET

Dimensions (w x h x d)	435 x 101 x 315 mm
Weight	Approximately 4.6 Kg

POWER SUPPLY

Power inlet	110-240V, 50/60Hz
Power usage	Approx. 30W
Power usage standby	< 1W

* typical playing time for movie with 2 spoken languages and 3 subtitle languages

Specifications subject to change without prior notice


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2. Safety Instructions, Warnings and Notes

2.1 Safety Instructions

2.1.1 General Safety

Safety regulations require that during a repair:

- Connect the unit to the mains via an isolation transformer.
- Replace safety components, indicated by the symbol , only by components identical to the original ones. Any other component substitution (other than original type) may increase risk of fire or electrical shock hazard.

Safety regulations require that after a repair, you must return the unit in its original condition. Pay, in particular, attention to the following points:

- Route the wires/cables correctly, and fix them with the mounted cable clamps.
- Check the insulation of the mains lead for external damage.
- Check the electrical DC resistance between the mains plug and the secondary side:
 1. Unplug the mains cord, and connect a wire between the two pins of the mains plug.
 2. Set the mains switch to the 'on' position (keep the mains cord unplugged!).
 3. Measure the resistance value between the mains plug and the front panel, controls, and chassis bottom.
 4. Repair or correct unit when the resistance measurement is less than 1 MΩ.
 5. Verify this, before you return the unit to the customer/user (ref. UL-standard no. 1492).
 6. Switch the unit 'off', and remove the wire between the two pins of the mains plug.

2.1.2 Laser Safety

This unit employs a laser. Only qualified service personnel may remove the cover, or attempt to service this device (due to possible eye injury).

Laser Device Unit

Type	: Semiconductor laser GaAlAs
Wavelength	: 650 nm (DVD) : 780 nm (VCD/CD)
Output Power	: 20 mW (DVD+RW writing) : 0.8 mW (DVD reading) : 0.3 mW (VCD/CD reading)
Beam divergence	: 60 degree

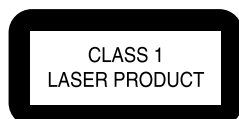



Figure 2-1 Class 1 Laser Product

Note: Use of controls or adjustments or performance of procedure other than those specified herein, may result in hazardous radiation exposure. Avoid direct exposure to beam.

2.2 Warnings

2.2.1 General

- All ICs and many other semiconductors are susceptible to electrostatic discharges (ESD, symbol ) . Careless handling during repair can reduce life drastically. Make sure that, during repair, you are at the same potential as the mass of the set by a wristband with resistance. Keep components and tools at this same potential. Available ESD protection equipment:
 - Complete kit ESD3 (small tablemat, wristband, connection box, extension cable and earth cable) 4822 310 10671.
 - Wristband tester 4822 344 13999.
- Be careful during measurements in the live voltage section. The primary side of the power supply (pos. 1005), including the heatsink, carries live mains voltage when you connect the player to the mains (even when the player is 'off!'). It is possible to touch copper tracks and/or components in this unshielded primary area, when you service the player. Service personnel must take precautions to prevent touching this area or components in this area. A 'lightning stroke' and a stripe-marked printing on the printed wiring board, indicate the primary side of the power supply.
- Never replace modules, or components, while the unit is 'on'.

2.2.2 Laser

- The use of optical instruments with this product, will increase eye hazard.
- Only qualified service personnel may remove the cover or attempt to service this device, due to possible eye injury.
- Repair handling should take place as much as possible with a disc loaded inside the player.
- Text below is placed inside the unit, on the laser cover shield:

CAUTION VISIBLE AND INVISIBLE LASER RADIATION WHEN OPEN AVOID EXPOSURE TO BEAM
 ADVARSEL SYNLIG OG USYNLIG LASERSTRÅLING NÅR DEKSEL ÅPNES UNNGÅ EKSPONERING FOR STRÅLEN
 ADVARSEL SYNLIG OG USYNLIG LASERSTRÅLING NÅR DENNA DEL ÅR ÖPPNAD BETRÄKTA EJ STRÅLEN
 VARO! AVATT AESSA OLET ALTTIINA NÄKYVÄLLE JA NÄKYMÄTTÖMÄLLE LASER SÄTEILYLLE. ÄLÄ KATSO SÄTEESEEN
 VORSICHT SICHTBARE UND UNSICHTBARE LASERSTRAHLUNG WENN ABDECKUNG GEÖFFNET NICHT DEM STRAHL AUSSETZEN
 DANGER VISIBLE AND INVISIBLE LASER RADIATION WHEN OPEN AVOID DIRECT EXPOSURE TO BEAM
 ATTENTION RAYO NNEMENT LASER VISIBLE ET INVISIBLE EN CAS D'OUVERTURE EXPOSITION DANGEREUSE AU FAISCEAU

Figure 2-2 Warning text

2.2.3 Notes

Dolby

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Figure 2-3

Trusurround

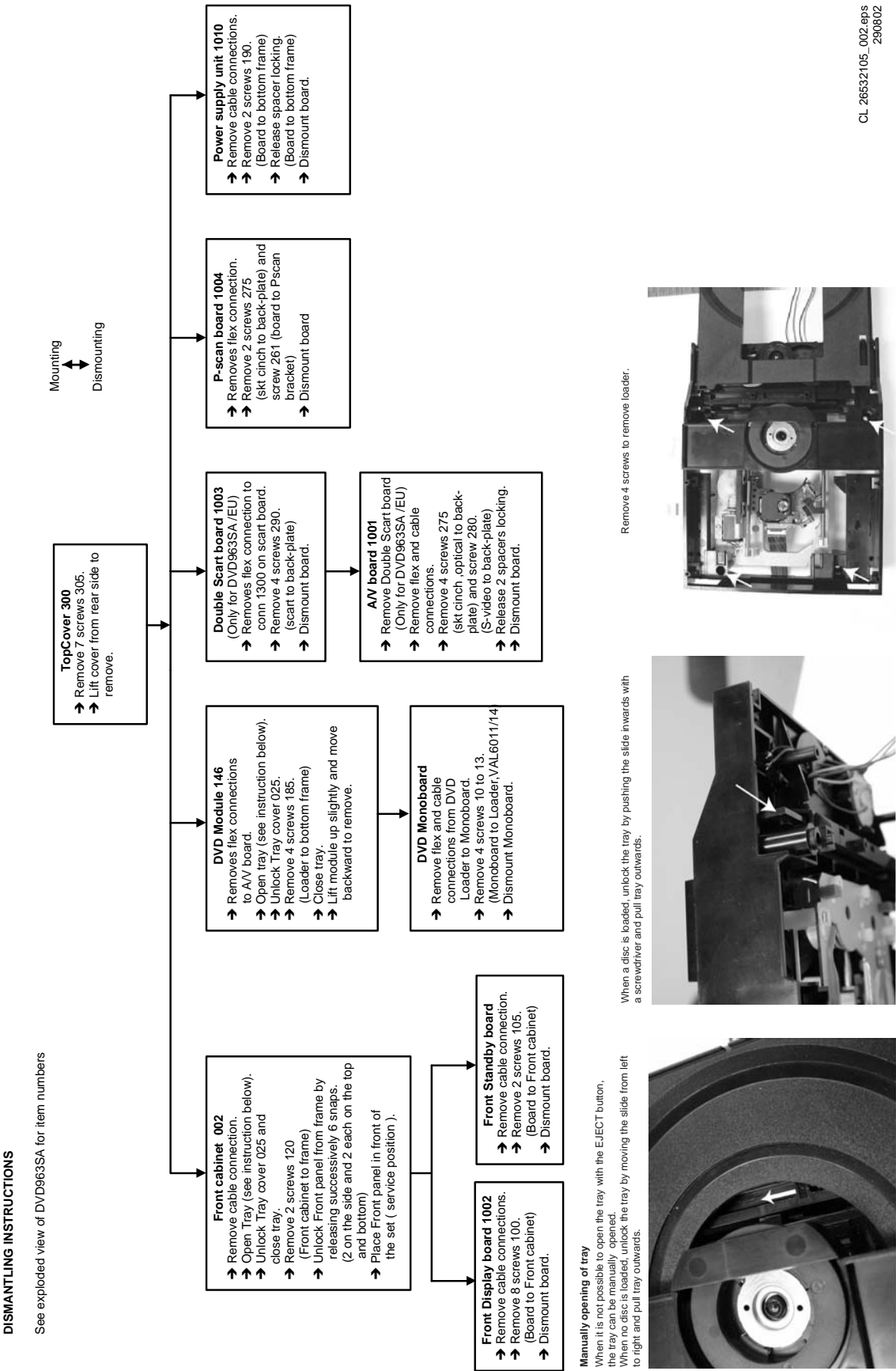
TRUSURROUND, SRS and symbol (fig 2-4) are trademarks of SRS Labs, Inc. TRUSURROUND technology is manufactured under licence from SRS labs, Inc.



Figure 2-4

4. Mechanical-, Dismantling Instructions, and Service Hints

4.1 Dismantling Instructions



4.2 Exploded View

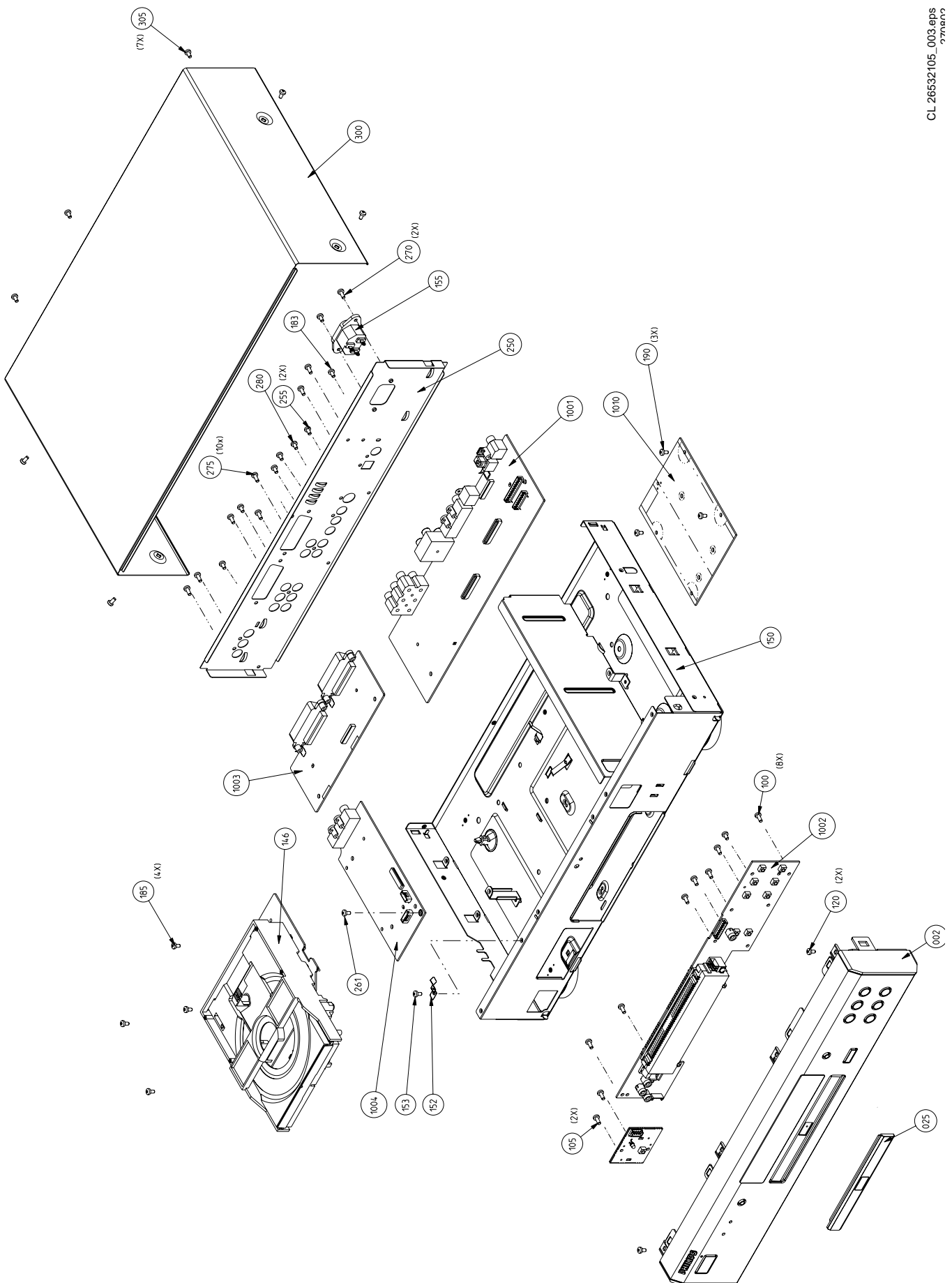


Figure 4-2 Exploded View

4.3 Service Hints

4.3.1 Switched Mode Power Supply

The power supply unit has to be replaced in case of failure. The schematic provided in the manual is only for information and no service parts will be available.

4.3.2 DVD Module

This module can be repaired as follows:

1. The VAL6011/14 is a combination of loading mechanism and DVD-mechanism. Both are not repairable units and in case of failure, it has to be replaced with a new loader VAL6011/14.

Note: When replacing with a new VAL6011/14, two solder joints have to be removed after connecting the OPU flex foil to the mono board.

The solder joints, which shortcircuits the laser diodes to ground, are for protection against ESD. Refer to figures 2-5 and 2-6 for location of solder joints.

2. The mono board has to be repaired down to component level. Repair handling of the monoboard requires a workshop with sophisticated desoldering tools.

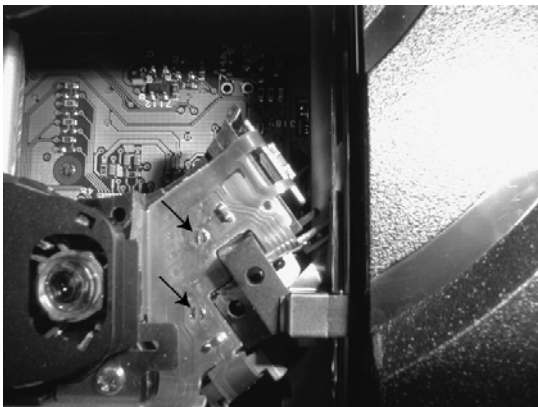


Figure 4-3 Solder joints

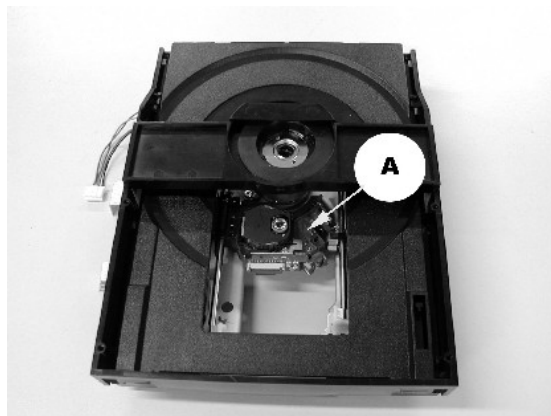


Figure 4-4 Solder joints

4.3.3 ComPair

For assistance with the repair process of the monoboard an electronic fault finding guidance has been developed. This program is called ComPair.

This ComPair program is available on CDROM.

The version of the CDROM for repair of the monoboard is V1.3 or higher and can be ordered with codenumber 4822 727 21637. This is an update CDROM, so when the ComPair CDROM is used for the first time, one has to install the ComPair Engine CDROM V1.2 first.

The V1.2 CDROM can be ordered with code number 4822 727 21634 and has to be registered after installation. The procedure for registration is explained in the help file of the program and in the CDROM booklet.

The cable to connect the monoboard with a PC can be ordered with codenumber: 3122 785 90017.

All the hardware and software requirements of the systems, necessary for working with ComPair, are described on the CDROM.

4.3.4 Service Positions

Refer to dismantling instructions for dismantling of the board. Figures 2-7 to 2-8 shows the service position that are recommended during repair of the boards.

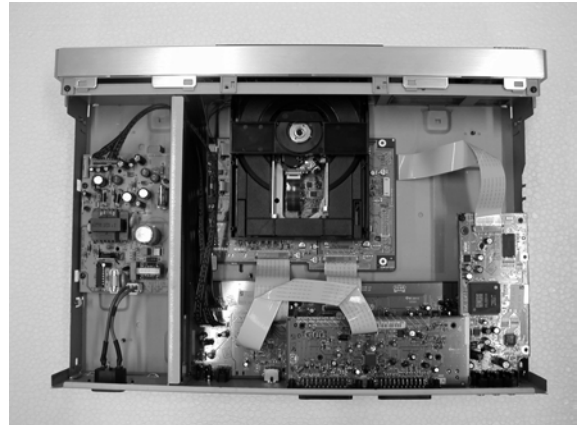


Figure 4-5 DVD963SA Layout

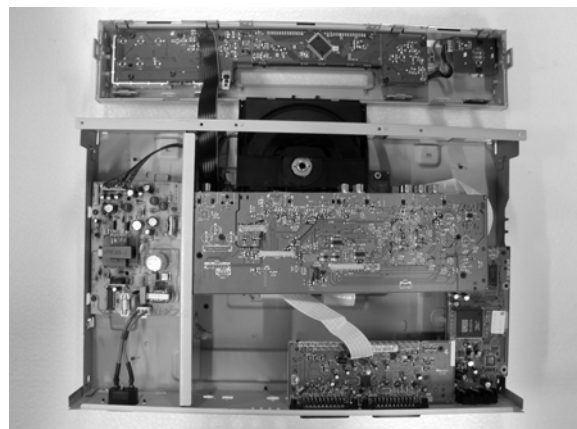


Figure 4-6 DVD963SA service manual

5. Diagnostic Software, Trouble Shooting and Test Instructions

5.1 Dealerscript

5.1.2 Contents of Dealer Script

5.1.1 Purpose of Dealer Script

The dealer script can give a diagnosis on a standalone DVD player, no other equipment is needed to perform a number of hardware tests to check if the DVD player is faulty. The diagnosis is simply a "error" or "pass" message. No indication is given of faulty hardware modules. Only tests within the scope of the diagnostic software will be executed hence only faults within this scope can be detected.

The dealer script executes all diagnostic nuclei that do not need any user interaction and are meaningful on a standalone DVD player.

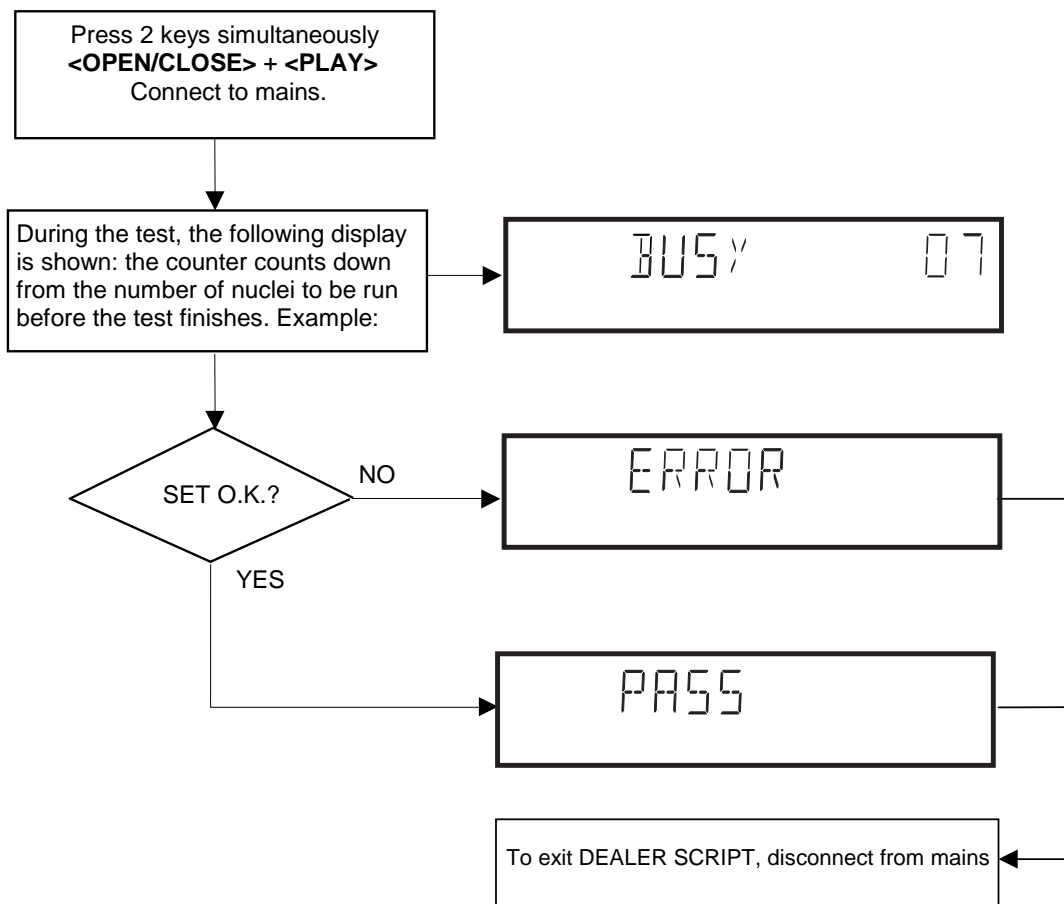
The nuclei called in the dealer script are the following (the number after each nucleus name corresponds with the number being on the local display when the nucleus is executed during the dealer script):

Nucleus

Display Countdown	Nucleus Number	Nucleus Name	Description
7	6	PapChksFl	Calculate and verify checksum of FLASH memory
6	12	PapI2cDisp	Checks the I2C interface with the slave processor on the display board
5	13	PapS2bEcho	Checks the I2C interface to the basic engine
4	11	PapI2cNvram	Checks the I2C interface with the NVRAM
3	15	PapNvramWrR	Pattern test of all locations in the NVRAM
2	16	CompSdramWrR	Pattern test of all locations in the SDRAM(s)
1	63	FUORERSdramWrRLow	Pattern test of all locations in the SDRAM(s)

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Figure 5-1 Dealer script nuclei



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Figure 5-2 Dealer Script

5.2 Player Script

Press the OPEN/CLOSE key to proceed to the next test.

5.2.1 Purpose of Player Script

The Player script will give the opportunity to perform a test that will determine which of the DVD player's modules are faulty, to read the error log and error bits and to perform an endurance loop test. To successfully perform the tests, the DVD player must be connected to a TV set to check the output of a number of nuclei. For DVDv2b a multi-channel amplifier, a set of 6 speakers and an external video source are necessary to test. To be able to check results of certain nuclei, the player script expects some interaction of the user (i.e. to approve a test picture or a test sound). Some nuclei (e.g. nuclei that test functionality of the Basic Engine module) require that the DVD player itself is opened, to enable the user to observe moving parts and approve their movement visually. Only tests within the scope of the diagnostic software will be executed hence only faults within this scope can be detected.

5.2.2 Contents of Player Script

The player script contains all nuclei that are useful on a DVD player that is connected to a TV set and help to determine which module of the DVD player is faulty, as well as to read out the contents of the error logs.

5.2.3 Structure of Player Script

The player script consists of a set of nuclei testing the three hardware modules in the DVD player: the Display PWB, the Digital PWB, and the Basic Engine. Nuclei run by the player test need some user interaction. In the next paragraph this interaction is described. The player test is done in two phases:

- Interactive tests:** this part of the player test depends strongly on user interaction and input to determine nucleus results and to progress through the full test. Reading the error log and error bits information can be useful to determine any errors that occurred recently during normal operation of the DVD player.
- The loop test:** this part of the player test will loop through the list of nuclei indefinitely, till the player is reset. The list of nuclei is as follows:
 - PapChksFlash
 - PapI2cNvram
 - CompSdramWrR
 - PapS2bEcho
 - PapI2cDisp

At the beginning of the tests, the DSW version number will be indicated on the local display of the DVD. The display will look like the following:

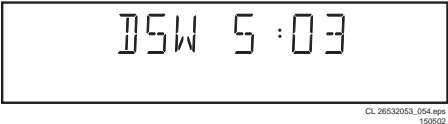


Figure 5-3

Pressing the PLAY key will proceed to the slave S/W version display, which is shown on the local display of the DVD player. The display will look like the following:

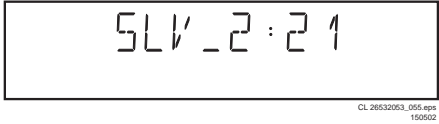


Figure 5-4

5.2.4 Survey

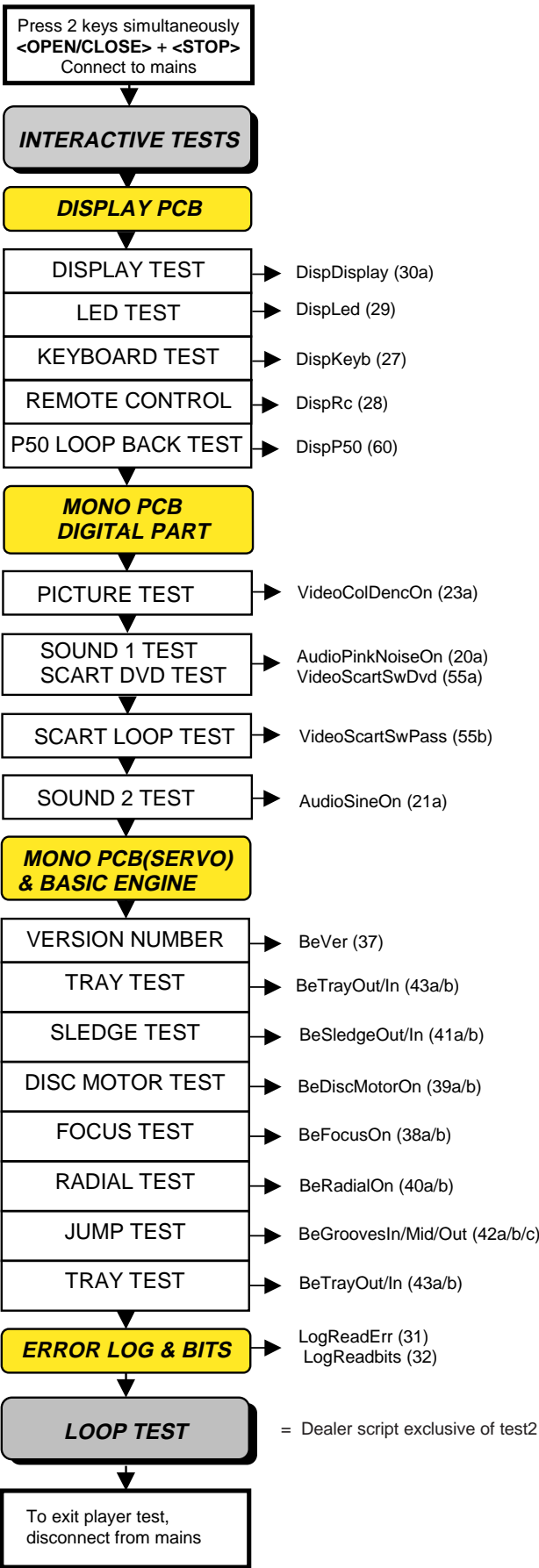


Figure 5-5

5.3 Display PCB

5.3.1 Display Test

The display test is performed by nucleus DispDisplay. By putting a series of test patterns on the local display, the local display is tested. To step through all different patterns, the user must either press OPEN/CLOSE (pattern is ok) or STOP (pattern was incorrect) to proceed to the next pattern. The display of patterns is continued in a cyclic manner, shown in Fig. 5-6, until the user presses PLAY. If the user presses PLAY before all display patterns are tested, the DispDisplay nucleus will return FALSE (display test unsuccessful).

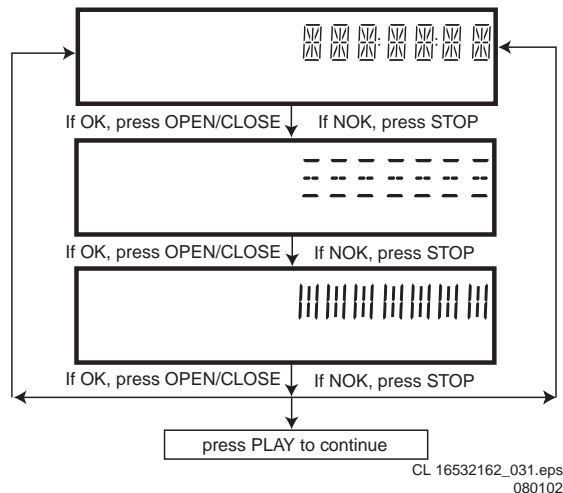


Figure 5-6

5.3.2 LED Test

The LED(s) on the DVD player is (are) tested by nucleus DispLed. The user must check if the LED(s) is (are) lighted; if it is, press OPEN/CLOSE, if it is not, press STOP. By pressing PLAY the script will proceed to the next test. If the user presses PLAY before OPEN/CLOSE or STOP, the DispLed nucleus will return TRUE (LED test successful).

5.3.3 Keyboard Test

The keyboard of the DVD player is tested by nucleus DispKeyb. The user is expected to press all keys on the local keyboard once. The code of the key pressed is shown on the local display (1 hexadecimal digit) immediately followed by a (hexadecimal) number indicating how many times that key has been pressed. Example of the local display during this test:

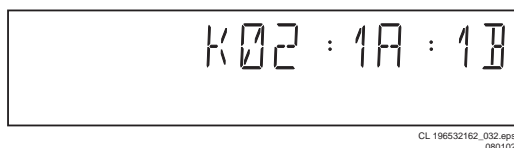


Figure 5-7

The key-codes displayed on the local display will scroll from right to left when the display gets full, the text "K" will remain on display.

KEY ID	KEY
0	PLAY
1	STOP
2	OPEN/CLOSE
3	STANDBY
4	NEXT
5	PREVIOUS
6	PAUSE
7	AUDIO DIRECT
E	SOUND MODE
F	REPEAT

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Figure 5-8

If any keys are detected more than once (due to hardware error), the key-code is displayed twice (or more), with the second digit increased by 1. If the user does not press all keys minimally once (in any order), the DispKeys nucleus will return FALSE and cause an error in the overall result of the player script. The user can leave the keyboard test by pressing the PLAY key on the local display of the DVD player for at least one full second. The result of the keyboard test is shown on local display as follows:

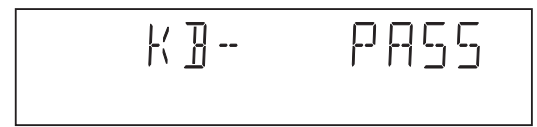


Figure 5-9

Or

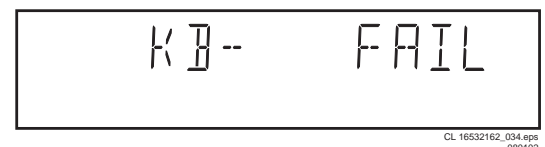


Figure 5-10

Pressing PLAY on the local keyboard again will proceed to the next text.

5.3.4 Remote Control Test

The remote control of the DVD player is tested by nucleus DispRc. The user must press any key on the remote control just once. The codes of the key pressed will be shown on the local display in hexadecimal format. Example:

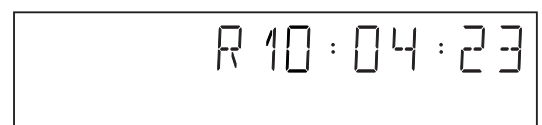


Figure 5-11

In this example 23 is the hexadecimal code of the pressed RC key. The user can leave the remote-control test by pressing PLAY on the local keyboard of the DVD player. The remote control test is successful if a code was received before the user

pressed the PLAY key. Pressing the PLAY key, before pressing a key on the remote control, gives an error in the remote control test (note that the remote control test will also fail if a key on the remote control was pressed but no code was received). The remote control test does not check upon the contents of the received code, that is it will not be checked if the received code matches the key pressed. If desired, the user can manually check this code by using a code-table for the remote control key-codes.

RC Key id	Hexadecimal code
STANDBY	0C
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
0	0
RETURN	83
DISPLAY	EF
DISC MENU	54
SYSTEM MENU	82
CURSOR UP	58
CURSOR DOWN	59
CURSOR LEFT	5A
CURSOR RIGHT	5B
OK	5C
PREVIOUS	21
NEXT	20
STOP	31
PLAY	2C
PAUSE	30
SUBTITLE	4B
ANGLE	85
ZOOM	F7
AUDIO	4E
REPEAT	1D
REPEAT A-B	3B
SHUFFLE	1C
SCAN	2A

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Figure 5-12

After pressing PLAY, the result of the remote control test is displayed on the local display of the DVD player as follows:

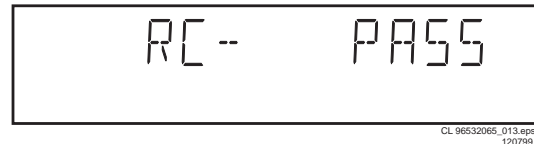


Figure 5-13

Or

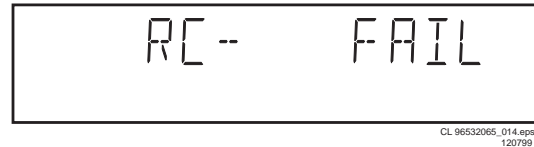


Figure 5-14

Pressing PLAY on the local keyboard again will proceed to the next test.

5.3.5 P50 Loop-Back Test

For the P50 loop-back test, the user must first press a key to decide if the test is to be performed. The display will show the following message:

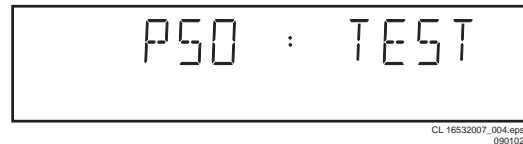


Figure 5-15

If the user presses STOP, the P50 test will be skipped. If the user presses OPEN/CLOSE, the P50 test is performed and the result is displayed as follows:

Test successful:



Figure 5-16

Test fails:



Figure 5-17

Press the PLAY key to continue to the next text

5.4 Mono PCB Digital Part

5.4.1 Picture Test

The picture test is performed by putting a predefined picture (colour bar) on the display (nucleus VideoColDencOn), and asking the user for confirmation.
The display will show the following message:

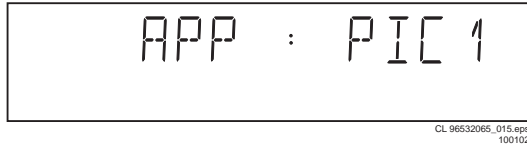


Figure 5-18

By pressing OPEN/CLOSE the user confirms the test, pressing STOP will indicate the picture was invisible or incorrect. Pressing PLAY will proceed to the next test. If the user presses PLAY without pressing OPEN/CLOSE or STOP first, the result of this test will be TRUE (picture ok).

Note: The colour bar must be simultaneously available on the CVBS, YC, and RGB (or YUV) outputs available. On the SCART only the CVBS and RGB signals will be available.

5.4.2 Sound 1 & SCART DVD Test

The first soundtest is performed by starting a pink noise sound that needs confirmation from the user (nucleus AudioPinkNoiseOn).

The display will show the following message:

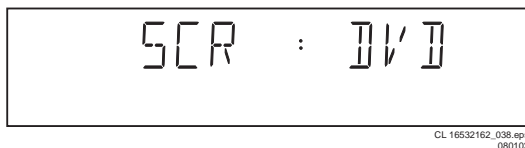


Figure 5-19

On the TV screen a colour bar (generated by nucleus VideoColDencOn) is visible and the internally generated pinknoise is audible.

By pressing the PLAY key, the user confirms the test. Pressing the STOP key will indicate the sound was inaudible or incorrect.

Note: Only for double scart models, SCART loop-through will be simultaneously active during this test. SCART loop-through will be measured with the aid of an external video source.

By pressing the PLAY key, there will be switched over to the external source. This must become now visible on the TV screen (using the SCART).

The local display will show the following message:

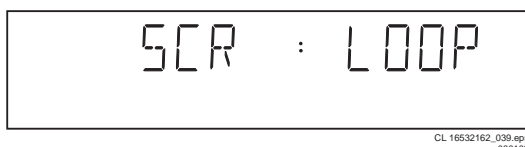


Figure 5-20

The internally generated colour bar is still available on the CVBS and Y/C outputs. And the pinknoise-signal is still available on the cinch audio outputs. By pressing the OPEN/CLOSE button, the internal generated colour bar becomes visual again.

The test can be left by pressing the PLAY key for more than one second.

5.4.3 Sound 2 Test

The second soundtest is performed by producing a sine sound (nucleus AudioSineOn). The signal can be stopped by pressing the STOP key.

The display will show the following message:

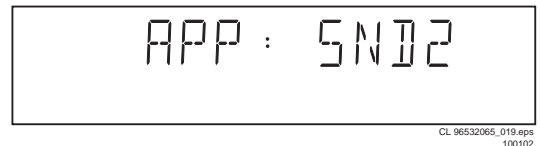


Figure 5-21

After the audio signal has been stopped, by pressing OPEN/CLOSE, the user confirms the test. Pressing STOP will indicate that something went wrong. Pressing PLAY will proceed to the next. If the user presses PLAY without pressing OPEN/CLOSE or STOP first, the result of this test will be TRUE (sound ok).

5.5 Basic Engine

5.5.1 Version Number

In the basic engine tests, the version number of the Basic Engine will be shown first, as the following example:

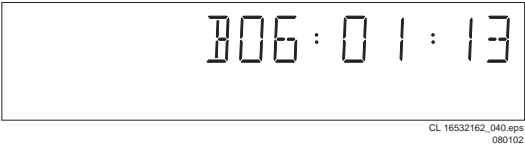


Figure 5-22

By pressing the PLAY key, the Basic Engine tests are started.

5.5.2 Tray Test

First, the tray is tested. The purpose of this test is also to give the user the opportunity to put a disc in the tray of the DVD player. Some tests on the Basic Engine require that a disc (e.g. DVD MPTD test disc) is present in the player. At the end of the Basic Engine tests this tray test will be repeated solely to enable the user to remove the disc in the tray. The local display will look as follows:

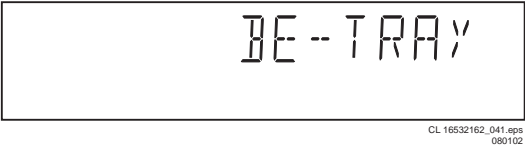


Figure 5-23

By pressing OPEN/CLOSE the user can toggle the position of the tray. Note that this test will not contribute to the test result of the Basic Engine. Pressing PLAY will proceed to the next test. At this point, the tray will be closed automatically by the software if it was open.

5.5.3 Sledge Test (Visual Test)

The second Basic Engine test tests the sledge. The user can move the sledge as many times as desired by using OPEN/CLOSE (nucleus BeSledgeOut) and STOP (nucleus BeSledgeIn). Pressing PLAY on the local keyboard proceeds to the next test. Note that this test will not contribute to the test result of the Basic Engine. The local display will look as follows during the sledge test:

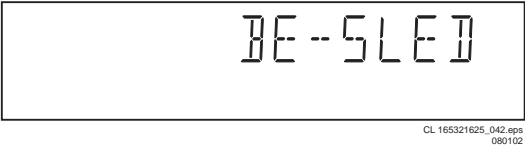


Figure 5-24

5.5.4 Disc Motor Test (Visual Test)

The third Basic Engine test tests the disc motor (nucleus BeDiscMotorOn). The local display looks as follows:

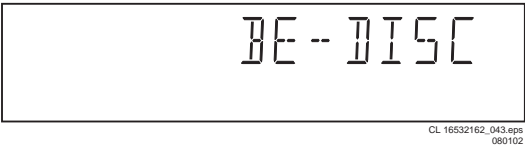


Figure 5-25

By pressing OPEN/CLOSE the user confirms that the disc motor is running. Pressing STOP indicates the disc motor does not work. Pressing PLAY proceeds to the next test, after a reset of the disc motor (nucleus BeDiscMotorOff). If the user presses PLAY before pressing OPEN/CLOSE or STOP, the result of this test will be TRUE (disc motor is running).

5.5.5 Focus Test (Visual Test)

The fourth Basic Engine test tests the focussing. First focussing is turned on by calling nucleus BeFocusOn. The display will look as follows:

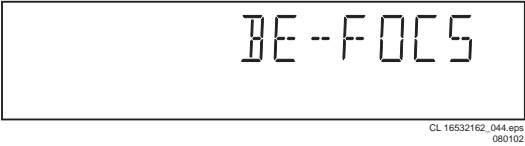


Figure 5-26

By pressing OPEN/CLOSE the user confirms that the focussing was succesful. Pressing STOP indicates a focussing failure. Pressing PLAY proceeds to the next test after a reset of the focussing (nucleus BeFocusOff). If PLAY is pressed before OPEN/CLOSE or STOP, the result of this test will be TRUE (focus successful).

5.5.6 Radial Test (Visual & Listening Test)

The fifth Basic Engine test tests the radial functionality (nucleus BeRadialOn). The local display looks as follows:

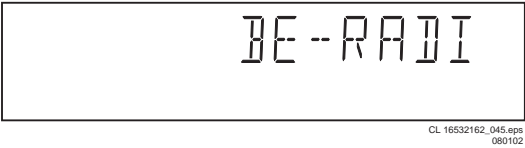


Figure 5-27

By pressing OPEN/CLOSE the user confirms that the radial function works. Pressing STOP indicates the function does not work. Pressing PLAY proceeds to the next test, after a reset of the radial (nucleus BeRadialOff). If the user presses PLAY before pressing OPEN/CLOSE or STOP, the result of this test will be TRUE (radial successful).

5.5.7 Jump Test (Listening Test)

The sixth and last Basic Engine test tests the jumping by calling nuclei BeGroovesIn, BeGroovesMid and BeGroovesOut. During this test, the local display looks as follows:

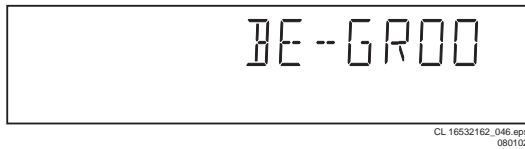


Figure 5-28

The user can switch between the three different types of groove settings by pressing OPEN/CLOSE (forward to next nucleus in the list In-Mid-Out), or STOP (backward in the list In-Mid-Out). This is done in a cyclic manner; note that this test will not contribute to the test result of the Basic Engine. Pressing PLAY proceeds to the next test, after the disc motor has been shut off with a call to nucleus BeDiscMotorOff.

5.5.8 Tray Test

As a last action for the Basic Engine tests, the tray test is repeated. The local display will look as follows:

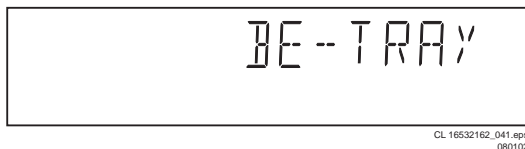


Figure 5-29

This test is meant to give the user the opportunity to remove the disc in the tray. The tray position can be toggled using the OPEN/CLOSE key. The tray will be closed (by the software, if it is open) before proceeding to the next test when the user presses the PLAY key.

5.5.9 Error Log (See Table on Page 25)

Reading the error log and error bits information can be useful to determine any errors that occurred recently during normal operation of the DVD player. Reading the error log is done by nucleus LogReadErr.

The display during the errorlog readout looks as follows :

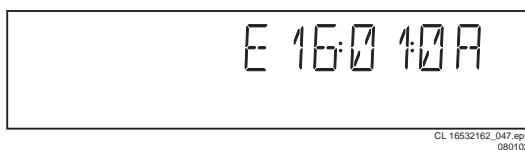


Figure 5-30

Note: Previous versions of the diagnostic software showed a 8-digit error code.

Due to limitations in the number of digits that can be displayed by some front panel displays, the most significant digits will not be shown. This can be done since all the error codes used by this player has set these 2 digits to "00"

By pressing OPEN/CLOSE or STOP the user can move forward or backward (respectively) through the logged error codes. If "0000" is displayed at all positions, the error log is empty. Display of the logged errors is done in a cyclic manner. By pressing PLAY on the local keyboard, the user can proceed to the next test.

5.5.10 Error Bits

Reading the error bits is done by nucleus LogReadBits. The display during the errorbits readout looks as follows:

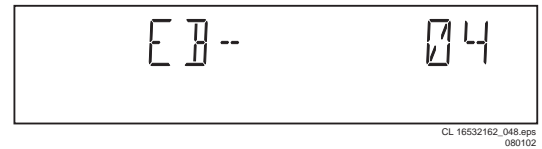


Figure 5-31

Only the identification number (decimal) representing set errorbits will be shown. By pressing OPEN/CLOSE or STOP, the user can move forward or backward (respectively) through the logged errorcodes. If the display only shows "EB-0", no error bits were set. By pressing PLAY the user can continue to the next test.

5.6 Loop Test (See Table Below)

At the start of the loop test, the local display of the DVD player will show the interactive player test result readout in the following display:

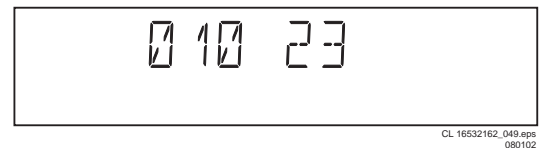


Figure 5-32

The left side of the display contains a 3-digit code, which can have a value between 000 and 111. These values indicate the faulty modules and are to be interpreted as follows:

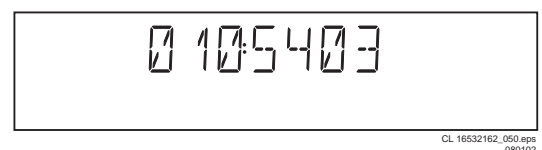
Displayed Value	Indication for each module		
	Basic Engine	Mono PCB	Display PCB
000	ok	ok	ok
001	ok	ok	faulty
010	ok	faulty	ok
011	ok	faulty	faulty
100	faulty	ok	ok
101	faulty	ok	faulty
110	faulty	faulty	ok
111	faulty	faulty	faulty

CL 96532065_031.eps
120799

Figure 5-33

The loop test will perform the same nuclei as the dealer test, but it will loop through the list of nuclei indefinitely. The display of the DVD player will display not only the three digits indicating correct/faulty modules and the last found error code (as mentioned, faults are detected as far as they can be within the scope of the diagnostic software), but also a loop counter indicating how many times the loop has been gone through. If an error was detected, the display will remain as in figure 5-34 until the user presses the PLAY key and then it will continue to the next loop.

Example:



CL 16532162_050.eps
080102

Figure 5-34

The 2-digit number (23) on the right of figure 5-32 indicates the number of times the loop test has been performed.

After one loop cycle: Display the 3-digit module bits together with the last error code which occurred in the loop test. The 4 digits at the right side of the display (fig. 5-34) show the last error that was found during the loop test. The leftmost two digits (54) of this code indicates which nucleus resulted in a fault. The rightmost two digits (03) refer to the faultcode within that nucleus. For further explanation of this error code, refer to chapter 5.8 (Nuclei Error Codes).

5.6.1 Errorlog

Explanation:

The application errors will be logged in the NVRAM. The maximum number of error bytes that will be visible is 16. The first word (4 digits) of the byte is the component identification, the last word is the error code.

The diagnostics software will present a combination of this component identification plus an error code on the local display (and on the attached terminal). The last reported error is shown as < 00000000, the oldest visible error as 00000000 > and the errors in between as < 00000000 >.

The devices that may report errors are the serial controller (UART), the basic engine (BE), the slave processor (SLPH), the SACD Stream Manager (SSM) and the SACD Media Access (SMA). The identification of these components is as follows:

Component name	Component identification
Serial controller (UART)	000A
Engine (BE)	0016
Slave Processor (SLPH)	001A
SACD Stream Manager (SSM)	001C
SACD Media Access (SMA)	002E
Diagnostic software (DS)	Dxxx

The tables in the next chapters list the error code and corresponding problem. The column 'Explanation' holds a more elaborate description and the most likely reason for the error.

Some Examples:

002E0000 (SMA reported a timeout error)

0016010A (Engine could not fully close or open the tray)

D0010001 (Flash checksum failed).

For further explanation of DS errors, see description of nuclei error codes in paragraph 5.8.

UART Error Codes

Error Number	Error name	Explanation
0000	BUF_OVE RFLOW	To many characters were offered in too little time. Reason: system was too busy doing other jobs.
0001	COMMUNI CATION	Usually a protocol error. Reason: bad connection between engine and processor.
0002	TIME OUT	

BE Errors

Error Number	Error name	Explanation
0101	S2B_ILL_CO MMAND	Parameter(s) not valid for this command. Reason: some communication problem between UART and engine.
0102	S2B_ILL_PAR AM	Command not allowed in this state or unknown. Reason: see S2B_ILL_COMMAND error

Error Number	Error name	Explanation
0103	S2B_SLEDGE	Sledge could not be moved to home position.
0104	S2B_FOCUS	Focus failure
0105	S2B_MOTOR	Motor could not reach speed within timeout
0106	S2B_RADIAL	Servo didn't get on track after several retries.
0107	S2B_PLL_LO CK	PLL could not lock in Accessing or Tracking state
0108	SBC_HEADE R_TO	Header timeout
0109	S2B_SBC_NO T_FOUND	Requested subcode item could not be found.
010A	S2B_TRAY	Tray could not be opened or closed completely.
010B	S2B_TOC_RE AD	TOC could not be read within timeout period.
010C	S2B_JUMP	Requested seek could not be performed.
010D	S2B_NON_EX IST_SES	Attempt to access a non-existing session.
010E	S2B_NON_EX IST_BCA	Caller tries to acces a non-existing BCA area
010F	Speed setting	A wrong or inappropriate speed value has been set
0116	NO_DISC	No disc selected
011A	TRAY_INIT	After reset, initialized tray
011B	NO TOC INFO	No TOC information in lead-in area or erase TOC found
01F0	S2B_OVERR UN	Too many bytes received over S2B Reason: see S2B_ILL_COMMAND error
01F1	S2B_COMM_ TO	Not enough bytes are received over S2B Reason: see S2B_ILL_COMMAND error
01F2	S2B_PARITY	Byte received with parity error. Reason: see S2B_ILL_COMMAND error
01F3	S2B_ILL_PHA SE	CMD IDC is not valid, transmission out of sync. Reason: see S2B_ILL_COMMAND error
01F4	S2B_ILL_NR_ OF_BYTES	Byte count has an illegal value. Reason: see S2B_ILL_COMMAND error

SLPH Error Codes

Error Number	Error name	Explanation
0000	COMMUNICA TION	Error in I2C communication. Reason: bad connection between slave processor and main processor.

SSM Error Codes

Error Code	Error name	Explanation
0006	SP_SYNCER ROR	System cannot get synchronised with sectors coming from disc. Reason: Usually a damaged disc or the player was dropped/pushed during operation. If not, the engine is malfunctioning.
0007	SP_EDCERR OR	Data coming from disc is damaged. Reason: see SP_SYNCERROR

Error Code	Error name	Explanation
0008	SP_CONTINUITYERROR	Sequence of sectors coming from disc is incorrect. Reason: see SP_SYNCERROR
0009	DMX_CONTINUITYERROR	Sequence of sectors is incorrect. Reason: problem with buffer RAM
000A	LLD_ERROR	An illegal audio format was offered to the decoder. Reason: unknown audio type on disc or problem with buffer RAM
000B	BCU_ERROR	Internal problem in Furore chip

SMA Error Codes

Error Number	Error name	Explanation
0000	SMA_TIMEOUTERROR	Data coming from disc not in time. Reason: damaged disc or engine problem.

5.6.2 Reprogramming of New Mono Boards.**Caution**

This information is confidential and may not be distributed. Only a qualified service person should reprogram the mono board.

After reset of NV-memory or repair of the mono board, all the customer settings and also the region code will be lost.

Reprogramming of the mono board will put the player back in the state in which it has left the factory, i.e. with the default settings and the allowed region code.

Reprogramming is limited to 25 times

When the counter reaches 25, reprogramming is not possible anymore

Reprogramming will be done by way of the remote control.

Put the player in stop mode, no disc loaded.

Press the following keys on the remote control:

<PLAY> followed by numerical keys <1> <5> <9>

The display shows: “-----”

Press now successively the following keys :

for DVD963SA /001 : <2><2><2> <0><1><2><0><0>

for DVD963SA 171 : <1><1><1> <0><1><0><0><0>

for DVD963SA 691 : <3><2><3> <0><1><0><0><0>

Press <PLAY> again.

The TV screen will become BLUE during a short time to confirm that the mono board has been reprogrammed.

Figure 5-35 Reprogramming code

5.6.3 Trade Mode

When the player is in Trade Mode, the player cannot be controlled by means of the front key buttons, but only by means of the remote control.

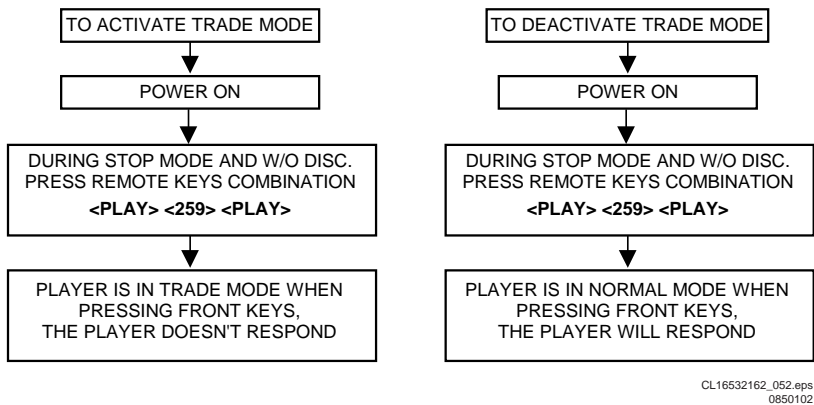


Figure 5-36

Note: To activate and deactivate the Trade Mode with the disc in the player, the procedure is similar to above, except that the remote control keys combination is pressed at the instant when the local display is flashing “READING”

5.7 Menu and Command Mode Interface

5.7.1 Layout of Results Diagnostic Nuclei

Results returned from a Diagnostic Nucleus will be displayed in the following layout:

```
< number >< string > [ok | ER]
```

< number >: is a 4-digit decimal number padded with leading zeros if its value is less than 4 digits. The first 2 digits identify the generating nucleus (or group of nuclei) while the latter 2 digits indicate the error number.

< string >: is a text string containing information about the result of the Diagnostic Nucleus.

< number > and < string > are defined in [SSD_DN] in the output sections of each Nucleus.

Examples:

- 0001Unknown command ER @
- 3100OK @
- 0901Data line X is not connected to the DRAM ER@

5.7.2 Command Mode Interface

Set-up Physical Interface Components

Hardware required:

- Service PC
- one free COM port on the Service PC
- special cable to connect DVD player to Service PC

The service PC must have a terminal emulation program (e.g. OS2 WarpTerminal or Procomm) installed and must have a free COM port (e.g. COM1). Activate the terminal emulation program and check that the port settings for the free COM port are: 19200 bps, 8 data bits, no parity, 1 stop bit and no flow control. The free COM port must be connected via a special cable to the RS232 port of the DVD player. This special cable will also connect the test pin, which is available on the connector, to ground (i.e. activate test pin).

Code number of PC interface cable: 3122 785 90017

Activation

Switch the player on and the following text will appear on the screen of the terminal (program):

```

DVDv4 Diagnostic Software version 5.03

(M)enu, (C)ommand or (S)2B interface ? [M]:@ C <enter>

SDRAM Interconnection test passed
Basic SDRAM test passed
Slave Processor: SLAVE2

DD:>
  
```

Figure 5-37

The first line indicates that the Diagnostic software has been activated and contains the version number. The second line lets the user choose the interface format. Enter ‘C’ to select Command Mode and the next three lines are the successful result of the two subsequent basic tests (nuclei 2, 4 and the detection of the display type used by the panel respectively). If not all these messages appear on the terminal screen, then the related nucleus found an error. The last line is the prompt (“DD>”). The diagnostic software is now ready to receive commands.

Command Overview of Nuclei

The following table gives an overview of all available nuclei. The first column contains an identification number, the second contains the name of a nucleus and the last column indicate the description of the nucleus.

Note: User confirmation is necessary during front panel tests

Table 5-1 Basic diagnostic nuclei

Ref. #	Reference Name	Remark
1	BasicSpAcc	Serial port Access test/ initialization
2a	BasicInterconDram	Data and address bus Interconnection (only for development)
2b	BasicInterconSdram	Data and address bus interconnection
3	BasicDramWrR	DRAM Write Read (only for development)
4	BasicSdramWrR	SDRAM Write Read

Table 5-2 Processor and peripherals

Ref. #	Reference Name	Remark
6	PapChksFl	Checksum FLASH
7a	PapUclkAcIkCdda	uClock A_CLK in CD-DA mode
7b	PapUclkAcIkDvd	uClock A_CLK in DVD mode
7c	PapUclkAcIkDvd96	uClock A_CLK in DVD (96kHz) mode
10	PapFlashWrAcc	FLASH Write Access (only for development)
11	PapI2cNvram	I2C NVRAM access
12	PapI2cDisp	I2C Display PWB
13	PapS2bEcho	S2B Echo
14	PapS2bPass	S2B Pass-through
15	PapNvramWrR	NVRAM Write Read
62	PapChksSum	Show checksums stored in flash

Table 5-3 Components

Ref. #	Reference Name	Remark
16	CompSdramWrR	SDRAM Write Read

Table 5-4 Audio

Ref. #	Reference Name	Remark
19a	AudioMuteOn	Audio Mute On
19b	AudioMuteOff	Audio Mute Off
20a	AudioPinkNoiseOn	Audio Pinknoise On
20b	AudioPinkNoiseOff	Audio Pinknoise (or beep tone) Off
20c	AudioBeepToneOn	Audio Beep Tone On
21a	AudioSineOn	Audio Sine signal On/Off
21b	AudioSineBurst	Audio Sine signal Burst
56a	AudioLfePortHigh	Set the LFE_SEL port to HIGH
56b	AudioLfePortLow	Set the LFE_SEL port to LOW
66a	DAC_I2C	Resets DAC and check I2C communication with DAC
66b	DAC_I2CEnable	Enable I2C communication to AV board
66c	DAC_I2CDisable	Disable I2C communication to AV board
67a	DAC_ClockInternal	Uses internal clock from monoboard for DAC (256fs)
67b	DAC_ClockExternal	Uses external clock for DAC (384fs)
68a	DAC_AudioPreMuteOn	Enable Audio Pre-mute pin
68b	DAC_AudioPreMuteOff	Disable Audio Pre-mute pin
69a	DAC_CenterOn	Enable Center on pin
69b	DAC_CenterOff	Disable Center on pin
79	DAC_Reset	Resets DAC
80a	DAC_ModeCDDA	Sets DAC to CDDA mode
80b	DAC_ModeDVD48	Sets DAC to DVD mode (48kHz)
80c	DAC_ModeDVD96	Sets DAC to DVD mode (96kHz)

Ref. #	Reference Name	Remark
80d	DAC_ModeDSD	Sets DAC to DSD mode
81a	DAC_LowPowerStandby On	Enable Low Power Standby
81b	DAC_LowPowerStandby Off	Disable Low Power Standby
82a	DAC_UpsamplingFreq192k	Sets Upsampling frequency to 192kHz
82b	DAC_UpsamplingFreq96k	Sets Upsampling frequency to 96kHz
82c	DAC_UpsamplingOn	Enable upsampling
82d	DAC_UpsamplingOff	Disable upsampling

Table 5-5 Video

Ref. #	Reference Name	Remark
17a	VidPortOutAA	Output the value 0XAA at the Digital Video Interface Port
17b	VidPortOut55	Output the value 0X55 at the Digital Video Interface Port
23a	VideoColDencOnPAL	Colourbar (PAL) DENC On
23b	VideoColDencOff	Colourbar DENC Off
23c	VideoColDencOnNTSC	Colourbar (NTSC) DENC On
24a	VideoProgMPEGon	Progressive - DigitalVideo Colour Bar ON
24b	VideoYuvMPEGon	Enhanced YUV-DigitalVideo Colour Bar
25a	VideoScartLo	Scart Low
25b	VideoScartMi	Scart Medium
25c	VideoScartHi	Scart High
54	VideoScartSwComm	Scart Switch communication
55a	VideoScartSwDvd	Scart Switch Dvd
55b	VideoScartSwPass	Scart Switch Pass-through
57a	VideoScartPinLo	PIO-pins as used in 2A for Scart-switching
57b	VideoScartPinMi	PIO-pins as used in 2A for Scart-switching
57c	VideoScartPinHi	PIO-pins as used in 2A for Scart-switching
61a	VideoColOutRGB (ST5508)	Output RGB from ST5508
61b	VideoColOutYUV (ST5508)	Output YUV from ST5508

Table 5-6 Display (slave processor)

Ref. #	Reference Name	Remark
26	DispVer	Version number
27	DispKeyb	Keyboard
28	DispRc	Remote Control
29	DispLed	LEDs
30a	DispDisplay	VFT Display test
30b	DispLCDDisplay	LCD Display test
30c	DispLCDBkLight	LCD Backlight test
60	DispP50	P50 loopback test

Table 5-7 Log (Error logging in Nvram)

Ref. #	Reference Name	Remark
31	LogReadErr	Read last Errors
32	LogReadBits	Read errors Bits
33	LogReset	Reset

Table 5-8 Miscellaneous

Ref. #	Reference Name	Remark
34	MiscReadConfig	Read Configuration area from NVRAM
35	MiscNvramReset	NVRAM Reset
36	MiscNvramMod	Modify NVRAM contents
46	MiscApplVer	Read version of application software
47a	MiscTrayOpenNr	Read the number of times the tray opened
47b	MiscPowerOnTime	Read the total time the player's power has been on
47c	MiscPlayTimeCddaVcd	Read the Playtime of CDDA and VCD discs
47d	MiscPlayTimeDvd	Read the Playtime of DVD discs

Table 5-9 Basic engine

Ref. #	Reference Name	Remark
37	BeVer	Version number
38a	BeFocusOn	Focus On
38b	BeFocusOff	Focus Off
39a	BeDiscmotorOn	Discmotor On
39b	BeDiscmotorOff	Discmotor Off
40a	BeRadialOn	Radial control On
40b	BeRadialOff	Radial control Off
41a	BeSledgeIn	Sledge Inwards
41b	BeSledgeOut	Sledge Outwards
42a	BeGroovesIn	jump Grooves to Inside
42b	BeGroovesMid	jump Grooves to Middle
42c	BeGroovesOut	jump Grooves to Outside
43a	BeTrayIn	Tray In
43b	BeTrayOut	Tray Out
44	BeReset	Reset Basic Engine
58a	LaserCdOn	CD Laser on
58b	LaserCdOff	CD Laser off
58c	LaserDvdOn	DVD Laser on
58d	LaserDvdOff	DVD Laser off
70	BedReadFlashID	Read flash memory manufacturer and device ID
71	BedCalcRomChecksum	Calculate ROM checksum
72	BedScratchTest	Test scratch detection circuit

Table 5-10 Furore IC

Ref. #	Reference Name	Remark
63	Furore_SdramWrR	Furore SDRAM Write Read test
64	Furore_SdramWrR Fast	Furore SDRAM interconnection test
65	Furore_Id	Furore version ID check
83	Furore_Reset	Furore reset

Ref. #	Reference Name	Remark
84a	Furore_High	Sets Furore output pins DSD_PCM0-9 to high
84b	Furore_Low	Sets Furore output pins DSD_PCM0-9 to low

Table 5-11 Karaoke (not available)

Ref. #	Reference Name	Remark
48a	KaraokeModeOff	Switch Karaoke mode off
48b	KaraokeModeOn	Switch Karaoke mode on
49	KaraokeMicInput	Check path from the microphone input to audio output
50a	KaraokeKeyOn	Set Karaoke Key to the maximum level (1200 cent)
50b	KaraokeKeyOff	Set Karaoke Key to flat octave (0 cent)
51a	KaraokeEchoOn	Set Echo Control function on
51b	KaraokeEchoOff	Set Echo Control function off

5.7.3 Menu Mode Interface

Activation

Switch the player on and the following text will appear on the screen of the terminal (program):

```
DVDv4 Diagnostic Software version 5.03

(M)enu, (C)ommand or (S)2B interface ? [M]:@ <enter>
SDRAM Interconnection test passed
Basic SDRAM test passed
Slave Processor: SLAVE2

Press ENTER to go to main menu
CC: > <enter>

MAIN MENU
1. Audio ...
2. Video ...
3. Front Panel ...
4. Basic Engine ...
5. Processor Peripherals ...
6. Error Log ...
7. Furore ...
8. Miscellaneous ...

Select > CL 26532105_059.eps
020902
```

Figure 5-38 Screen menu

The first line indicates that the Diagnostic software has been activated and contains the version number. The next lines are the successful result of the SDRAM interconnection test and the basic SDRAM test. The last line allows the user to choose between the four possible interface forms. If pressing M has made a choice for Menu Interface, the Main Menu will appear.

Layout of Menu and Submenu

The following menu layout will appear after starting up the DVD player in menu mode. The symbol “- - -” indicates that the current menu choice will invoke the display of a submenu. The number between [] indicates the nucleus number. These numbers will not be shown on the screen.

Menus

MAIN MENU

- 1 Audio...
- 2 Video...
- 3 Front Panel...

- 4 Basic Engine...
- 5 Processor Peripherals...
- 6 Error Log...
- 7 Furore...
- 8 Miscellaneous...

First Level Submenus**MAIN > AUDIO MENU**

- 1 Mute...
- 2 Pink Noise...
- 3 Sine Wave...
- 4 Digital Ports...
- 5 Ext. DAC Board...

MAIN > VIDEO MENU

- 1 Colourbar...
- 2 Scart...
- 3 Digital Port...

MAIN > FRONT PANEL MENU

- 1 Slave Processor...
- 2 VFT Display [30a]
- 3 LCD Display [30b]
- 4 LCD BkLight [30c]
- 5 Keyboard [27]
- 6 LEDs [29]
- 7 Remote Control [28]
- 8 P50 Check [60]

MAIN > BASIC ENGINE MENU

- 1 Reset [44]
- 2 Version [37]
- 3 S2B...
- 4 Loader Mechanism...
- 5 Special Diagnostics...

MAIN > PROCESSOR PERIPHERALS MENU

- 1 Clock...
- 2 Flash...
- 3 NVRAM...
- 4 SDRAM Write/Read [16]

MAIN > ERROR LOG MENU

- 1 Read Last Errors [31]
- 2 Read Error Bits [32]
- 3 Reset Error Log [33]

MAIN > FURORE MENU

- 1 SDRAM Write/Read [63]
- 2 SDRAM Write/Read [64]
- 3 Chip Revision ID [65]
- 4 Set DSD_PCM Output High [84a]
- 5 Set DSD_PCM Output Low [84b]
- 6 Reset Furore IC [83]

MAIN > MISCELLANEOUS MENU

- 1 Statistics Info...
- 2 Read DVD Application version[46]

Second Level Submenus**MAIN > AUDIO > MUTE MENU**

- 1 Mute On [19a]
- 2 Mute Off [19b]
- 3 Mute Front LR On (963 only) [19c]
- 4 Mute Front LR Off (963 only) [19d]

MAIN > AUDIO > PINK NOISE MENU

- 1 Pink Noise On [20a]
- 2 Pink Noise / Beep Tone Off [20b]
- 3 Beep Tone On [20c]

MAIN > AUDIO > SINE WAVE MENU

- 1 Audio Sine On [21a]
- 2 Audio Burst On [21b]

MAIN > AUDIO > DIGITAL PORTS MENU

- 1 No tets available

MAIN > AUDIO > EXT DAC BOARD MENU

- 1. DAC Reset [79]
- 2. I2C Test...
- 3. Clock...
- 4. Audio...
- 5. Low Power Standby...
- 6. DAC Mode...

MAIN > VIDEO > COLOURBAR MENU

- 1 Colourbar DENC On (PAL) [23a]
- 2 Colourbar DENC On (NTSC) [23c]
- 3 Colourbar DENC/MPEG Off [23b]
- 4 ProgressiveScan MPEG On [24a]
- 5 Enhanced YUV MPEG On [24b]
- 6 Set Video Out To RGB [61a]
- 7 Set Video Out To YUV [61b]

MAIN > VIDEO > SCART MENU

- 1 I2C Scart IC Check [54]
- 2 Scart To DVD [55a]
- 3 Scart Pass Through [55b]
- 4 Scart Pin 8 Low (0 to 2)V [25a]
- 5 Scart Pin 8 Mid (4.5 to 7)V [25b]
- 6 Scart Pin 8 Hi(9.5 to 12)V [25c]

MAIN > VIDEO > DIGITAL PORT MENU

- 1 Video Port Out 0xAA [17a]
- 2 Video Port Out 0x55 [17b]
- 3 P-scan board IO exp [17c]

MAIN > FRONT PANEL > SLAVE PROCESSOR MENU

- 1 Bus Comms Check [12]
- 2 S/W Version [26]

MAIN > BASIC ENGINE > S2B MENU

- 1 S2B Echo [13]
- 2 S2B Pass-Through [14]

MAIN > BASIC ENGINE > MECHANISM MENU

- 1 Disc Motor...
- 2 Laser...
- 3 Tray...
- 4 Focus...
- 5 Radial...
- 6 Sledge...
- 7 Grooves...

MAIN > BASIC ENGINE > SPECIAL DIAGNOSTICS MENU

- 1 Read FlashID [70]
- 2 ROM Checksum [71]
- 3 Scratch Circuit [72]

MAIN > PROCESSOR PERIPHERALS > PCM CLOCK MENU

- 1 Ext PCM_CLK In CDDA Mode (11.3MHz) [7a]
- 2 Ext PCM_CLK In DVD Mode (12.3MHz) [7b]
- 3 Ext PCM_CLK In DVD96kHz Mode (24.6MHz) [7c]

MAIN > PROCESSOR PERIPHERALS > FLASH MENU

- 1 Verify FLASH Checksum [6]
- 2 Show FLASH Checksum [62]

MAIN > PROCESSOR PERIPHERALS > NVRAM MENU

- 1 I2C NVRAM Acces [11]
- 2 NVRAM Config [34]
- 3 NVRAM Reset [35]

- 4 NVRAM Modify [36]
- 5 NVRAM Read/Wr Test [15]

MAIN > MISCELLANEOUS > STATISTICS INFO MENU

- 1 Total Nr Of Times Tray Open [47a]
- 2 Total Time Power On [47b]
- 3 Total Play-Time CDDA & VCD [47c]
- 4 Total Play-Time DVD [47d]

Third Level Submenus

MAIN > AUDIO > EXT DAC BOARD > I2C TEST MENU

- 1. I2C Test [66a]
- 2. I2C Enable Pin On [66b]
- 3. I2C Enable Pin Off [66c]

MAIN > AUDIO > EXT DAC BOARD > CLOCK MENU

- 1. Clock Internal [67a]
- 2. Clock External [67b]
- 3. Clock Upsampling 192k (963 only) [82a]
- 4. Clock Upsampling 96k (963 only) [82b]
- 5. Clock Upsampling On (963 only) [82c]
- 6. Clock Upsampling Off (963 only) [82d]

MAIN > AUDIO > EXT DAC BOARD > AUDIO

- 1. Audio Pre-Mute On [68a]
- 2. Audio Pre-Mute Off [68b]
- 3. Audio Center On [69a]
- 4. Audio Center Off [69b]

MAIN > AUDIO > EXT DAC BOARD > LOW POWER STANDBY

- 1. Low Power Standby On [81a]
- 2. Low Power Standby Off [81b]

MAIN > AUDIO > EXT DAC BOARD > DAC MODE MENU

- 1. DAC CDDA Mode [80a]
- 2. DAC DVD48 Mode [80b]
- 3. DAC DVD96 Mode [80c]
- 4. DAC DSD Mode [80d]

MAIN > BASIC ENGINE > MECHANISM > DISC MOTOR MENU

- 1 Disc Motor On [39a]
- 2 Disc Motor Off [39b]

MAIN > BASIC ENGINE > MECHANISM > LASER MENU

- 1 CD Laser On [58a]
- 2 CD Laser Off [58b]
- 3 DVD Laser On [58c]
- 4 DVD Laser Off [58d]

MAIN > BASIC ENGINE > MECHANISM > TRAY MENU

- 1 Tray Open [43b]
- 2 Tray Close [43a]

MAIN > BASIC ENGINE > MECHANISM > FOCUS MENU

- 1 Focus On [38a] (load DVD first)
- 2 Focus Off [38b]

MAIN > BASIC ENGINE > MECHANISM > RADIAL MENU

- 1 Radial Control On [40a] (load DVD first)
- 2 Radial Control Off [40b]

MAIN > BASIC ENGINE > MECHANISM > SLEDGE MENU

- 1 Sledge Inwards [41a]
- 2 Sledge Outwards [41b]

MAIN > BASIC ENGINE > MECHANISM > GROOVES (Uses DVD) MENU

- 1 Jump To Inside Grooves [42a]
- 2 Jump To Middle Grooves [42b]

- 3 Jump To Outside Grooves [42c]

5.8 Nuclei Error Codes

In the following tables the error description of the error codes will be described.

5.8.1 Audio Nuclei

Error code	Error text
1800	Audio Deemp On OK
1820	Audio Deemp Off OK
1880	Audio Deemp Tri OK
1900	Audio mute On OK
1920	Audio mute Off OK
1940	Audio Front LR mute On OK
1960	Audio Front LR mute Off OK
2000	Audio Pinknosie On OK
2001	Audio Pinknoise On DAC fail
2020	Audio Pinknoise Off OK
2040	Audio Beep tone On OK
2041	Audio Beep tone On DAC fail
2100	Audio Sine On OK
2101	Audio Sine On DAC fail
2120	Audio Sine Burst OK
2121	Audio Sine Burst DAC fail
6600	DAC I2C OK
6601	DAC I2C Bus busy
6602	DAC I2C expander
6603	DAC I2C DAC 4362
6620	DAC I2C enable OK
6640	DAC I2C disable OK
6700	DAC Clk internal OK
6701	DAC Clk internal I2C bus busy
6702	DAC Clk internal I2C failed
6720	DAC Clk external OK
6721	DAC Clk external I2Cbus busy
6722	DAC Clk external I2C failed
6800	DAC Audio pre-mute On OK
6801	DAC Audio pre-mute On I2C bus busy
6802	DAC Audio pre-mute On I2C failed
6820	DAC Audio pre-mute Off OK
6821	DAC Audio pre-mute Off I2C bus busy
6822	DAC Audio pre-mute Off I2C failed
6900	DAC Center On OK
6901	DAC Center On I2C bus busy
6902	DAC Center On I2C failed
6920	DAC Center Off OK
6921	DAC Center Off I2C bus busy
6922	DAC Center Off I2C failed
7900	DAC Reset OK
7901	DAC Reset I2C bus busy
7902	DAC Reset I2C expander
8000	DAC Mode CDDA OK
8001	DAC Mode CDDA I2C bus busy
8002	DAC Mode CDDA I2C failed
8020	DAC Mode DVD48 OK
8021	DAC Mode DVD48 I2C bus busy
8022	DAC Mode DVD48 I2C failed
8040	DAC Mode DVD96 OK
8041	DAC Mode DVD96 I2C bus busy
8042	DAC Mode DVD96 I2C failed

Error code	Error text
8060	DAC Mode DSD OK
8061	DAC Mode DSD I2C bus busy
8062	DAC Mode DSD I2C failed
8100	DAC Low Power Standby On OK
8101	DAC Low Power Standby On I2C bus busy
8102	DAC Low Power Standby On I2C failed
8120	DAC Low Power Standby Off OK
8121	DAC Low Power Standby Off I2C bus busy
8122	DAC Low Power Standby Off I2C failed
8200	DAC Upsample 192k OK
8201	DAC Upsample 192k I2C bus busy
8202	DAC Upsample 192k I2C failed
8220	DAC Upsample 96k OK
8221	DAC Upsample 96k I2C bus busy
8222	DAC Upsample 96k I2C failed
8240	DAC UpSample On OK
8241	DAC UpSample On bus busy
8242	DAC UpSample On I2C failed
8260	DAC UpSample Off OK
8261	DAC UpSample Off bus busy
8262	DAC UpSample Off I2C failed

5.8.2 Basic Engine Nuclei

Error code	Error text
3900	Test successful
3901	"Parity error from Basic Engine to Serial"
3902	"Unexpected response from Basic Engine"
3903	"Communication time-out error"
3904	"Basic Engine returned error number 0xXX"
3921	"Parity error from Basic Engine to Serial"
3922	"Unexpected response from Basic Engine"
3923	"Communication time-out error"
3924	"Basic Engine returned error number 0xXX"
3800	Test successful
3801	"Parity error from Basic Engine to Serial"
3802	"Unexpected response from Basic Engine"
3803	"Communication time-out error"
3804	"Basic Engine returned error number 0xXX"
3805	"Focus loop could not be closed"
3820	Test successful
3821	"Parity error from Basic Engine to Serial"
3822	"Unexpected response from Basic Engine"
3823	"Communication time-out error"
3824	"Basic Engine returned error number 0xXX"
4200	Test successful
4201	"Parity error from Basic Engine to Serial"
4202	"Unexpected response from Basic Engine"
4203	"Communication time-out error"
4204	"Basic Engine returned error number 0xXX"
4205	"Sledge could not be moved to home position"
4206	"Focus loop could not be closed"
4207	"Motor not on speed within time-out"
4208	"Radial loop could not be closed"
4209	"PLL could not lock in accessing or tracking state"

Error code	Error text
4210	"Subcode or sector information could not be read"
4211	"Requested subcode item could not be found"
4212	"TOC could not be read in time"
4213	"Seek could not be performed"
4220	Test successful
4221	"Parity error from Basic Engine to Serial"
4222	"Unexpected response from Basic Engine"
4223	"Communication time-out error"
4224	"Basic Engine returned error number 0xXX"
4225	"Sledge could not be moved to home position"
4226	"Focus loop could not be closed"
4227	"Motor not on speed within time-out"
4228	"Radial loop could not be closed"
4229	"PLL could not lock in accessing or tracking state"
4230	"Subcode or sector information could not be read"
4231	"Requested subcode item could not be found"
4232	"TOC could not be read in time"
4233	"Seek could not be performed"
4240	Test successful
4241	"Parity error from Basic Engine to Serial"
4242	"Unexpected response from Basic Engine"
4243	"Communication time-out error"
4244	"Basic Engine returned error number 0xXX"
4245	"Sledge could not be moved to home position"
4246	"Focus loop could not be closed"
4247	"Motor not on speed within time-out"
4248	"Radial loop could not be closed"
4249	"PLL could not lock in accessing or tracking state"
4250	"Subcode or sector information could not be read"
4251	"Requested subcode item could not be found"
4252	"TOC could not be read in time"
4253	"Seek could not be performed"
4000	Test successful
4001	"Parity error from Basic Engine to Serial"
4002	"Unexpected response from Basic Engine"
4003	"Communication time-out error"
4004	"Basic Engine returned error number 0xXX"
4005	"Radial loop could not be closed"
4020	Test successful
4021	"Parity error from Basic Engine to Serial"
4022	"Unexpected response from Basic Engine"
4023	"Communication time-out error"
4024	"Basic Engine returned error number 0xXX"
4400	Test successful
4401	Test successful
4100	Test successful
4101	"Parity error from Basic Engine to Serial"
4102	"Unexpected response from Basic Engine"
4103	"Communication time-out error"

Error code	Error text
4104	"Basic Engine returned error number XX"
4120	Test successful
4121	"Parity error from Basic Engine to Serial"
4122	"Unexpected response from Basic Engine"
4123	"Communication time-out error"
4124	"Basic Engine returned error number XX"
4300	Test successful
4301	"Parity error from Basic Engine to Serial"
4302	"Unexpected response from Basic Engine"
4303	"Communication time-out error"
4304	"Basic Engine returned error number 0xXX"
4320	Test successful
4321	"Parity error from Basic Engine to Serial"
4322	"Unexpected response from Basic Engine"
4323	"Communication time-out error"
4324	"Basic Engine returned error number 0xXX"
3700	"Version: X.Y.Z"
3701	"Parity error from Basic Engine to Serial"
3702	"Unexpected response from Basic Engine"
3703	"Communication time-out error"
3704	"Basic Engine returned error number 0xXX"
5800	Test successful
5820	Test successful
5840	Test successful
5860	Test successful
5801	"Unexpected response from Basic Engine"
7000	"Manuf. ID: <XX>" "Device ID: <YY>"
7001	"Comm Test Failed"
7002	"Load Cmd Failed"
7003	"Load Dat Failed"
7004	"Run Cmd Failed"
7100	"ROM Checksum: XXXX"
7101	"Comm Test Failed"
7102	"Load Cmd Failed"
7103	"Load Dat Failed"
7104	"Run Cmd Failed"
7201	"Comm Test Failed"
7200	"Test successful"
7202	"Load Cmd Failed"
7203	"Load Dat Failed"
7204	"Run Cmd Failed"
7205	"Scratch circuit not OK"

5.8.3 Display PWB Nuclei

Error code	Error text
3000	"Test successful"
3001	"Disp not responding"
3002	"Disp key no response"
3003	"One or more patterns not correct"
3004	"Disp type invalid"
3020	"Test successful"
3021	"Disp not responding"
3022	"Disp key no response"
3023	"One or more patterns not correct"
3040	"Test successful"

Error code	Error text
3041	"Disp not responding"
3042	"Disp key no response"
3043	"One or more patterns not correct"
2700	"Model name in wich the test is running"
2701	"Disp key no response"
2702	"Disp not responding"
2707	"Stop key not pressed"
2708	"Pause key not pressed"
2709	"Play key not pressed"
2710	"Open/close key not pressed"
2713	"Previous key not pressed"
2714	"Next key not pressed"
2715	"More than one key not pressed"
2716	"Audio key not pressed"
2900	"Test successful"
2901	"Slave not responding"
2902	"Slave keyboard not responding"
2903	"Standby led not working"
2800	"Test successful"
2801	"Slave display controller not responding"
2802	"Slave keyboard not responding"
2803	"No key press received from remote control"
2600	"The ROM version of the slave processor = 0xXX, and the internal ID = 0xYY"
2601	"I2c bus busy"
2602	"I2c bus not working"
6000	P50 test
6001	"No readback on P50"
6002	"Disp not responding"
6003	"P50 readback error"

5.8.4 Processor & Peripherals Nuclei

Error code	Error text
700	Test successful
720	Test successful
740	Test successful
600	"All checksums are correct"
601	"Following checksum is faulty: BootCode1 Checksum is 0xY2 and is not correct (must be 0xZ2)"
601	"This test is not available when stand-alone compiled"
6200	"Checksums = 0xA1, 0xB1, 0xC1, 0xD1"
6201	"This test is not available when stand-alone compiled"
1000	Test successful
1001	Test successful
1020	Test successful
1021	Test successful
1100	Test successful

Error code	Error text
1104	"NVRAM reply time-out"
1200	Test successful
1202	"Slave bus not working"
1203	"Slave controller not responding"
1204	"Slave response is not correct"
5900	Test successful
5901	"I2c bus busy"
5902	"I2c bus not working"
5904	"DTS chip response not correct"
1300	Test successful
1301	"Parity error from basic engine to serial"
1302	"Parity error from serial to basic engine"
1303	"No communication between serial and basic engine"
1304	"Communication time-out error"
1600	Test successful
1601	"The DVD SDRAM is faulty"

5.8.5 Log Nuclei

Error code	Error text
3100	"Show error log"
3101	"Error log is invalid"
3102	"Error log could not be read from NVRAM"
3103	"I2C bus busy before start"
3200	"Show error bit"
3201	"Error log is invalid"
3202	"I2C bus busy before start"
3203	"Error log could not be read from NVRAM"
3300	"Error log is cleared"
3301	"Error log could not be cleared"
3302	"I2C bus busy before start"

5.8.6 Miscellaneous Nuclei

Error code	Error text
3400	Test successful
3401	"The configuration data could not be read from NVRAM"
3402	"I2C bus busy before start"
3500	"NVRAM is cleared"
3501	"The NVRAM could not be reset."
3502	"I2C bus busy before start"
3600	"NVRAM contents updated."
3601	"NVRAM contents and configuration checksum updated."
3602	"NVRAM contents could not be updated."
3603	"I2C bus busy before start"
3604	"NVRAM contents could not be read"
3605	"NVRAM not accessible."
3606	"NVRAM checksum could not be updated."

Error code	Error text
1500	Test successful
1502	"NVRAM access time-out"
1504	"NVRAM fails"
5400	Test successful
5401	"I2c bus busy"
5402	"I2c bus not working"
5403	"Scart switch controller not responding"
5404	"Scart switch controller response not correct"
5500	Test successful
5501	"I2c bus busy"
5502	"I2c bus not working"
5520	Test successful
5521	"I2c bus busy"
5522	"I2c bus not working"
5523	"Scart switch controller not responding"
5200	Test successful
5201	"I2c bus busy"
5202	"I2c bus not working"
5300	Test successful
5301	"I2c bus busy"
5302	"I2c bus not working"
5320	Test successful
5321	"I2c bus busy"
5322	"I2c bus not working"
4700	"Number of times Tray went Open : XX"
4701	The total number of times tray went open could not be read from NVRAM.
4702	I2C bus busy before start
4720	"Total Power On time (minutes) : XX"
4721	The total power-on time could not be read from NVRAM.
4722	I2C bus busy before start
4740	"Total CDDA & VCD disks Play-time (minutes) : XX"
4741	The playtime of CDDA & VCD disks could not be read from NVRAM.
4742	I2C bus busy before start
4760	"Total DVD disks Play-time (minutes) : XX"
4761	The playtime of DVD disks could not be read from NVRAM.
4762	I2C bus busy before start
4600	"Version of Application Software : XX"
4601	"The application version could not be read from NVRAM."
4602	"I2C bus busy before start"

5.8.7 Video Nuclei

Error code	Error text
1700	Video digital port OK
1701	Video digital port failed
1720	Pscan IO exp OK
1721	Pscan IO exp com failed
2300	Video colourbar On OK

Error code	Error text
2301	Color Denc On transmit error
2302	I2C Color Denc On bus busy
2320	Video colourbar Off OK
2340	Video colourbar On NTSC OK
2400	ADV7300 com OK
2401	I2C ADV7300 com bus busy
2402	ADV7300 com transit error
2403	No ack from ADV7300 com
2404	ADV7300 com response error
2420	FLI2200 com OK
2421	I2C FLI2200 com bus busy
2422	FLI2200 com transmit error
2423	No ack from FLI2200 com
2424	FLI2200 com response error
2460	Proscan com OK
2440	ADV7190 com OK
2441	I2C ADV7190 com bus busy
2442	ADV7190 com transmit error
2443	No ack from ADV7190 com
2444	ADV7190 com response response
2500	Video scart Low OK
2501	I2C Scart Low bus busy
2502	Scart Low transmit error
2520	Video scart Mid OK
2521	I2C Scart Mid bus busy
2522	Scart Mid transmit error
2540	Video scart High OK
2541	I2C Scart High bus busy
2542	Scart High transmit error
6100	Video colour RGB OK
6120	Video colour YUV OK

5.8.8 Furore Nuclei

Error code	Error text
6300	Furore Sdram WRR_L OK
6301	Furore Sdram WRR_L DB fail
6302	Furore Sdram WRR_L AB fail
6303	Furore Sdram WRR_L DVC fail
6320	Furore Sdram WRR_H OK
6321	Furore Sdram WRR_H DB fail
6322	Furore Sdram WRR_H AB fail
6323	Furore Sdram WRR_H DVC fail
6400	Furore Sdram WRR_F_L OK
6401	Furore Sdram WRR_F_L DB fail
6402	Furore Sdram WRR_F_L AB fail
6403	Furore Sdram WRR_F_L DVC fail
6420	Furore Sdram WRR_F_H OK
6421	Furore Sdram WRR_F_H DB fail
6422	Furore Sdram WRR_F_H AB fail
6423	Furore Sdram WRR_F_H DVC fail
6500	Furore ID OK
6501	Furore ID invalid
8300	Furore reset OK
8301	Furore reset ID NOK
8400	Furore High OK
8420	Furore Low OK

5.9 Test Instruction Front Display and Audio/Video Board

These test instruction is designed specifically for SACD 2002 single disc models which has the following outputs:

- 6 Channel Audio output
- Coaxial / Optical digital output
- CVBS
- Component output YUV
- SVHS
- Double SCART output
- Front Display

5.9.1 General

- All the waveforms measurement carried out in these test instruction will be base on the testpoint indicated in the A/V Board and Front Display schematic diagram in the Service manual.
- Impedance of the measuring-equipment should be > 1MΩ
- Most of the tests can be done using either the Diagnostic software "Player script" which can be found in the chapter "Diagnostic Software description and troubleshooting" or the Menu interface using the Service PC with a terminal emulation program (e.g. Window Hyperterminal) where it is possible to control the execution of the Diagnostic Nuclei
- Setup for the measurement will be done in set level with all modules connected as shown in the Wiring Block diagram.

5.9.2 General Start-Up Measurement

Supply Check:

Before starting the measurement,ensure that all power supply are connected to the A/V and Front Display board via conn.1420 and 1108 respectively.

Pin nr.	A/V Board	Front Display
Voltage	Conn. 1420	Conn. 1108
1	+3V3_Power	-
2	+3V3_Power	-
3	GND	-
4	+12V_Power	-
5	+12VSTBY	-
6	GND	+5VSTBY
7	+5VSTBY	+12V_Power
8	GND	-32V_Power
9	-12V_Power	-
10	GND	-
11	-32V_Power	-
12		-

Clock Check

Ensure the present of the clock to the DAC and the slave μP.

Clock Name	Testpoint	Frequency
PCM_CLK	I116	11.2896MHz ± 0.02% tolerance
XOUT	S1	8MHz ± 0.2% tolerance

Audio Mute Check

Measure the Audio mute voltage input at pin 22 of connector 1421

Status	Value
AudioMuteOn	HIGH (>3V)
AudioMuteOff	LOW (<3V)

To toggle between ON and OFF,use the following commands:

Ref.#	Command Name	Remarks
19a	AudioMuteOn	Audio Mute On
19b	AudioMuteOff	Audio Mute Off

5.9.3 Audio DAC And Amplifier

Ensure that the Audio mute signal is OFF

To check the DAC and buffer amplifier, send the following commands.

Ref.#	Command Name	Remarks	Audio output
21a	AudioSineOn	Audio Sine signal ON	Sine, 1KHz on stereo
----	Press stop button	Audio Sine signal OFF	No waveform
20a	AudioPinkNoiseOn	Audio Pinknoise ON	Pink Noise on 6 channels
20b	AudioPinkNoiseOff	Audio Pinknoise OFF	No waveform

The audio signal (sine or pink noise) will also be present on the digital output (SPDIF). This can be checked by connecting digital signal to an amplifier with digital input.

Check the I2S and audio signal at the following testpoints:

Name	Testpoint
PCM_LRCLK	I112
PCM_SCLK	I114
PCM_CLK	I116
SDT1	I110
SDT2	I106
SDT3	I102
DIG_OUT	I629
STEREO L/R OUT	I510 / I520
FRONT L/R OUT	I552 / I524
SURROUND L/R OUT	I533 / I536
CENTRE OUT	I530
SUB WOOFER L/R OUT	I527

All waveforms can be referred to the A/V board schematic diagram.

5.9.4 Video Output And Buffer Amplifier

Check DC output-level at all video cinch output : 1.0V DC \pm 10%

Generate a color bar using the following software commands:

Ref.#	Command Name	Remarks
23a	VideoColDencOn	Colour DENC ON
23b	VideoColDencOff	Colourbar DENC OFF

Check the video outputs at the following testpoints:

Name	Testpoint
GREEN_SCART	I601
BLUE_SCART	I600
RED_SCART	I602
CVBS out_Mono	I662
C_Mono	I675
Y_Mono	I667

Il waveforms can be referred to the A/V board schematic diagram.

5.9.5 Play and 16/9 Detection

Check DC voltage at S-VIDEO-CHROMA output (pin 4) with a 6k8 ohm load and SCART connector 1403 (pin 16) and change the SCART0 and SCART1 input using the following commands:

Ref.#	Command Name	Remarks
25a	VideoScartLo	Sends out 0V \pm 0.5V
25b	VideoScartMi	Sends out 6V \pm 10%

Ref.#	Command Name	Remarks
25c	VideoScartHi	Sends out 12V \pm 10%

5.9.6 Kill Circuit

To check the functionality of the Kill circuitry, the audio outputs has to be present by the following command:

Ref.#	Command Name	Remarks	Audio output
21a	AudioSineOn	Audio Sinewave ON	1kHz tone

Check the audio outputs at the audio cinch of the A/V and SCART board: 1kHz tone.

Activate the Kill circuit by using the following command:

Ref.#	Command Name	Remarks
19a	AudioMuteOn	Audio Mute On

Check the audio outputs at the audio L/R cinch and SCART of the A/V and SCART board respectively: No waveform
Switch off the kill circuit by using the following command:

Ref.#	Command Name	Remarks
19b	AudioMuteOff	Audio Mute Off

Check the audio outputs at the audio L/R cinch and SCART of the A/V and SCART board: 1kHz tone

5.9.7 Digital Silence

Digital silence is a signal from the audio DAC7301 (MFL,MFR) and DAC7300 (MSR/MC), when there is no input to the audio DAC, or when the player is in STOP/PAUSE mode, or during disc changing track.

To check the MFL signal, use the following command and check the voltage level at pin 41 of 7200:

Ref.#	Command Name	Remarks	KILL_LR signal
21a	AudioSineOn	Audio Sinewave ON	LOW (<0.3V)
---	Press STOP button	Audio Sine signal OFF	HIGH (>4.5V)

5.9.8 Front Display

To check the segment display of the FTD, the following command can be used. And for full detail description of the test, refer to the chapter of "Diagnostic Software Player Script" which can be found in chapter "Diagnostic Software Description and Troubleshooting"

Ref.#	Command Name	Remarks
30a	DispDisplay	Turn ON local display

5.9.9 IR Receiver

Check at pin 22 of 7401 and observe if this line switches from LOW (<0.3V) to HIGH (>4.5V) when pressing a key on a philips RC5 or RC6 remote control

5.9.10 P50 Interface

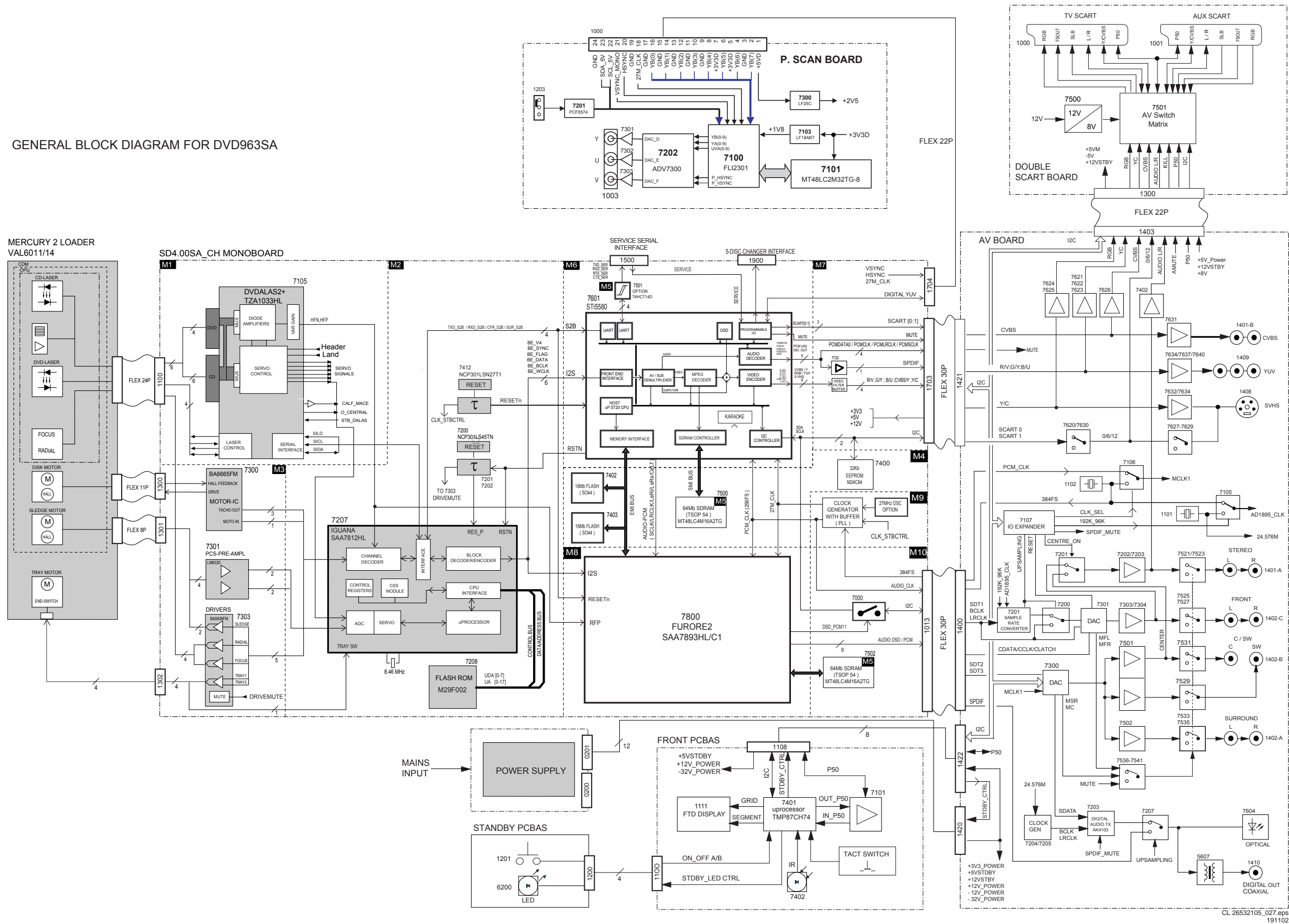
P50 (or Easylink) is a bi-directional serial interface for communication between video equipment. To check for the functionality of the P50 Interface, refer to the chapter of 'Diagnostic Software Player Script' for full detail description.

This image shows a single sheet of white paper with horizontal ruling lines. The lines are evenly spaced and run across the width of the page. There are no margins, text, or other markings on the paper.

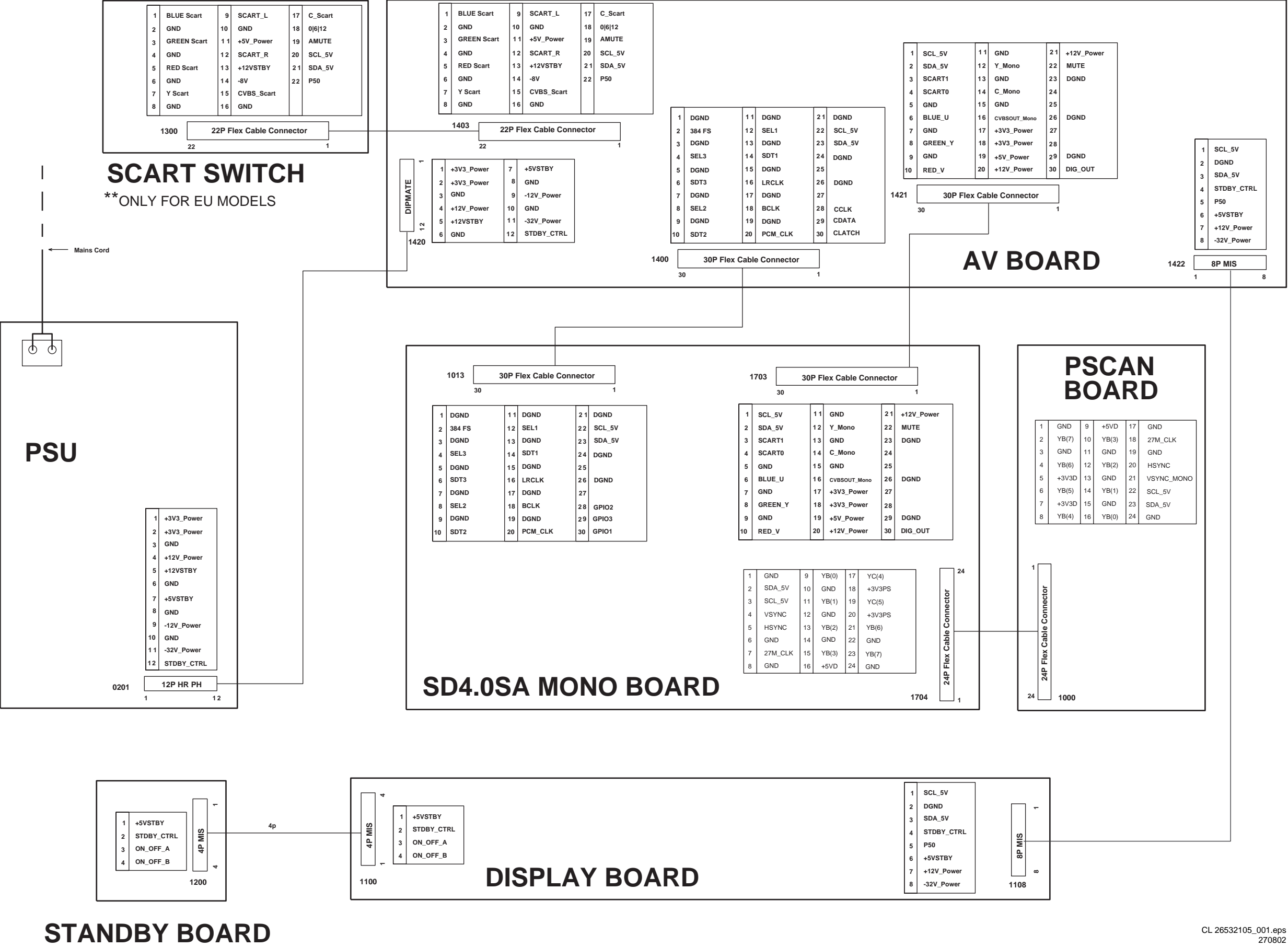
6. Block and Wiring Diagram.

Block Diagram DVD963SA

GENERAL BLOCK DIAGRAM FOR DVD963SA

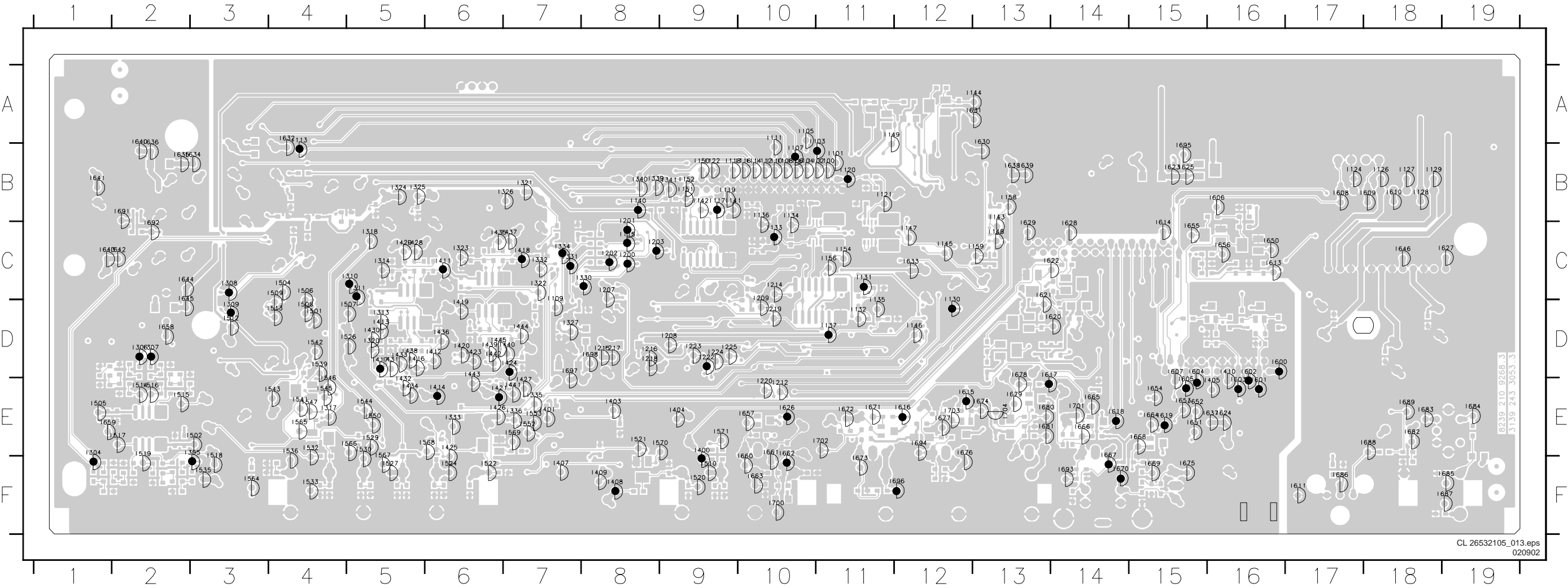


Wiring Diagram DVD963SA



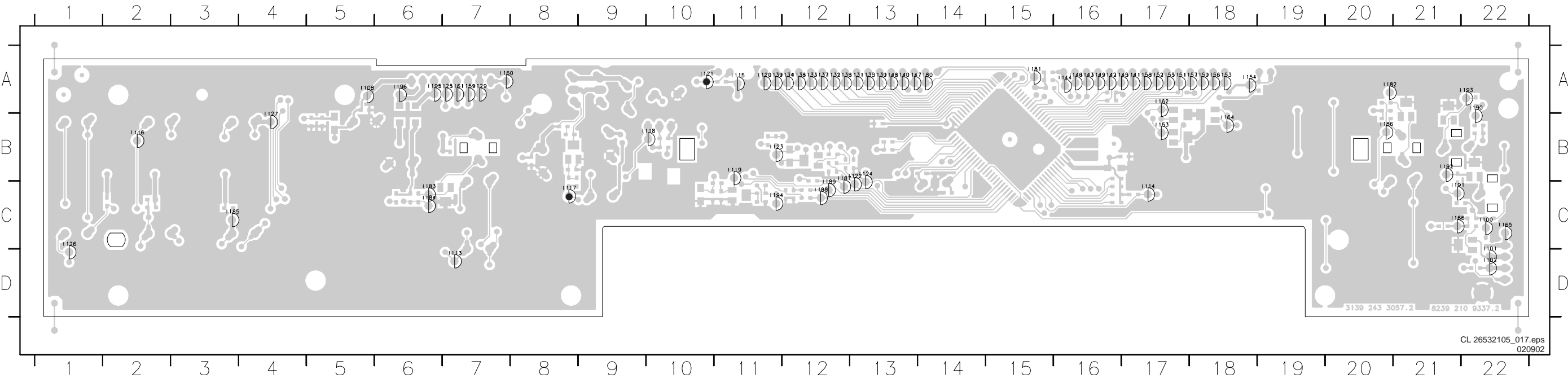
Testpoint Overview A/V Board

I100	B11	I115	C8	I132	D11	I149	B11	I212	E10	I307	D2	I326	B7	I404	E9	I421	E6	I437	C7	I508	D4	I526	D5	I547	E4	I603	E16	I619	E15	I634	B3	I652	E15	I667	F14	I682	E18	I698	D8
I101	B11	I116	B10	I133	C10	I150	B9	I214	C10	I308	C3	I327	D7	I405	E16	I423	D6	I438	D5	I509	D4	I527	F5	I548	E5	I604	E15	I620	D14	I635	B2	I653	E15	I668	E15	I683	E18	I700	F10
I102	B10	I117	B9	I134	C10	I151	B9	I215	D8	I309	D3	I330	C8	I407	F7	I424	D7	I439	D6	I510	F9	I529	E5	I552	E7	I605	E15	I621	D13	I636	B2	I654	E15	I669	F15	I684	E19	I701	E14
I103	B11	I118	B9	I135	D11	I152	B9	I216	D8	I310	C5	I331	C7	I408	F8	I425	F6	I440	D7	I512	D3	I530	F5	I553	E7	I606	B16	I622	C14	I637	E16	I655	C15	I670	F14	I685	F19	I702	E11
I104	B10	I119	B9	I136	C10	I153	C11	I217	D8	I311	C5	I332	C7	I409	F8	I426	E6	I441	E7	I513	D4	I532	F4	I554	F3	I607	E15	I623	B15	I638	B13	I656	C16	I671	E11	I686	F17	I703	E12
I105	A10	I120	B11	I137	D11	I156	C11	I218	D8	I313	D5	I333	E6	I410	E16	I427	E7	I442	D6	I514	E2	I533	F4	I555	E4	I608	B17	I624	E16	I639	B13	I657	E10	I672	E11	I687	F19	I704	E13
I106	B10	I121	B11	I140	B8	I158	B13	I219	D10	I314	C5	I334	C7	I411	C6	I428	C5	I443	E6	I515	E2	I535	F3	I556	E5	I609	B18	I625	B15	I640	B2	I658	D2	I673	F11	I688	E18		
I107	B10	I122	B9	I141	B9	I159	C13	I220	E10	I317	E4	I335	E7	I412	D6	I429	C5	I444	D7	I516	E2	I536	F4	I557	F5	I610	B18	I626	E10	I641	B1	I659	E1	I674	E13	I689	E18		
I108	B10	I124	B17	I142	B9	I200	C8	I222	D9	I318	C5	I336	E7	I413	D5	I430	D5	I445	D6	I517	E2	I539	D4	I558	E6	I611	F17	I627	C19	I642	C2	I660	F10	I675	F15	I691	B2		
I109	D7	I126	B18	I143	C13	I201	C8	I223	D9	I320	D5	I339	B8	I414	E6	I431	D5	I501	D4	I518	F3	I541	E4	I569	E7	I613	C16	I628	C14	I643	C1	I661	F10	I676	F12	I692	C2		
I110	B10	I127	B18	I144	A13	I202	C8	I224	D9	I321	B7	I340	B8	I416	D5	I432	E5	I502	E3	I519	F2	I542	D4	I570	E9	I614	C15	I629	C13	I644	C2	I662	F10	I677	E12	I693	F14		
I111	B10	I128	B18	I145	C12	I203	C8	I225	D9	I322	C7	I341	B9	I417	D5	I433	D5	I504	C4	I520	F9	I543	E4	I571	E9	I615	E12	I630	B13	I645	D2	I663	F10	I678	E13	I694	E12		
I112	B10	I129	B18	I146	D12	I207	C8	I304	F1	I323	C6	I400	F9	I418	C7	I434	E5	I505	E1	I521	E8	I544	E5	I600	D16	I616	E12	I631	A13	I646	C18	I664	E15	I679	E13	I695	B15		
I113	B4	I130	D12	I147	C12	I208	D9	I305	F3	I324	B5	I401	E7	I419	D6	I435	C6	I506	D4	I522	F6	I545	E4	I601	E16	I617	E13	I632	B4	I650	C16	I665	E14	I680	E13	I696	F12		
I114	B10	I131	C11	I148	C13	I209	D10	I306	D2	I325	B5	I403	E8	I420	D6	I436	D6	I507	D5	I524	F6	I546	E4	I602	E16	I618	E14	I633	C12	I651	E15	I666	E14	I681	E13	I697	E7		



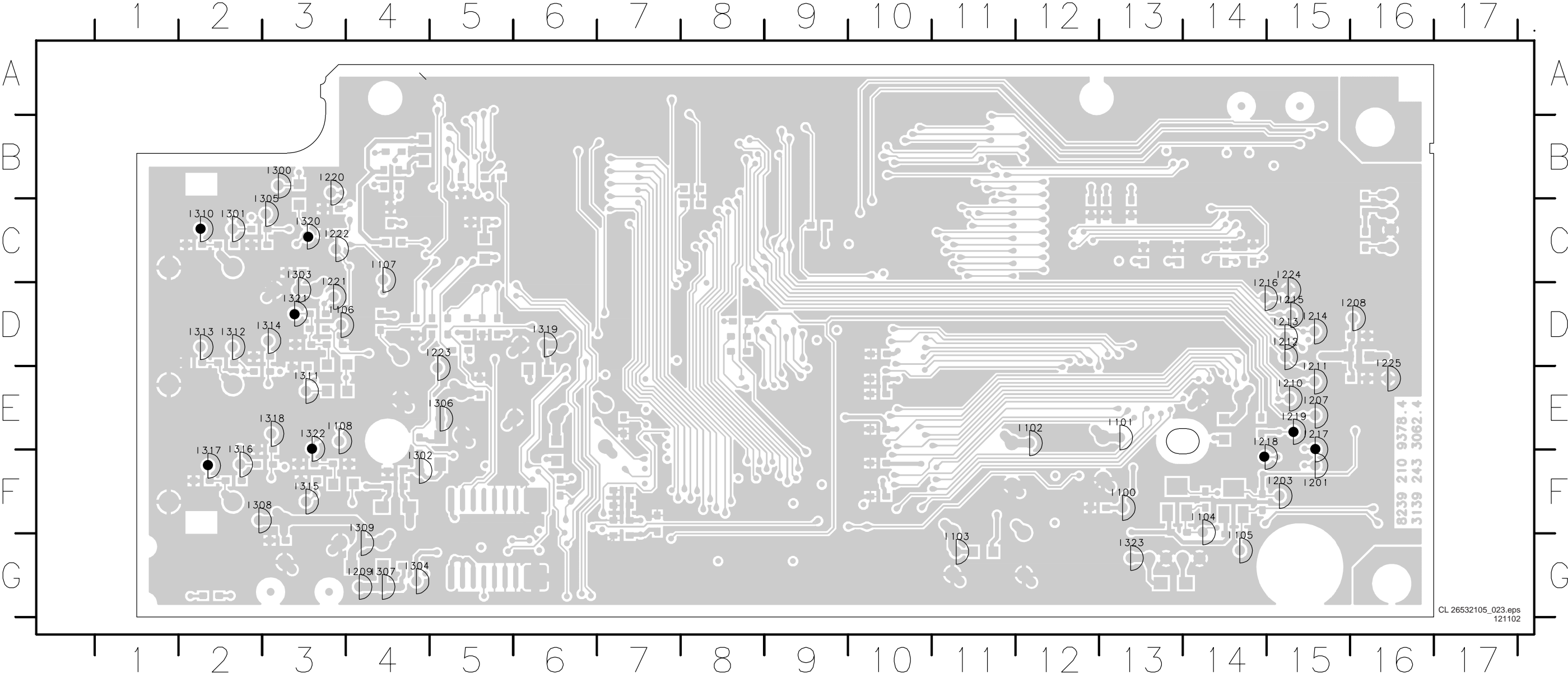
Testpoint Overview Display Panel

I100	C22	I113	D7	I117	C8	I121	A10	I125	A7	I130	A13	I134	A12	I138	A12	I142	A16	I146	A16	I150	A18	I154	A18	I158	A17	I162	A17	I166	C21	I183	C6	I187	C12	I191	C21	I195	A6
I101	D22	I114	C17	I118	B10	I122	C13	I126	D1	I131	A13	I135	A13	I139	A11	I143	A16	I147	A13	I151	A17	I155	A17	I159	A7	I163	B17	I180	A14	I184	C6	I188	C12	I192	B21	I196	A6
I102	D22	I115	A11	I119	B11	I123	B11	I127	B4	I132	A12	I136	A12	I140	A13	I144	A16	I148	A13	I152	A17	I156	A18	I160	A7	I164	B18	I181	A15	I185	C3	I189	C12	I193	A22		
I108	A5	I116	B2	I120	A11	I124	C13	I129	A7	I133	A12	I137	A12	I141	A17	I145	A16	I149	A16	I153	A18	I157	A18	I161	A7	I165	C22	I182	A20	I186	B20	I190	B22	I194	C11		



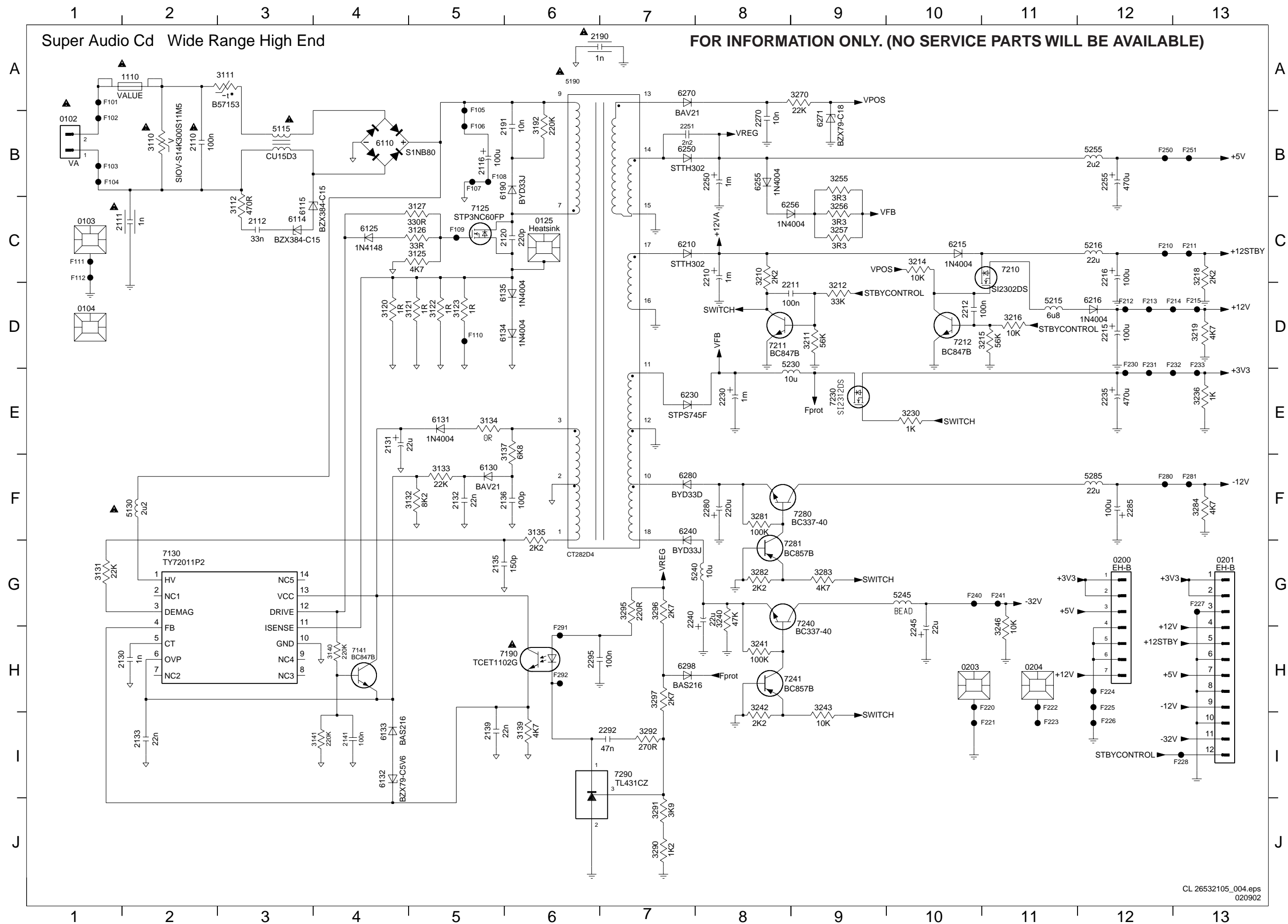
Testpoint OverviewP-Scan Board

I100 F13	I103 G11	I106 D3	I201 F15	I208 D16	I211 E15	I214 D15	I217 E15	I220 B3	I223 E5	I300 B3	I303 D3	I306 E5	I309 G4	I312 D2	I315 F3	I318 E3	I321 D3
I101 E13	I104 F14	I107 C4	I203 F15	I209 G4	I212 D15	I215 D15	I218 F14	I221 D3	I224 D15	I301 C2	I304 G4	I307 G4	I310 C2	I313 D2	I316 F2	I319 D6	I322 E3
I102 E12	I105 G14	I108 E3	I207 E15	I210 E15	I213 D15	I216 D14	I219 E15	I222 C3	I225 E16	I302 F4	I305 C3	I308 F2	I311 E3	I314 D3	I317 F2	I320 C3	I323 G13

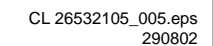


7. Electrical Diagrams

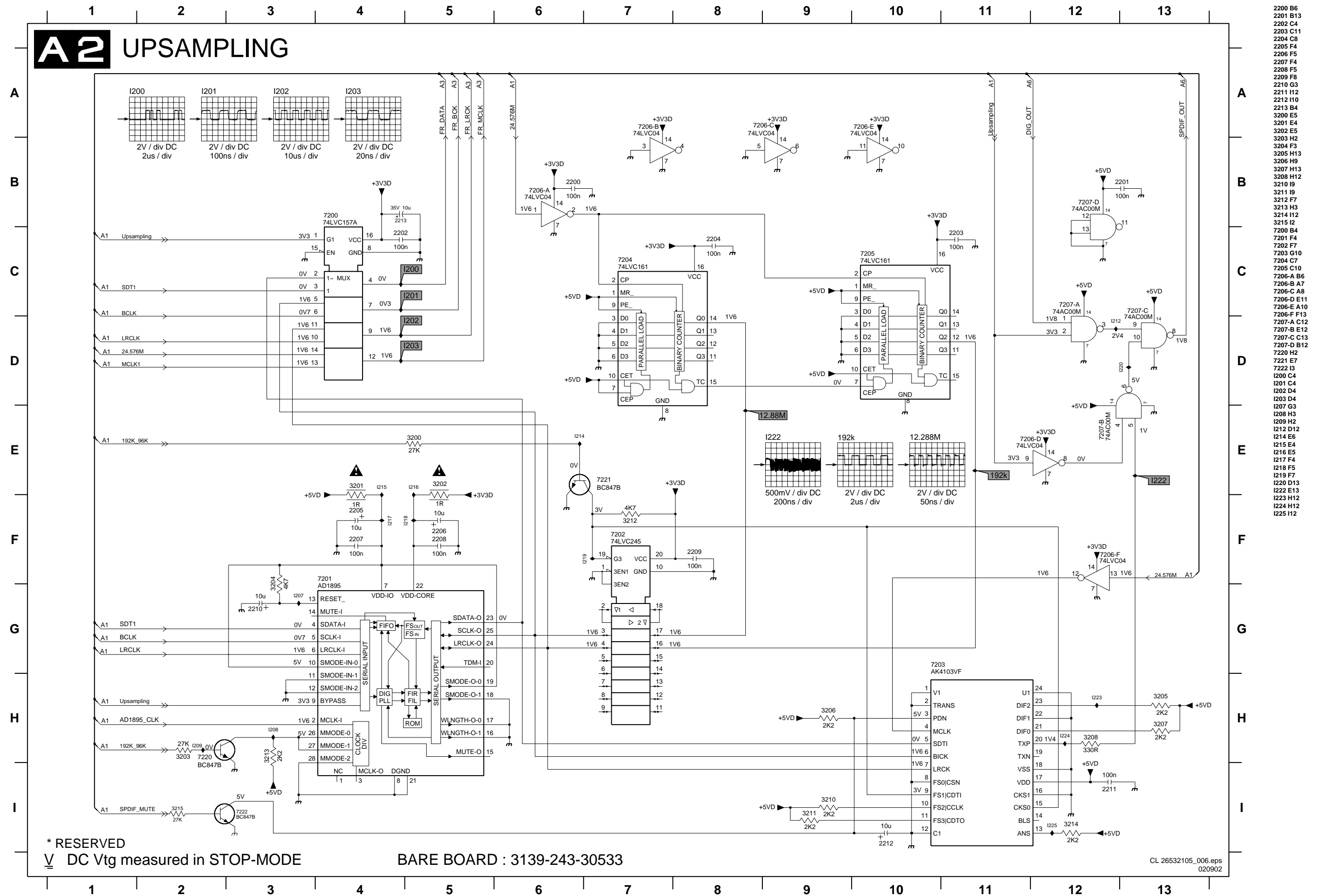
Power Supply Unit DVD963SA



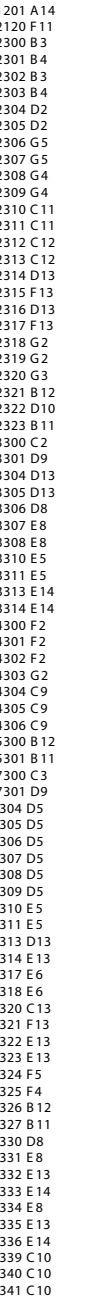
A 1 SYS CLK & CONTROL



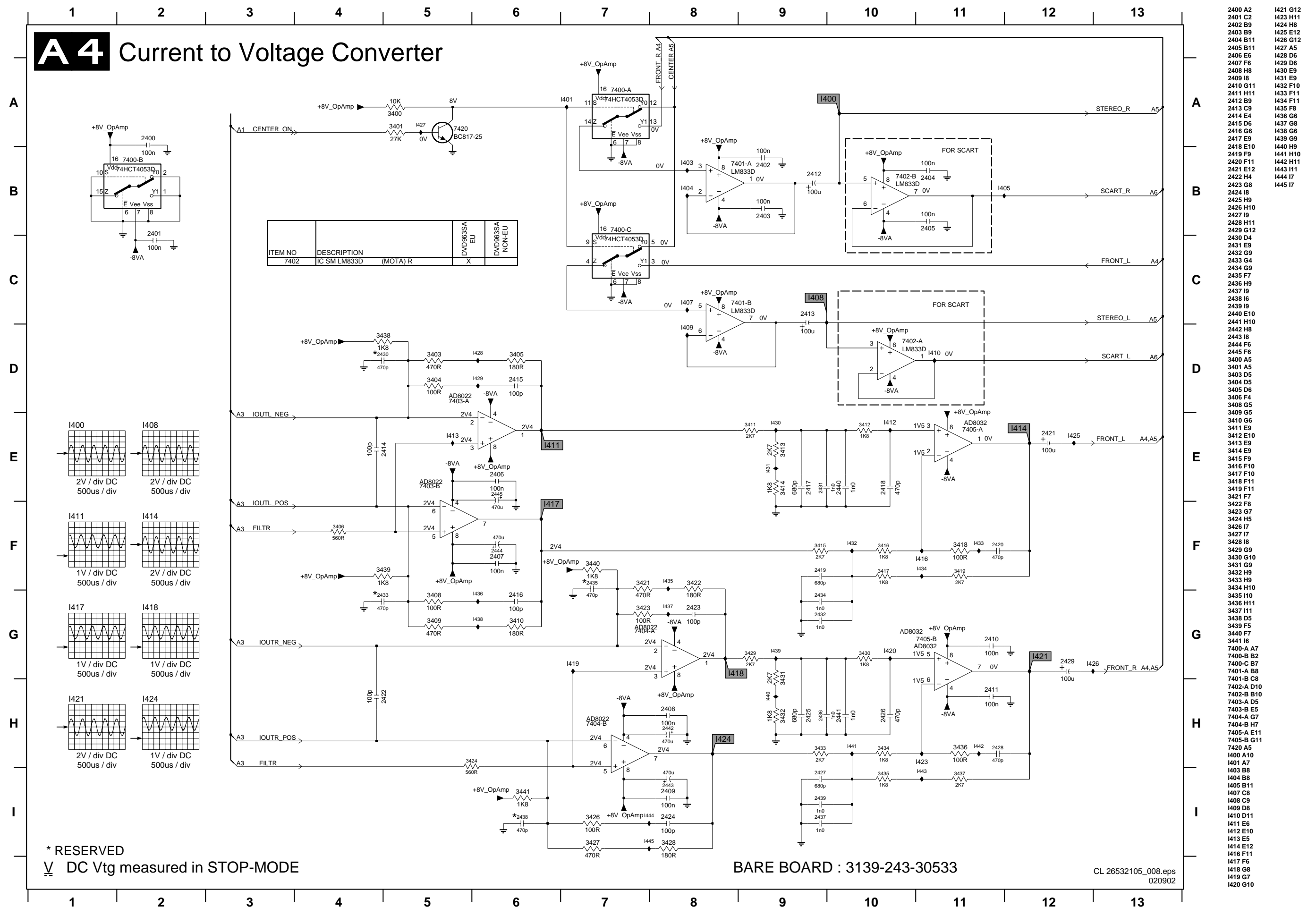
AV-Board: Upsampling



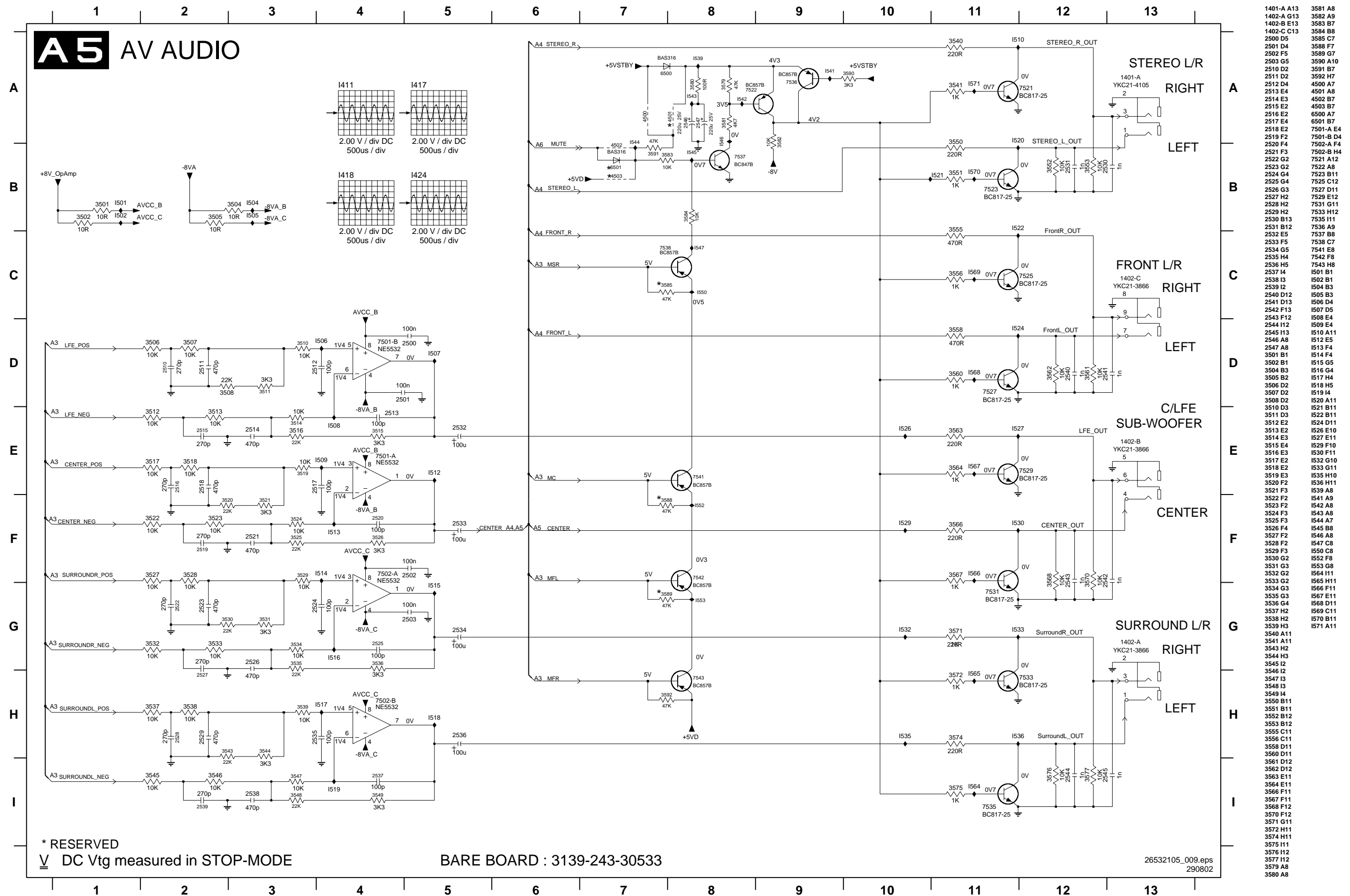
A3 AUDIO DAC

CL 26532105_007.eps
290802

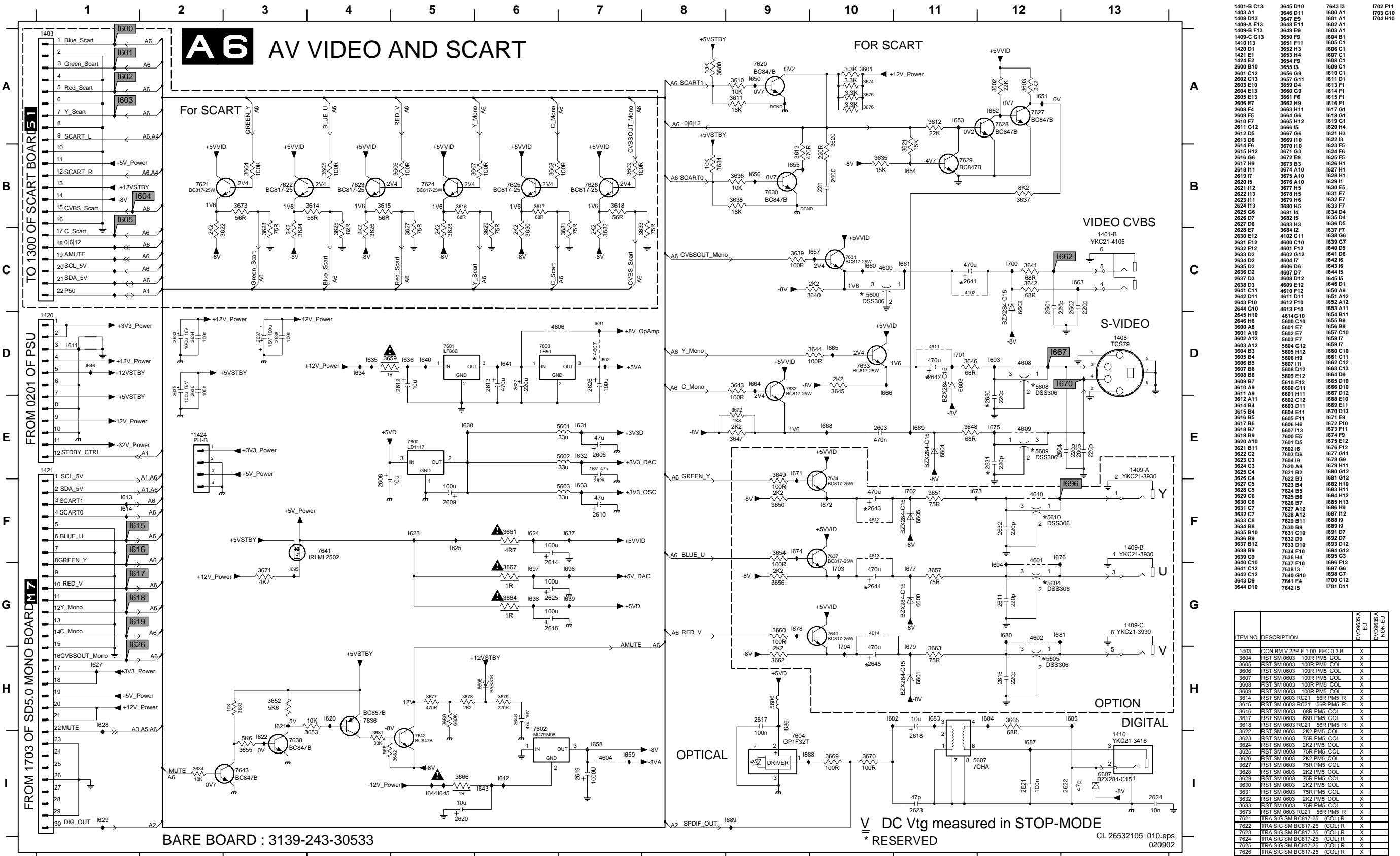
AV-Board: Current to Voltage Converter



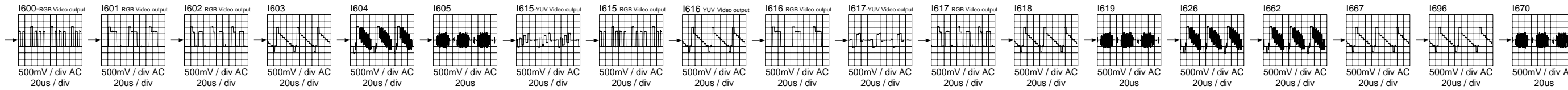
AV-Board: Audio



AV-Board: Video and SCART

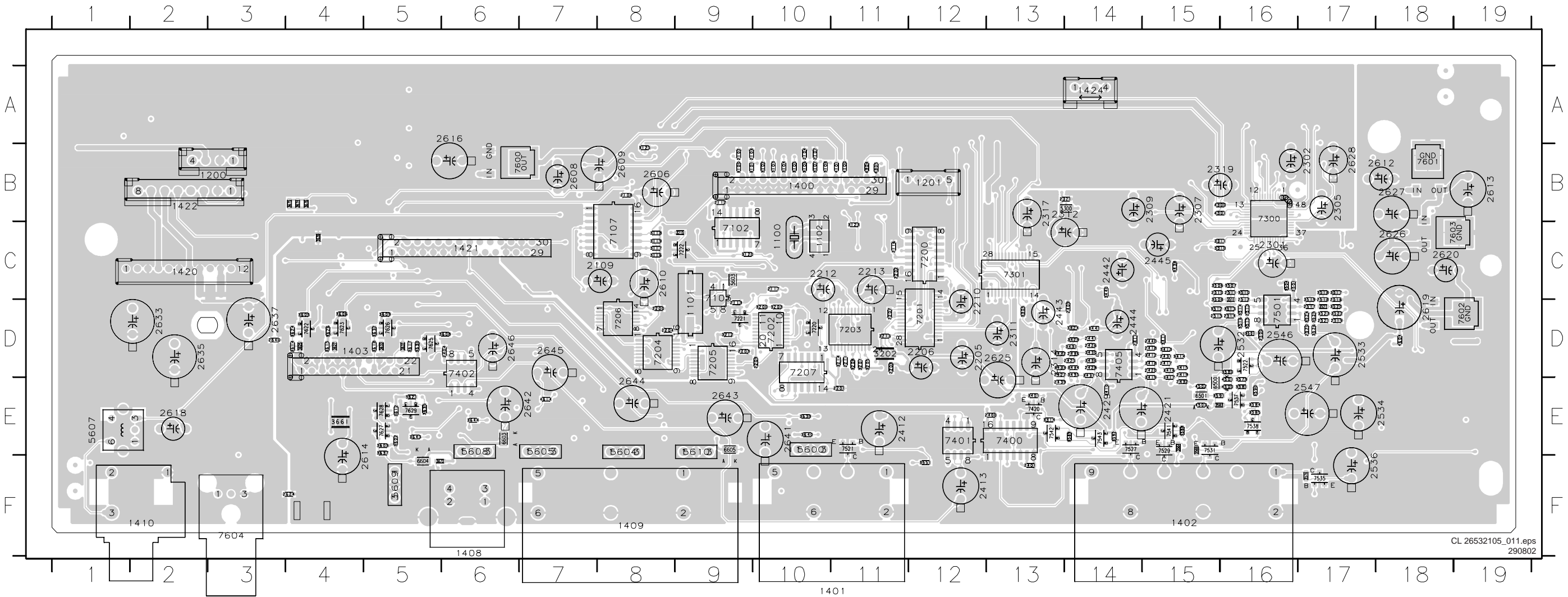


1401-B C13	3645 D10	7643 I3	I702 F11
1403 A1	3646 D11	1600 A1	I703 G10
1408 D13	3647 E9	1601 A1	I704 H10
1409-A E13	3648 E11	1602 A1	
1409-B F13	3649 E9	1603 A1	
1409-C G13	3650 F9	1604 B1	
1410 I13	3651 F11	1605 C1	
1420 D1	3652 H3	1606 C1	
1421 E1	3653 H4	1607 C1	
1424 E2	3654 F9	1608 C1	
1600 B10	3655 I3	1609 C1	
1601 C12	3656 G9	1610 C1	
1602 C13	3657 G11	1611 D1	
1603 E10	3659 D4	1613 F1	
1604 E13	3660 G9	1614 F1	
1605 E13	3661 F6	1615 F1	
1606 E7	3662 H9	1616 F1	
1608 F4	3663 H11	1617 G1	
1609 F5	3664 G6	1618 G1	
1610 F7	3665 H12	1619 G1	
1611 G12	3666 I5	1620 H4	
1612 D5	3667 G6	1621 H3	
1613 D6	3669 H0	1622 I3	
1614 F6	3670 H0	1623 F5	
1615 H2	3671 G3	1624 F6	
1616 G6	3672 E9	1625 F5	
1617 H9	3673 B3	1626 H1	
1618 H11	3674 A10	1627 H1	
1619 I7	3675 A10	1628 H1	
1620 I5	3676 A10	1629 I1	
1621 H2	3677 H5	1630 E5	
1622 I13	3678 H5	1631 E7	
1623 I11	3679 H6	1632 E7	
1624 I13	3680 H5	1633 F7	
1625 G6	3681 H4	1634 D4	
1626 D7	3682 I5	1635 D4	
1627 D6	3683 H3	1636 D5	
1628 H3	3684 I2	1637 I7	
1629 C10	4102 C11	1638 G6	
1630 E12	4600 C10	1639 G7	
1631 D2	4601 F12	1640 D5	
1632 D2	4602 D12	1641 D6	
1633 D2	4604 I7	1642 I6	
1634 D2	4606 D6	1643 H6	
1635 D2	4607 D7	1644 I5	
1636 D3	4608 D12	1645 I5	
1637 D3	4609 E12	1646 D1	
1638 D3	1641 C11	1647 H11	
1639 D3	1642 D11	1648 I12	
1640 D3	1643 F10	1649 A12	
1641 D3	1644 G10	1650 A12	
1642 D3	1645 H10	1651 A12	
1643 D3	1646 H6	1652 A12	
1644 D3	1647 H6	1653 A11	
1645 D3	1648 H6	1654 B11	
1646 D3	1649 H6	1655 B9	
1647 D3	1650 A8	1656 B9	
1648 D3	1651 A10	1657 C10	
1649 D3	1652 A12	1658 I7	
1650 D3	1653 A12	1659 I7	
1651 D3	1654 B3	1660 C10	
1652 D3	1655 H9	1661 C11	
1653 D3	1656 B5	1662 C12	
1654 D3	1657 B6	1663 C13	
1655 D3	1658 B7	1664 D9	
1656 D3	1659 B7	1665 D10	
1657 D3	1660 G11	1666 D10	
1658 D3	1661 H11	1667 D12	
1659 D3	1662 A11	1668 E10	
1660 D3	1663 B4	1669 E11	
1661 D3	1664 B4	1670 D13	
1662 D3	1665 B5	1671 E9	
1663 D3	1666 B6	1672 F9	
1664 D3	1667 B7	1673 F11	
1665 D3	1668 B8	1674 F9	
1666 D3	1669 B9	1675 E12	
1667 D3	1670 B9	1676 F12	
1668 D3	1671 B9	1677 G11	
1669 D3	1672 B9	1678 G9	
1670 D3	1673 B9	1679 H11	
1671 D3	1674 B9	1680 G12	
1672 D3	1675 B9	1681 G12	
1673 D3	1676 B9	1682 H10	
1674 D3	1677 B9	1683 H11	
1675 D3	1678 B9	1684 H12	
1676 D3	1679 B9	1685 H13	
1677 D3	1680 B9	1686 H9	
1678 D3	1681 B9	1687 I12	
1679 D3	1682 B9	1688 I9	
1680 D3	1683 B9	1689 I9	
1681 D3	1684 B9	1690 I7	
1682 D3	1685 B9	1691 D7	
1683 D3	1686 B9	1692 D7	
1684 D3	1687 B9	1693 D12	
1685 D3	1688 B9	1694 G12	
1686 D3	1689 B9	1695 G3	
1687 D3	1690 B9	1696 F12	
1688 D3	1691 B9	1697 G6	
1689 D3	1692 B9	1698 G7	
1690 D3	1693 B9	1700 C12	
1691 D3	1694 B9	1701 D11	



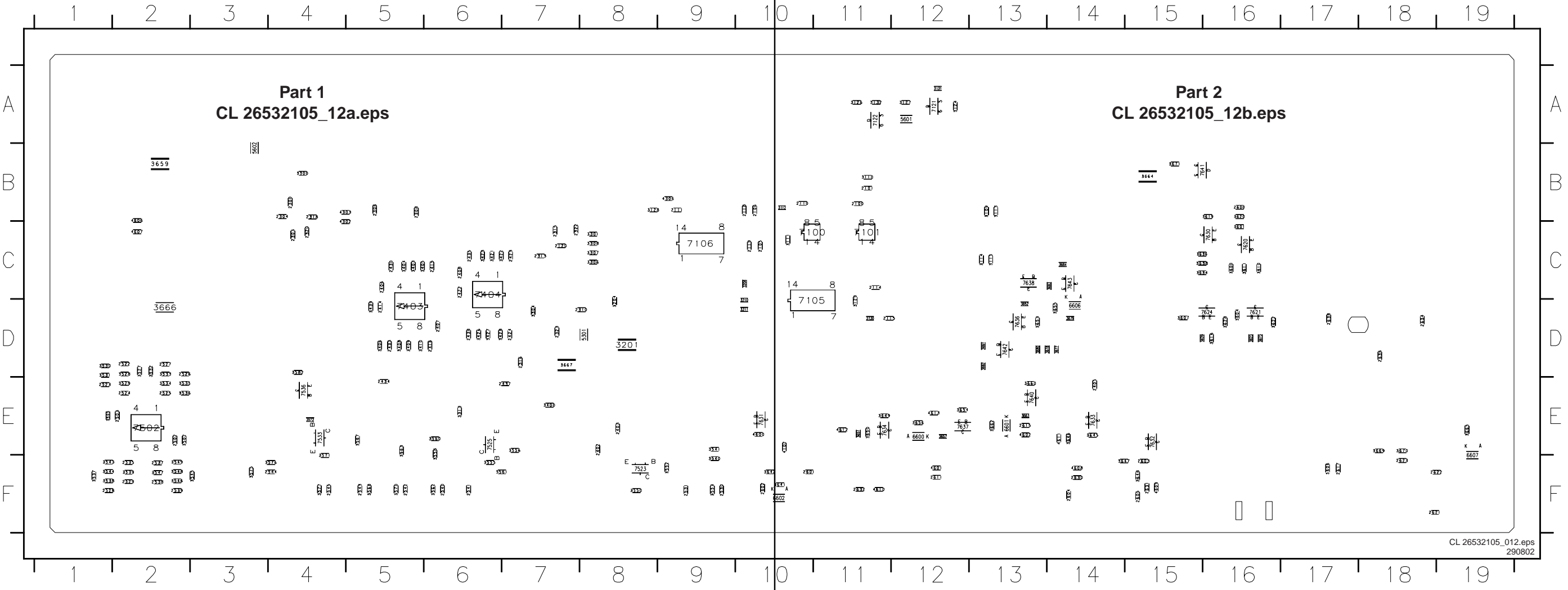
Layout AV-Board (Top Side)

1100	C10	2103	B9	2207	D11	2319	B16	2426	D14	2510	C15	2546	D16	2627	B18	3103	B10	3205	D11	3415	D15	3440	C14	3519	D17	3584	E16	3617	D5	3644	E6	4604	D18	7102	C9	7400	E13	7543	E14
1101	D9	2104	B10	2208	D11	2321	B14	2427	D13	2511	C16	2547	E17	2628	B17	3104	B10	3207	D11	3416	D15	3441	D14	3520	D17	3585	E16	3618	D5	3661	E4	4614	E7	7103	C9	7401	E12	7600	B6
1102	C10	2107	B10	2209	D10	2400	E13	2428	D14	2512	D16	2606	B8	2633	D2	3105	B10	3208	D11	3417	E15	3501	D16	3521	D17	3588	E15	3621	E5	3672	F5	5300	B14	7107	C8	7402	D6	7601	B18
1200	B3	2108	B8	2210	D12	2401	F13	2429	E14	2513	D16	2608	B7	2635	D2	3106	B10	3212	D9	3418	D15	3504	C16	3522	D17	3589	E14	3624	D4	3674	B4	5600	E10	7200	C12	7405	D14	7602	D19
1201	B12	2109	C8	2211	D11	2404	D6	2431	D15	2514	D16	2609	B8	2637	D3	3107	B10	3213	D11	3419	E15	3506	C15	3523	D17	3590	E16	3625	D4	3675	B4	5603	C9	7201	D12	7420	E13	7603	C18
1400	B10	2112	B9	2212	C10	2409	D14	2432	E15	2515	D15	2610	C8	2639	C4	3108	B10	3214	D11	3424	D14	3507	C16	3524	D17	3591	E15	3626	D4	3676	B4	5604	E8	7202	D10	7501	D16	7604	F3
1402	F15	2113	D9	2213	C11	2410	E14	2436	D14	2516	D17	2612	B18	2640	D3	3116	C8	3215	C9	3429	D14	3508	D15	3525	D17	3592	E14	3627	D4	4100	B11	5605	E7	7203	D11	7521	E11	7622	D4
1403	D4	2116	C11	2301	C16	2411	D14	2439	E13	2517	D17	2613	B19	2641	E10	3117	C8	3305	D13	3430	D14	3510	C16	3526	D17	3602	E5	3630	D5	4101	B11	5607	E1	7204	D8	7522	D16	7623	D4
1408	F6	2117	C10	2302	B17	2412	E11	2440	D15	2518	D17	2614	F5	2642	E7	3118	C8	3310	C16	3431	D14	3511	D16	3551	F12	3603	E5	3631	D5	4300	B16	5608	E6	7205	D9	7527	E14	7625	D5
1409	F8	2200	D8	2305	B17	2413	F12	2441	D14	2519	D17	2616	A6	2643	E9	3119	C8	3311	C16	3432	D13	3512	D15	3564	E15	3605	D4	3632	D5	4301	B16	5609	F5	7206	D8	7529	E15	7626	D5
1410	F2	2201	E11	2307	B15	2417	D15	2442	C14	2520	D17	2618	E2	2644	E8	3120	C9	3314	E13	3433	D13	3513	D16	3566	E15	3606	D4	3633	D5	4304	B11	5610	E9	7207	D10	7531	E15	7627	E5
1420	C2	2202	C11	2309	B15	2418	D15	2443	D13	2521	D17	2619	D18	2645	D7	3123	C11	3401	E13	3434	D14	3514	D16	3574	F17	3608	D6	3635	E5	4305	B11	6500	E15	7220	D10	7535	F17	7628	E5
1421	C6	2203	D9	2311	D13	2419	D15	2444	D14	2532	D16	2620	C18	2646	D6	3125	B8	3411	D15	3435	D14	3515	D16	3579	E16	3609	D5	3637	E5	4500	E16	6501	E15	7221	D9	7537	E16	7629	E5
1422	B2	2204	D8	2312	B14	2420	D15	2445	C15	2533	D17	2624	F4	3100	B9	3200	D10	3412	D15	3436	D14	3516	D16	3580	D16	3612	E5	3639	E10	4501	E16	6603	E6	7222	C9	7538	E16		
1424	A14	2205	D12	2314	D13	2421	E15	2500	D16	2534	E18	2625	D13	3101	B9	3202	D11	3413	D15	3437	D14	3517	C17	3581	E16	3614	D4	3640	E10	4502	E15	6604	F5	7300	B16	7541	E15		
2102	B9	2206	D12	2317	B13	2425	D14	2501	C16	2536	F17	2626	C18	3102	B10	3203	D10	3414	D15	3438	C15	3518	C17	3583	E15	3615	D4	3643	E5	4503	E15	6605	E9	7301	C13	7542	E13		

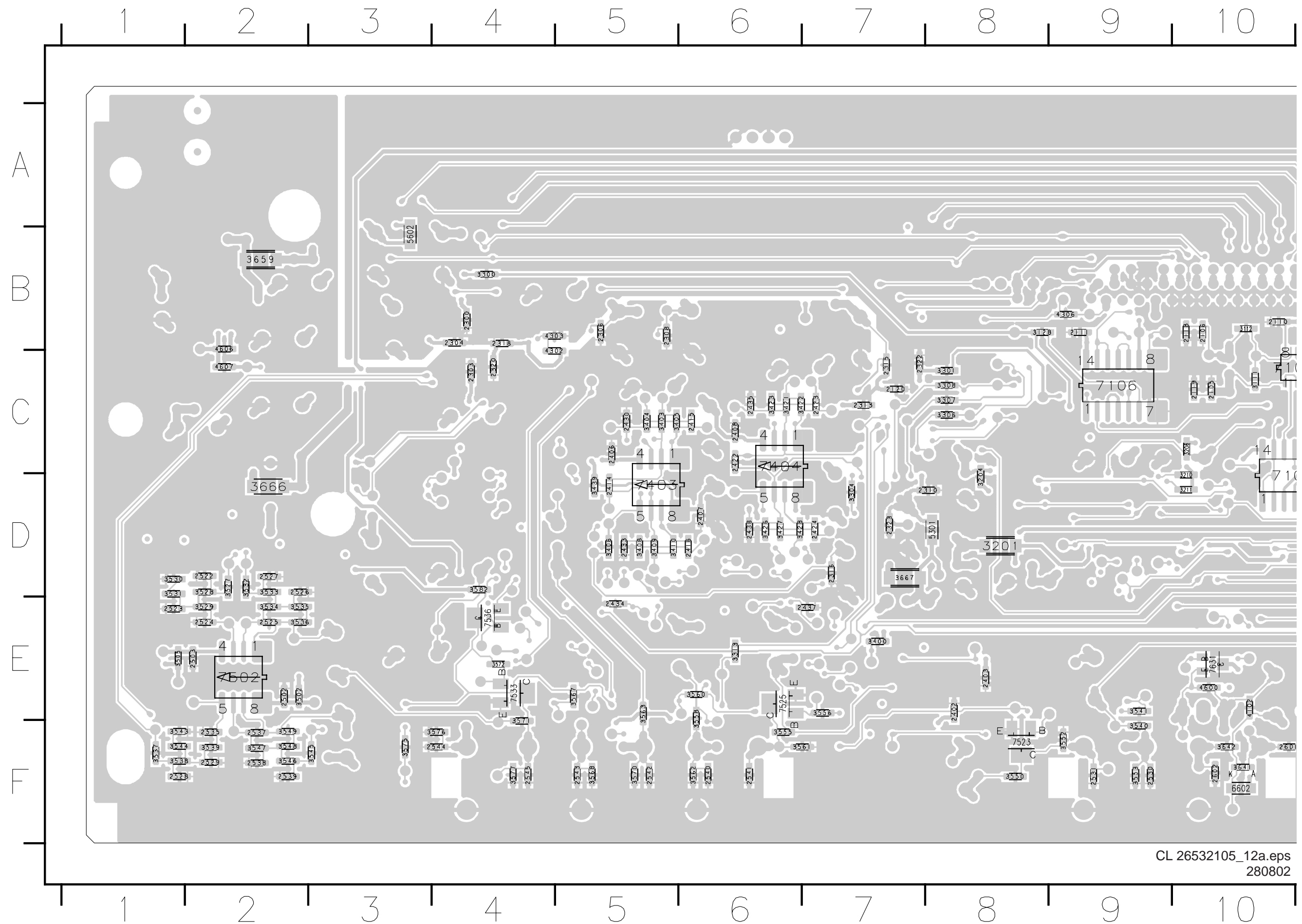


Layout AV-Board (Overview Bottom Side)

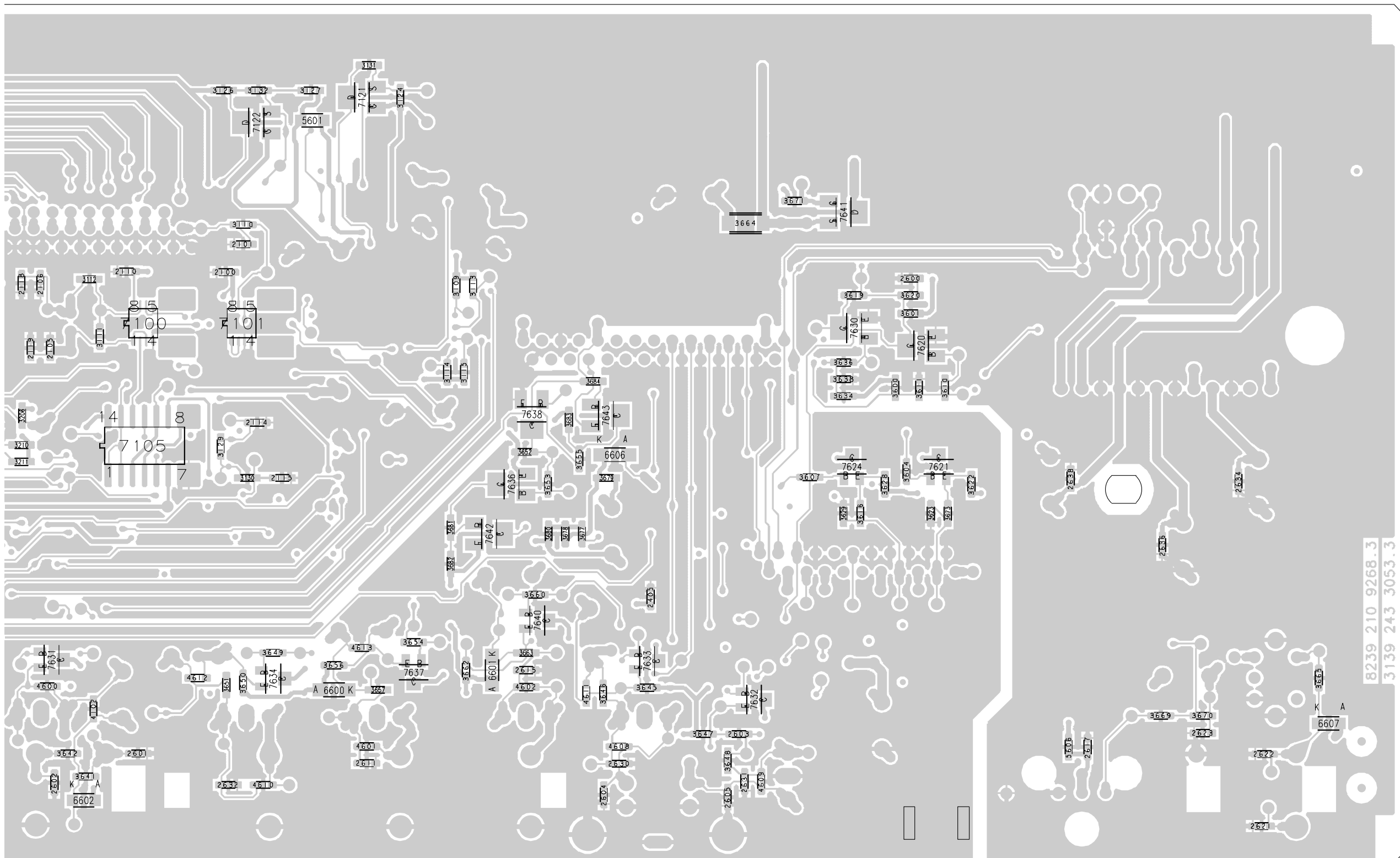
2100 B11	2306 B5	2407 D6	2502 E2	2538 F2	2611 F12	3111 C10	3204 D8	3405 C5	3527 D2	3541 E9	3560 E6	3601 C16	3638 C15	3656 E12	3677 D14	4602 E13	6601 E13	7525 E6	7640 E13
2101 B11	2308 B5	2408 C6	2503 E2	2539 F2	2615 E13	3112 B10	3206 C10	3406 D5	3528 D2	3543 F1	3561 F7	3604 D16	3641 F10	3657 E12	3678 D14	4606 B2	6602 F10	7533 E4	7641 B15
2105 C10	2310 D8	2414 D5	2522 D2	2540 F6	2617 F17	3113 B13	3210 D10	3408 D5	3529 E2	3544 F1	3562 F6	3607 D15	3642 F10	3659 B2	3679 D14	4607 C2	6606 D14	7536 E4	7642 D13
2106 B10	2313 C7	2415 C6	2523 E1	2541 F6	2621 F18	3114 C13	3211 D10	3409 D5	3530 D1	3545 F3	3563 E5	3610 C16	3645 E14	3660 E13	3680 D13	4608 F14	6607 F19	7620 C16	7643 C14
2110 B10	2315 C7	2416 D6	2524 E2	2542 F5	2622 F19	3115 C13	3300 B4	3410 D5	3531 D1	3546 F2	3567 E5	3611 C16	3646 E14	3662 E13	3681 D13	4609 F15	7100 C10	7621 D16	
2111 B9	2316 D7	2422 C6	2525 E2	2543 F5	2623 F18	3124 A12	3301 C8	3421 C6	3532 D2	3547 F2	3568 F5	3616 D16	3647 F14	3663 E13	3682 D13	4610 F11	7101 C11	7624 D16	
2114 C11	2318 B4	2423 C7	2526 D2	2544 F4	2630 F14	3126 A11	3304 D7	3422 C6	3533 D2	3548 F2	3570 F5	3619 B16	3648 F15	3664 B15	3683 C14	4611 E14	7105 D10	7630 C16	
2115 D11	2320 C4	2424 D7	2527 D2	2545 F4	2631 F15	3127 A12	3306 C8	3423 C6	3534 E2	3549 F2	3571 F4	3620 B16	3649 E11	3665 E19	3684 C14	4612 E11	7106 C9	7631 E10	
2118 B10	2322 C7	2430 C5	2528 F1	2600 B16	2632 F11	3128 B8	3307 C8	3426 D6	3535 E2	3550 F8	3572 E4	3622 D16	3650 E11	3666 D2	4102 E10	4613 E12	7121 A12	7632 E15	
2119 C10	2323 D7	2433 D5	2529 F2	2601 F10	2634 D18	3129 D11	3308 C8	3427 D6	3536 E2	3552 F9	3575 F3	3623 D16	3651 E11	3667 D7	4302 C4	5301 D8	7122 A11	7633 E14	
2120 C7	2402 E8	2434 E5	2530 F9	2602 F10	2636 D18	3130 D11	3313 E6	3428 D6	3537 F1	3553 F9	3576 F4	3628 D16	3652 D13	3669 E18	4303 B4	5601 A12	7403 D5	7634 E11	
2300 B4	2403 E8	2435 C6	2531 F9	2603 F15	2638 D17	3131 A12	3400 E7	3439 D5	3538 F1	3555 F6	3577 F4	3629 D15	3653 D13	3670 E18	4306 B9	5602 B3	7404 C6	7636 D13	
2303 C4	2405 E14	2437 E7	2535 F2	2604 F14	3109 B13	3132 A11	3403 C5	3502 E2	3539 F2	3556 E7	3582 D4	3634 C15	3654 E12	3671 B15	4600 E10	5606 F17	7502 E2	7637 E12	
2304 B4	2406 C5	2438 D6	2537 F2	2605 F15	3110 B11	3201 D8	3404 C5	3505 E1	3540 F9	3558 E6	3600 C16	3636 C15	3655 D14	3673 D16	4601 F12	6600 E12	7523 F8	7638 C13	



Layout AV-Board (Part 1 Bottom Side)

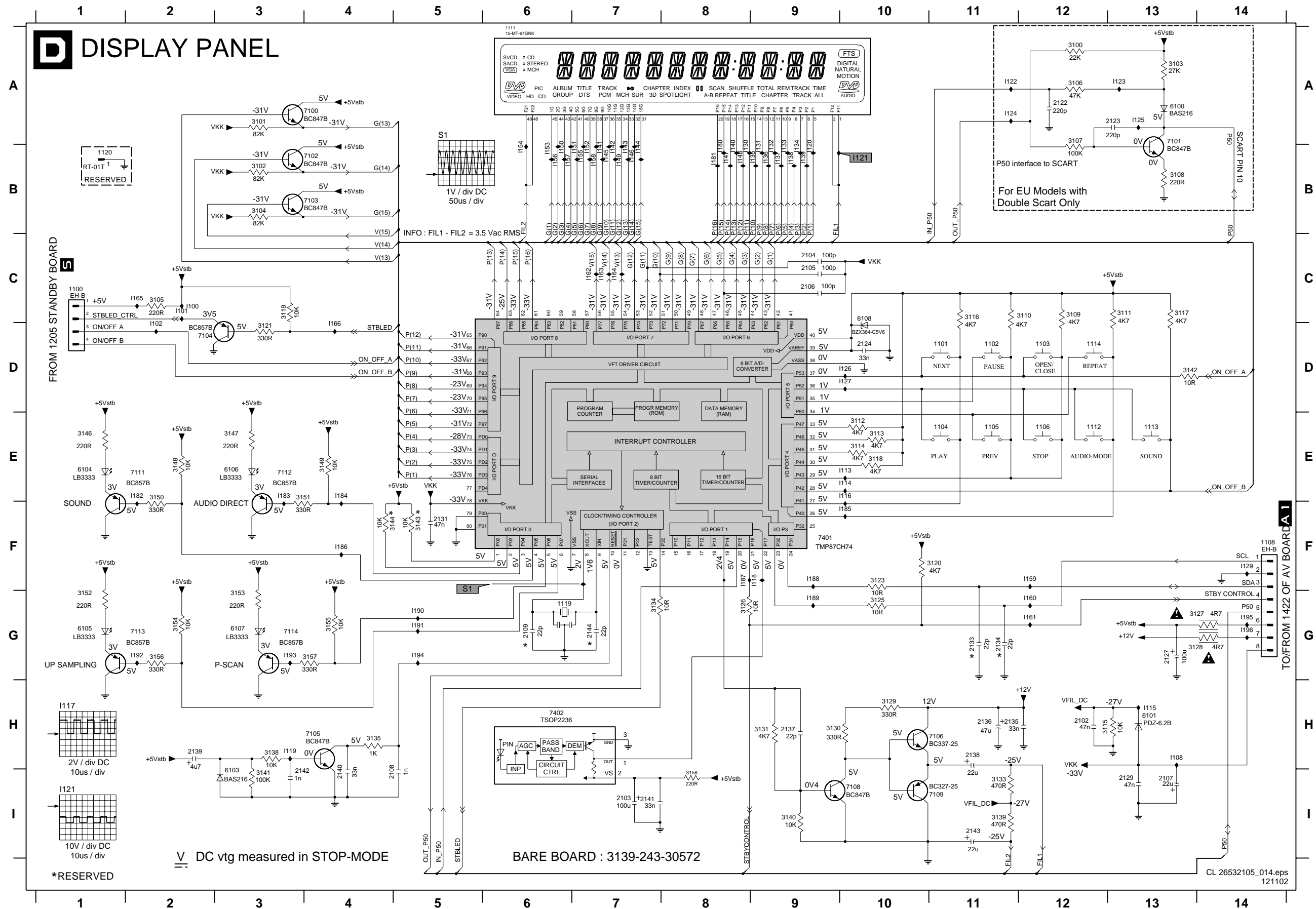


A horizontal number line with tick marks at every integer from 10 to 19. The numbers are written above the corresponding tick marks.

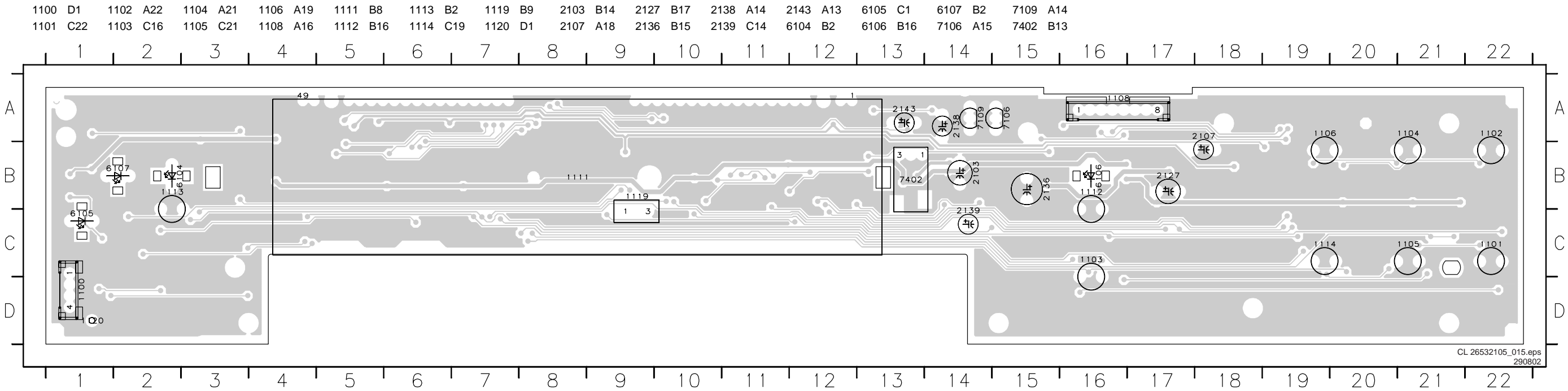


A horizontal number line with tick marks at every integer from 10 to 19. The numbers are labeled below the line.

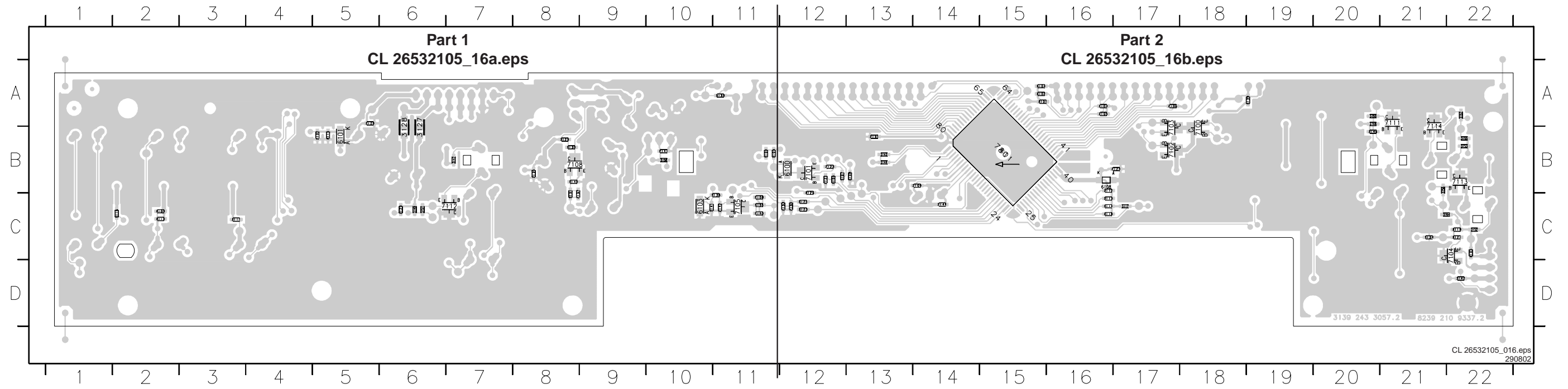
Display Panel



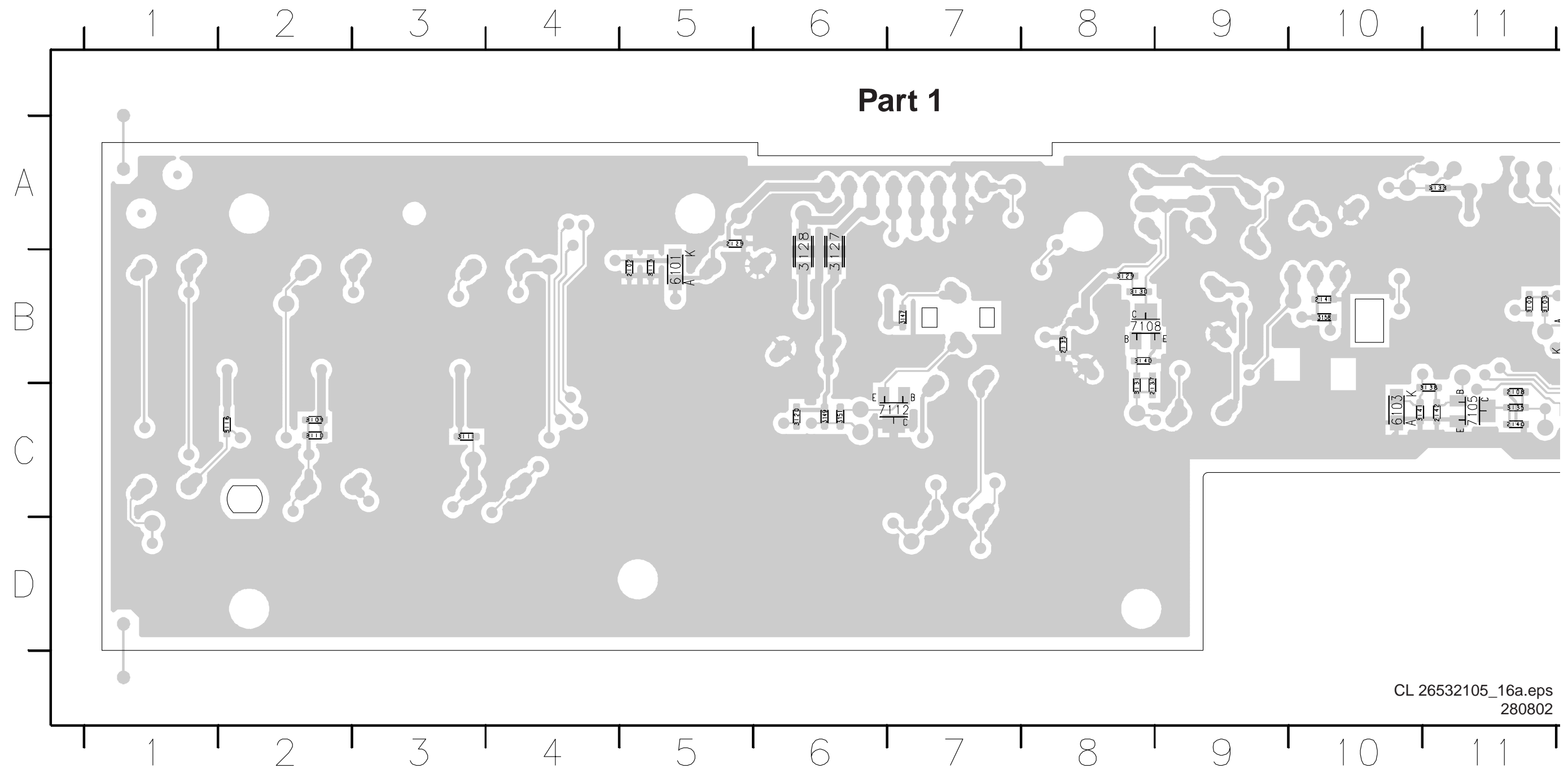
Layout Display Panel (Top Side)



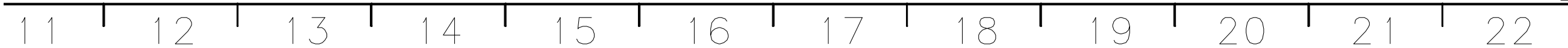
2102	B5	2109	B14	2131	B13	2140	C11	3101	A15	3106	B12	3111	C3	3116	C2	3121	C22	3128	B6	3134	C21	3141	C10	3147	B7	3152	C22	3157	A22	6108	B16	7104	C22	7113	B22
2104	A16	2122	B12	2133	C12	2141	B10	3102	A15	3107	B12	3112	B16	3117	C17	3123	C12	3129	B8	3135	C11	3142	D22	3148	A20	3153	B22	3158	B10	7100	B18	7105	C11	7114	A21
2105	A16	2123	B12	2134	C12	2142	C11	3103	B11	3108	B13	3113	C16	3118	C16	3125	C12	3130	B8	3138	C11	3143	B13	3149	C6	3154	C21	6100	B12	7101	B12	7108	B8	7401	B15
2106	A17	2124	B17	2135	B8	2144	C14	3104	A15	3109	C2	3114	C16	3119	C22	3126	B12	3131	C8	3139	A19	3144	B13	3150	A20	3155	B22	6101	B5	7102	B17	7111	A21		
2108	C11	2129	A5	2137	C8	3100	B11	3105	C22	3110	C2	3115	B5	3120	C6	3127	B6	3133	A11	3140	B8	3146	B20	3151	C6	3156	B21	6103	C10	7103	B17	7112	C7		



Layout Display Panel (Part 1 Bottom Side)

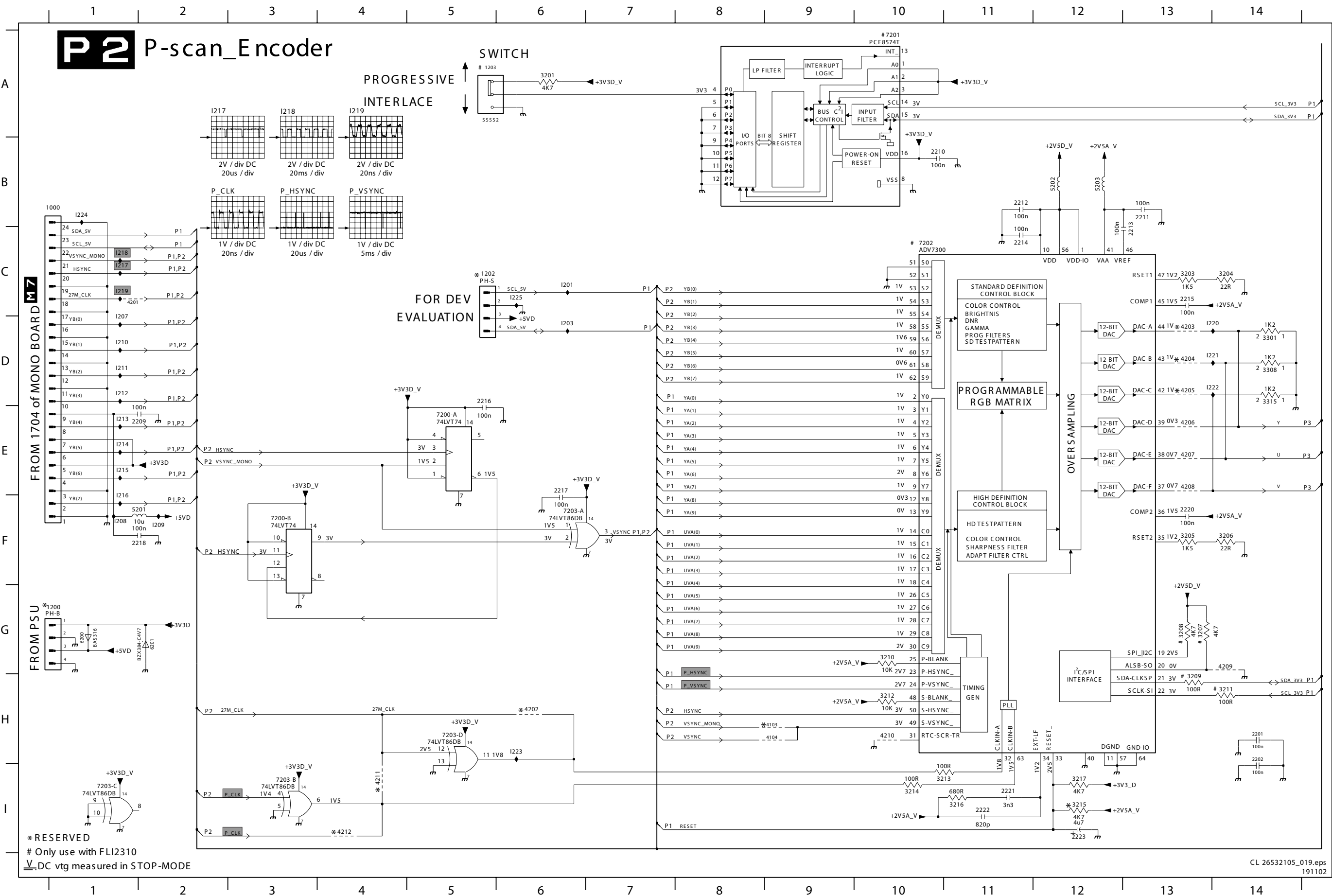


11 12 13 14 15 16 17 18 19 20 21 22

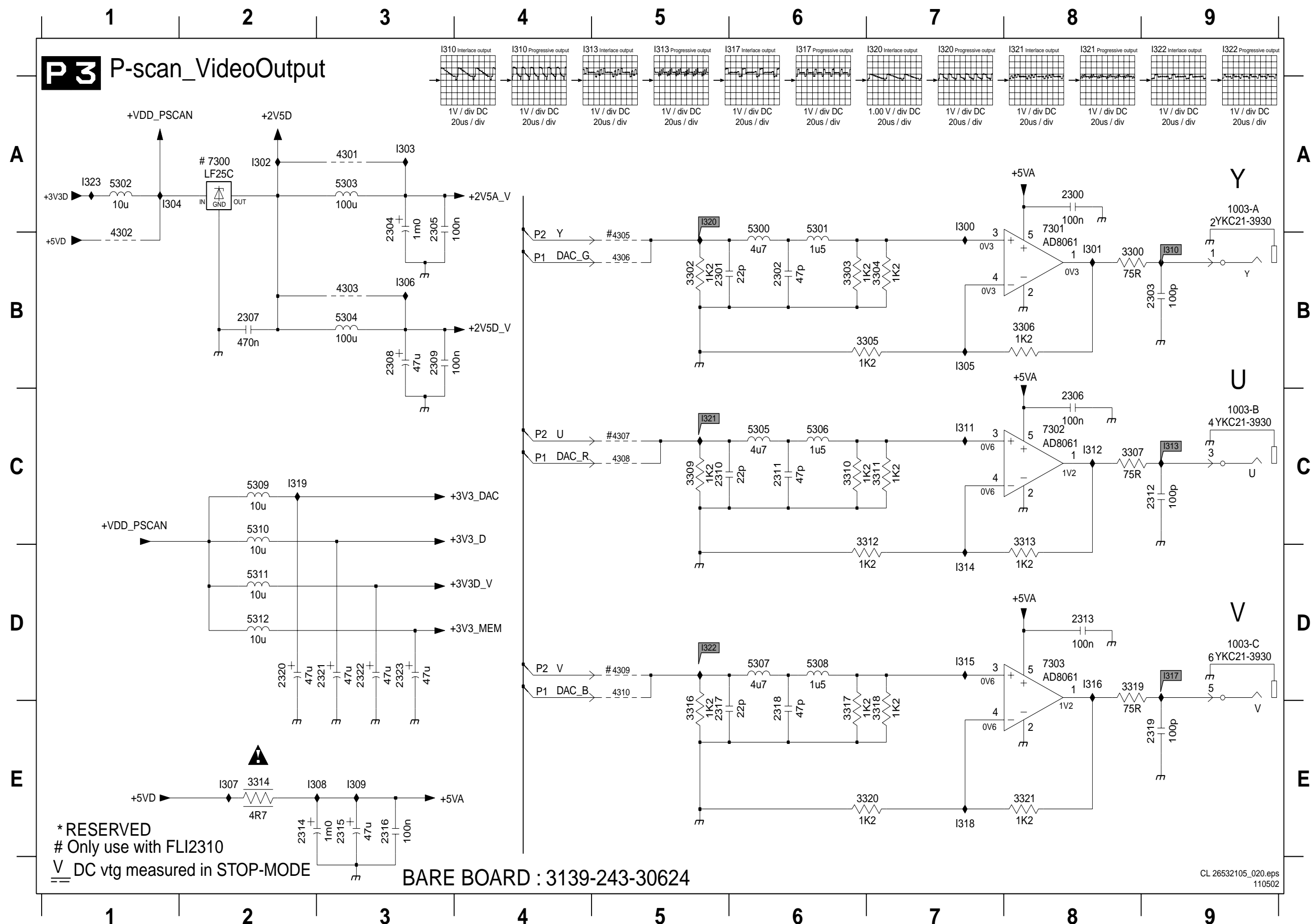


CL 26532105_16b.eps
280802

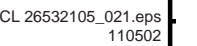
P-Scan Board: Encoder



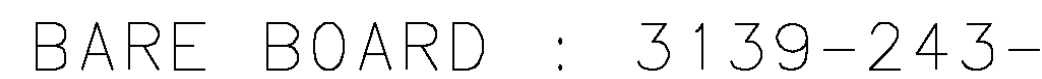
P-Scan Board: Video Output



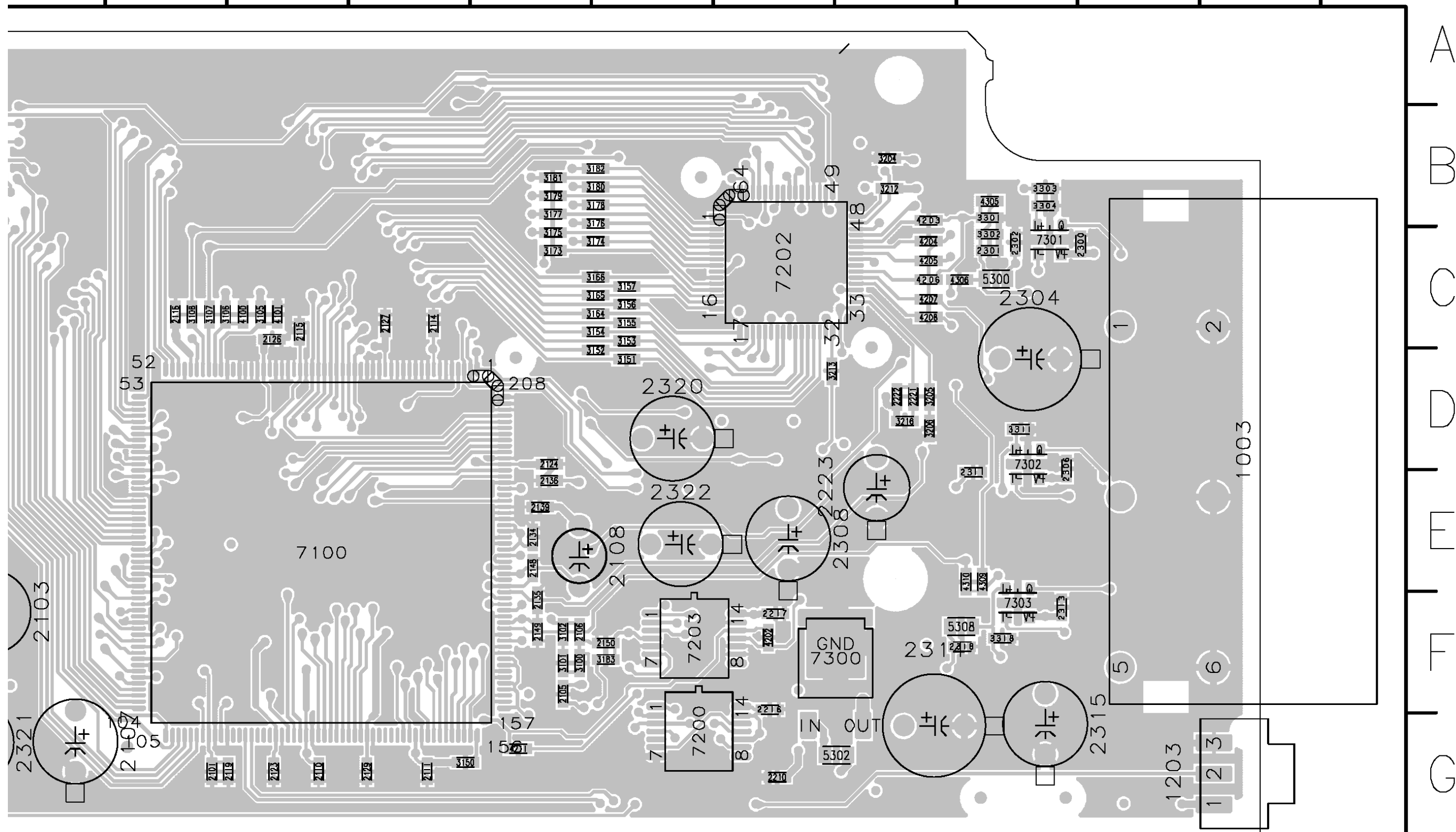
1000	E3	2114	C10	2140	B4	2223	E13	2322	E12	3114	B6	3128	C6	3142	D5	3156	C12	3176	B12	3301	B15	4208	C14	7101	C4
1003	D17	2115	C9	2144	B4	2300	C16	2323	D4	3115	D5	3129	D5	3143	D5	3157	C12	3177	B11	3302	C15	4211	G11	7103	G6
1200	G5	2116	C8	2145	B4	2301	C15	3100	F11	3116	B7	3130	D4	3144	D5	3158	C6	3178	B12	3303	B15	4305	B15	7200	G12
1202	C2	2119	G9	2146	B3	2302	C15	3101	F11	3117	D5	3131	D4	3145	C6	3159	C6	3179	B11	3304	B15	4306	C15	7202	C13
1203	G16	2123	G9	2148	E11	2304	C15	3102	F11	3118	D5	3132	D4	3146	D4	3160	B6	3180	B12	3311	D15	4309	E15	7203	F12
2100	F6	2124	D11	2149	F11	2306	D15	3105	C9	3119	B6	3133	C6	3147	D4	3161	B6	3181	B11	3318	F15	4310	E15	7300	F14
2101	G8	2126	C9	2150	F12	2308	E14	3106	C8	3120	C6	3134	C6	3148	D5	3162	B6	3182	B12	4100	C9	5100	F5	7301	C15
2103	F7	2127	C10	2201	A3	2311	E15	3107	C8	3121	C6	3135	D4	3149	D4	3163	C6	3183	F12	4101	C9	5101	F6	7302	D15
2105	F11	2129	G10	2202	G2	2313	F15	3108	C8	3122	D6	3136	D4	3150	G10	3164	C12	3204	B14	4202	F13	5300	C15	7303	F15
2106	F11	2133	B5	2210	G13	2314	F14	3109	B7	3123	D6	3137	D4	3151	D12	3165	C12	3205	D14	4203	B14	5302	G14		
2107	G8	2134	E11	2216	F13	2315	G16	3110	B6	3124	D6	3138	C6	3152	D12	3166	C12	3206	D14	4204	C14	5308	F15		
2108	E12	2135	F11	2217	F13	2318	F15	3111	B7	3125	D6	3139	D4	3153	C12	3173	C11	3212	B14	4205	C14	5310	F6		
2110	G9	2136	E11	2221	D14	2320	D12	3112	B7	3126	C6	3140	C6	3154	C12	3174	C12	3213	D13	4206	C14	6201	G5		
2111	G10	2139	E11	2222	D14	2321	G7	3113	B6	3127	C6	3141	D4	3155	C12	3175	C11	3216	D14	4207	C14	7100	E9		



2 3 4 5 6 7 8 9 10 11 12 1



7 8 9 10 11 12 13 14 15 16 17

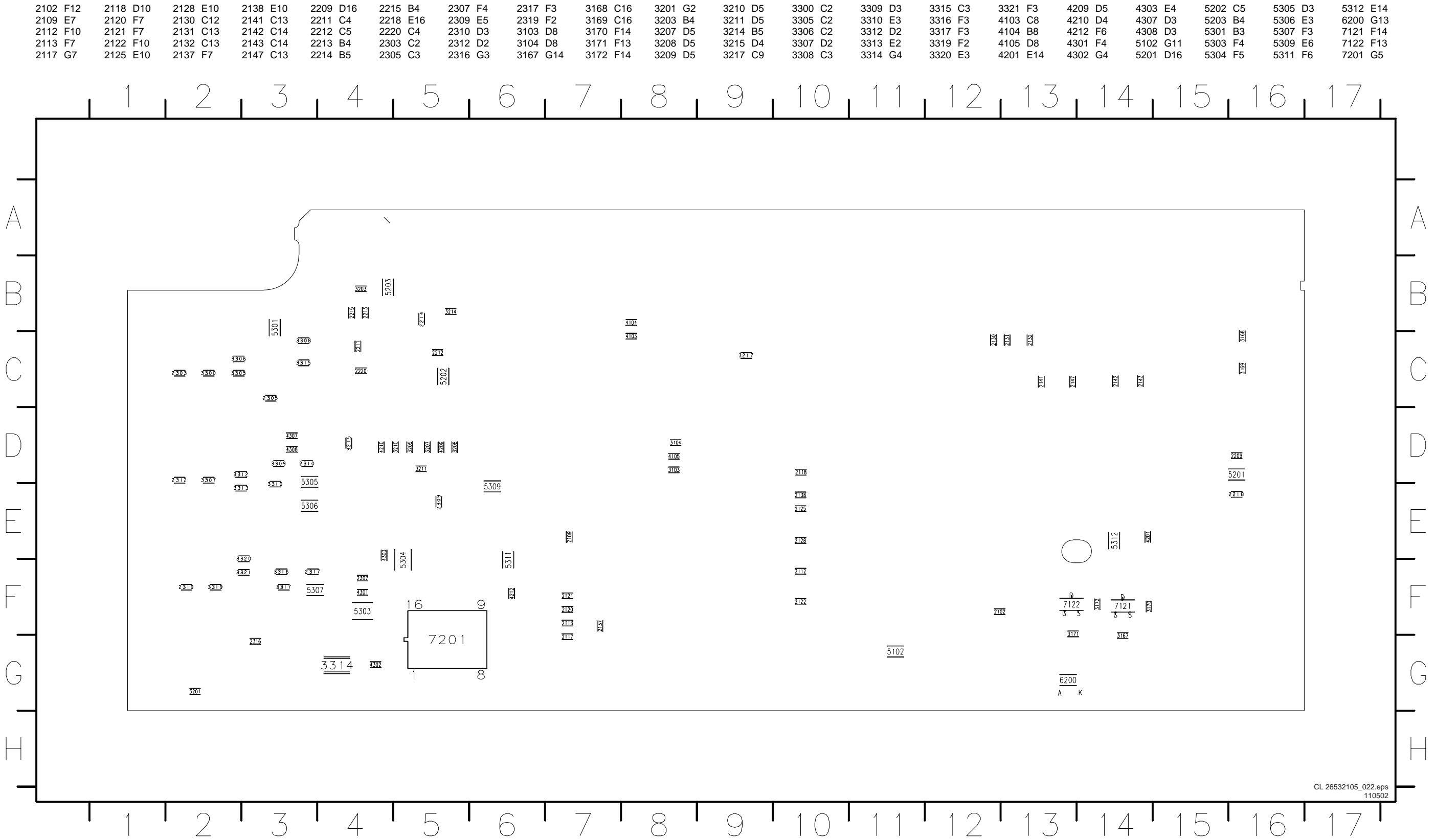


BARE BOARD : 3139-243-30624

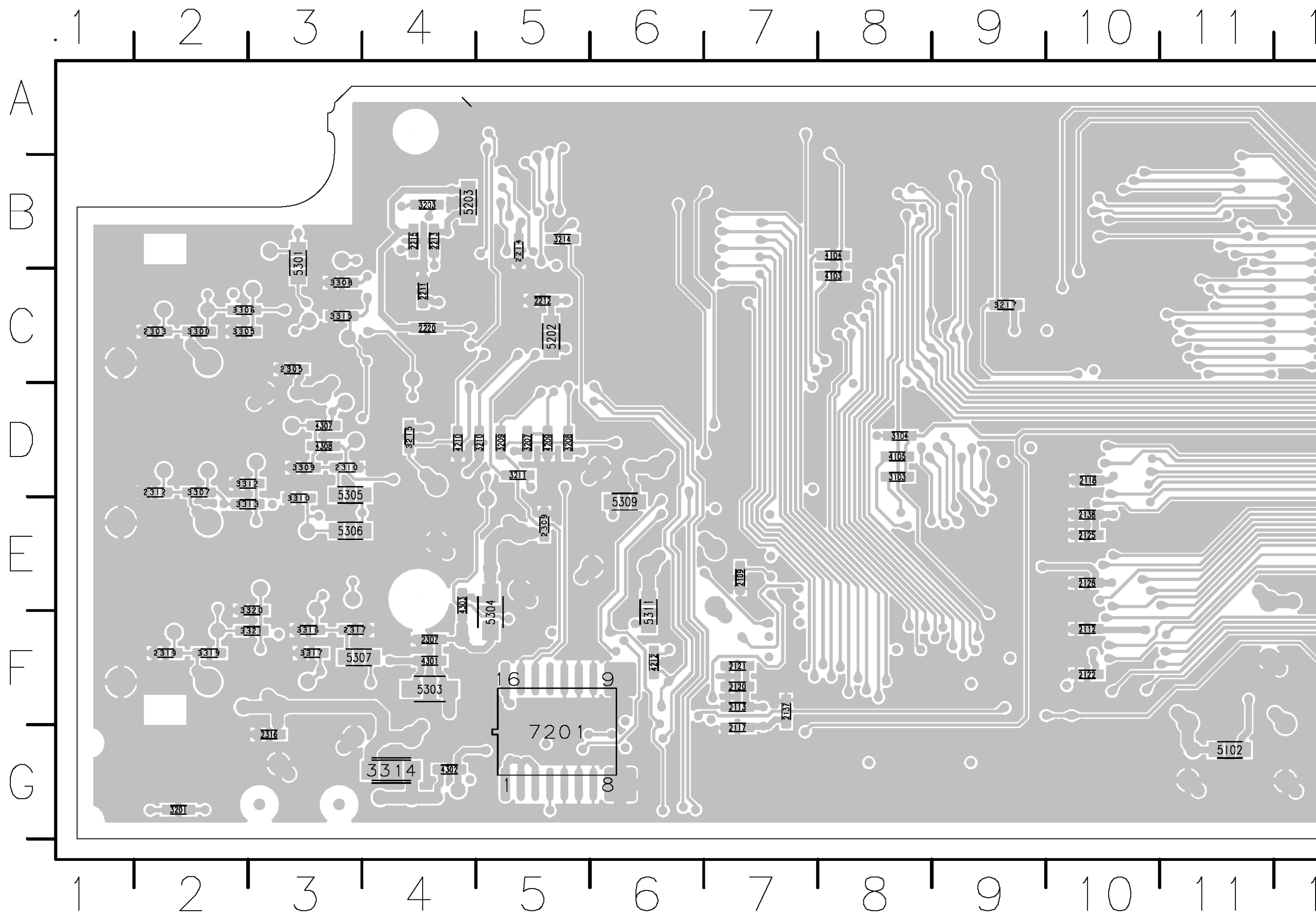
CL 26532105_21a.eps
110502

7 8 9 10 11 12 13 14 15 16 17

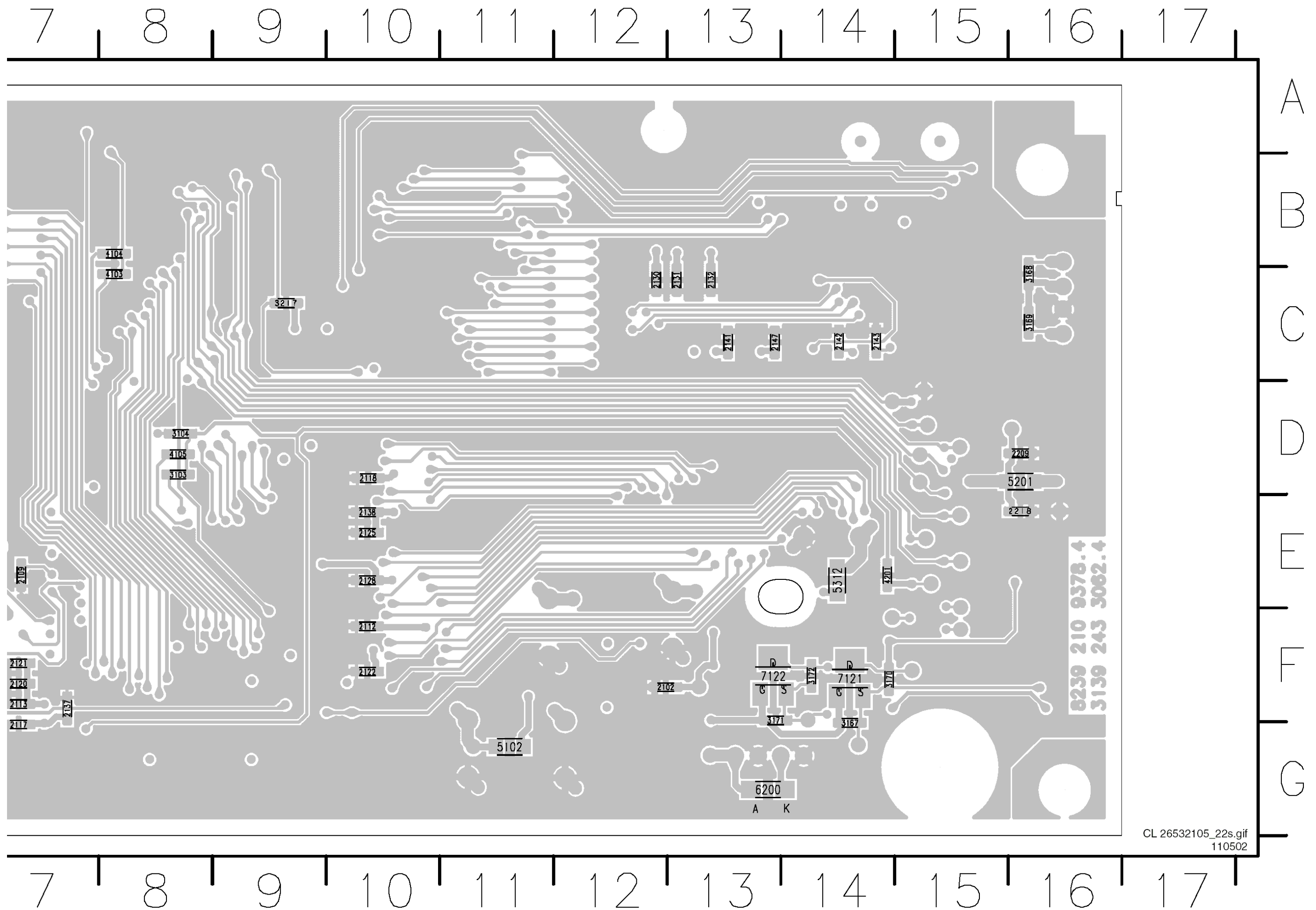
Layout P-Scan Board (Overview Bottom Side)



Layout P-Scan Board (Part 1 Bottom Side)



Layout P-Scan Board (Part 2 Bottom Side)



[illegible][illegible]

Pay-out Standby Board (Bottom Side)

A

B

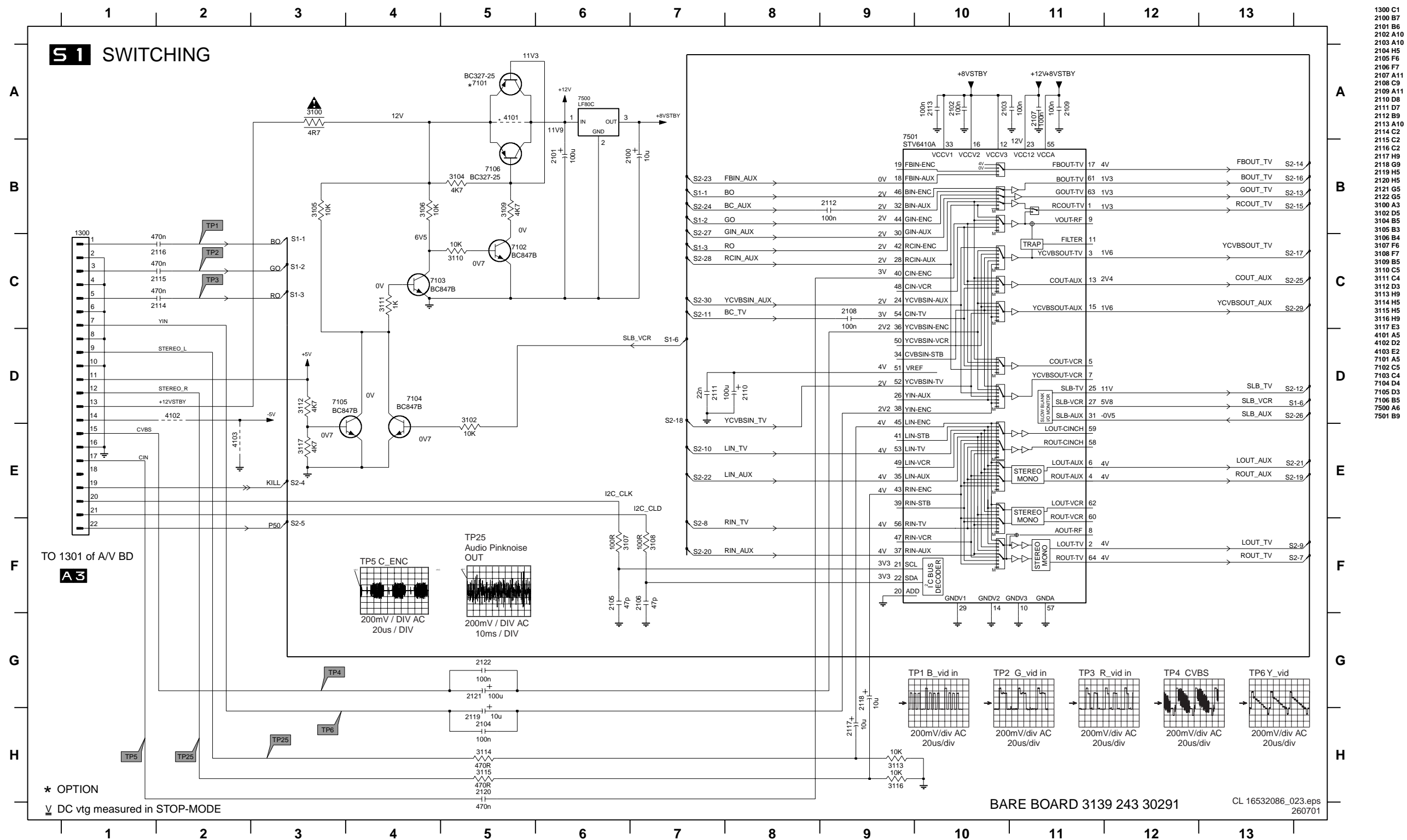
1 2 3

3200

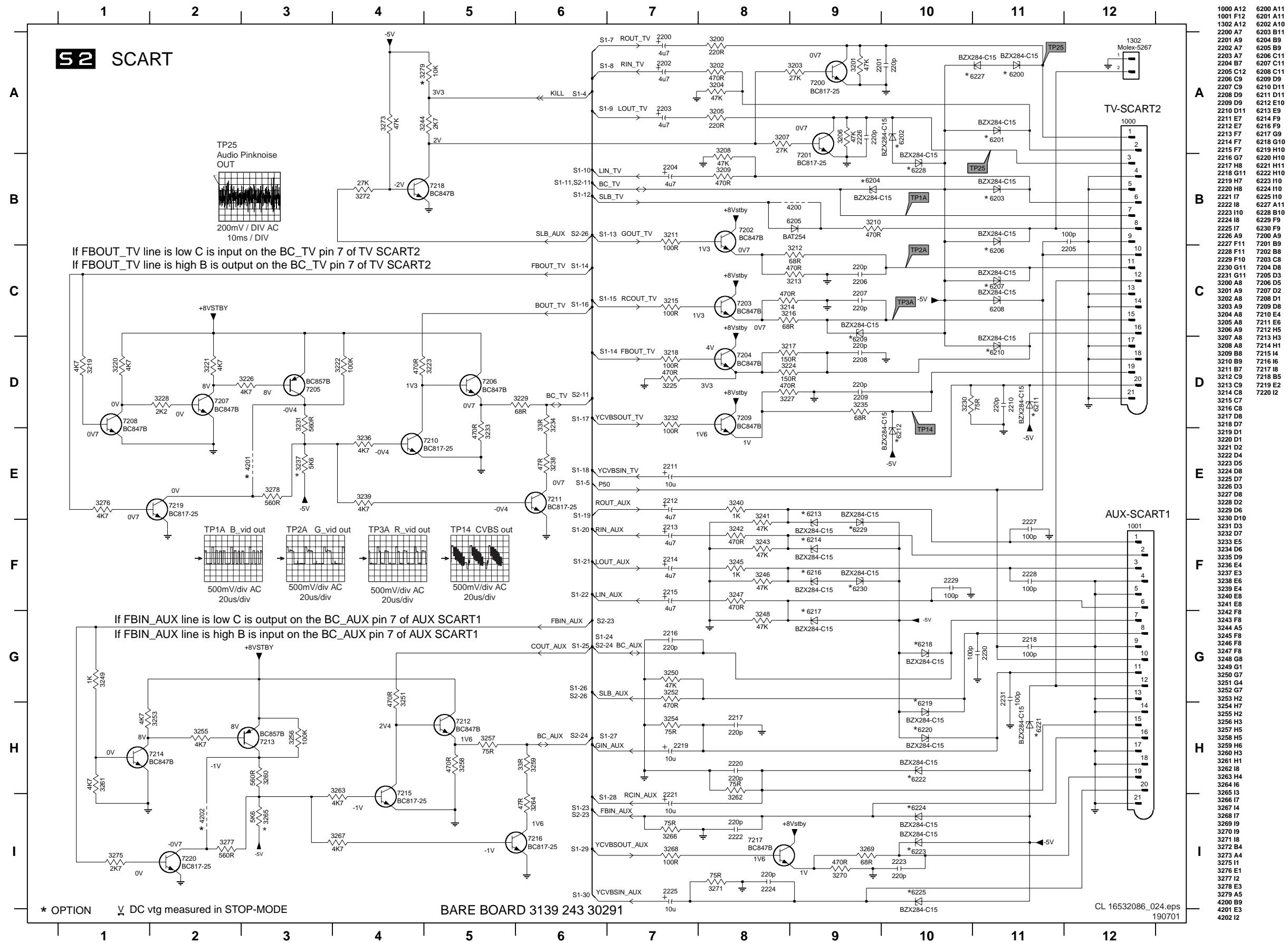
3139 243 30491 01
8239 210 93051 01

CL 26532105_026.eps
290802

Double SCART Panel: Switching (Only for EU models)



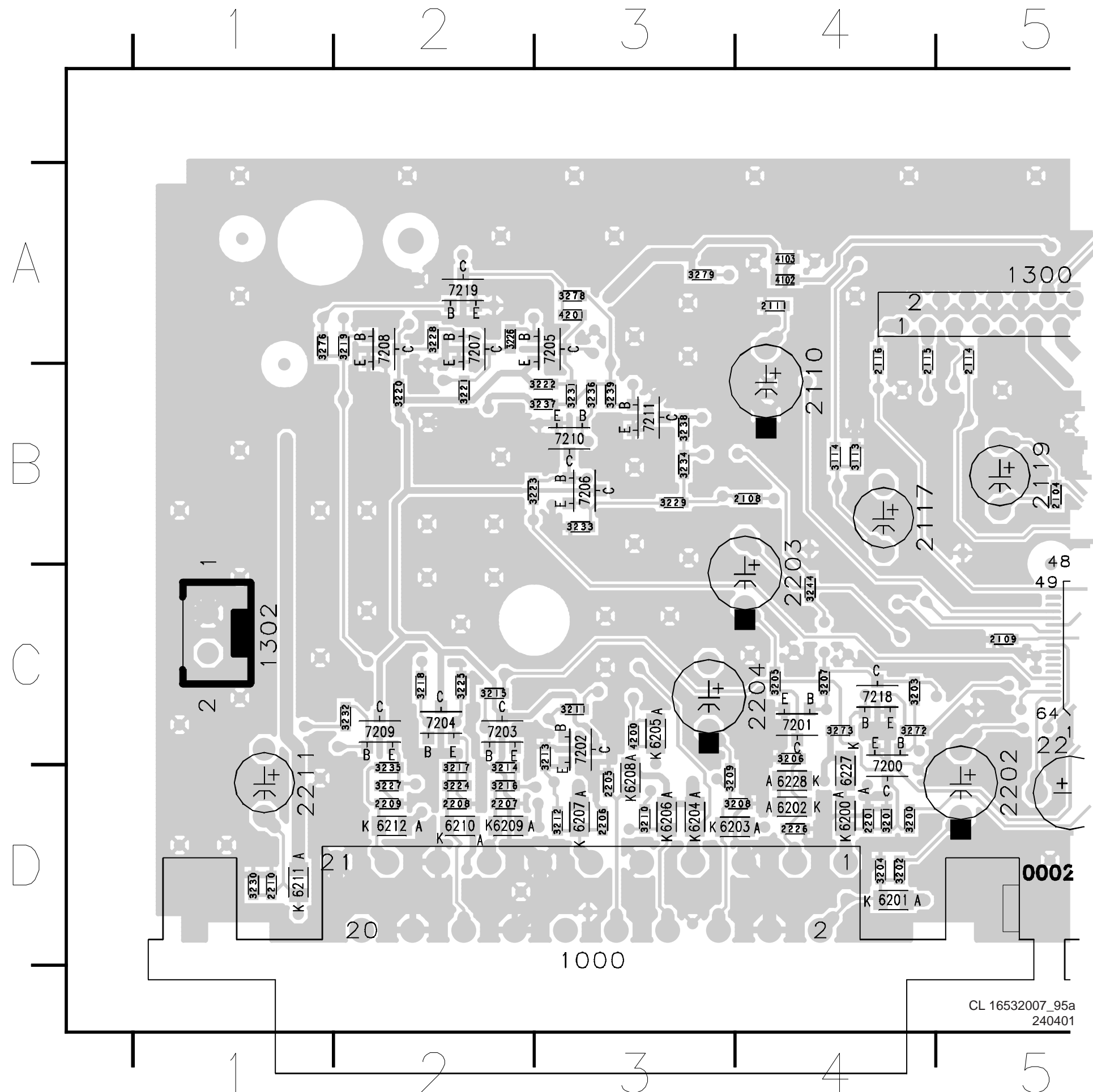
Double SCART Panel: Switching (Only for EU models)



Layout Double SCART panel (Only for EU models) (Overview Top Side)

10000	D3	21116	A4	22213	C	31102	A10	32205	C4	32225	C2	32445	C	32266	B9	42001	B10	62200	D4	62221	D7	72005	A3
10001	D7	21117	B4	22214	C	31103	A9	32206	C4	32226	A2	32446	C	32267	B9	42002	B10	62201	D4	62222	D7	72006	B3
13000	A5	22218	B5	22215	C	32204	A9	33200	C4	33222	A2	33244	C	33264	B9	42003	B10	62202	D4	62223	D7	72007	A2
21000	B7	22219	A6	22216	C	32205	A8	33201	C4	33223	A3	33245	C	33265	B9	42004	B10	62203	D4	62224	D7	72008	C2
22000	A8	22220	B6	22217	C	32206	A9	33202	C4	33224	A4	33246	C	33266	B9	42005	B10	62204	D4	62225	D7	72009	C3
22001	B8	22221	A7	22218	C	32207	A8	33203	C4	33225	A5	33247	C	33267	B9	42006	B10	62205	D4	62226	D7	72010	B3
22002	A9	22222	B7	22219	C	32208	A9	33204	C4	33226	A6	33248	C	33268	B9	42007	B10	62206	D4	62227	D7	72011	A2
22003	B9	22223	A8	22220	C	32209	A8	33205	C4	33227	A7	33249	C	33269	B9	42008	B10	62207	D4	62228	D7	72012	C3
22004	A4	22224	B9	22221	C	32210	A9	33206	C4	33228	A8	33250	C	33270	B9	42009	B10	62208	D4	62229	D7	72013	B3
22005	B4	22225	A5	22222	C	32211	A8	33207	C4	33229	A9	33251	C	33271	B9	42010	B10	62209	D4	62230	D7	72014	A2
22006	A5	22226	B5	22223	C	32212	A9	33208	C4	33230	A10	33252	C	33272	B9	42011	B10	62210	D4	62231	D7	72015	C3
22007	B5	22227	A6	22224	C	32213	A8	33209	C4	33231	A11	33253	C	33273	B9	42012	B10	62211	D4	62232	D7	72016	B3
22008	A6	22228	B6	22225	C	32214	A9	33210	C4	33232	A12	33254	C	33274	B9	42013	B10	62212	D4	62233	D7	72017	C4
22009	B6	22229	A7	22226	C	32215	A8	33211	C4	33233	A13	33255	C	33275	B9	42014	B10	62213	D4	62234	D7	72018	A7
22010	A7	22230	B7	22227	C	32216	A9	33212	C4	33234	A14	33256	C	33276	B9	42015	B10	62214	D4	62235	D7	72019	C5
22011	B7	22231	A8	22228	C	32217	A8	33213	C4	33235	A15	33257	C	33277	B9	42016	B10	62215	D4	62236	D7	72020	B10
22012	A8	22232	B8	22229	C	32218	A9	33214	C4	33236	A16	33258	C	33278	B9	42017	B10	62216	D4	62237	D7	72021	C5
22013	B8	22233	A9	22230	C	32219	A8	33215	C4	33237	A17	33259	C	33279	B9	42018	B10	62217	D4	62238	D7	72022	A7
22014	A9	22234	B9	22231	C	32220	A9	33216	C4	33238	A18	33260	C	33280	B9	42019	B10	62218	D4	62239	D7	72023	C5
22015	B9	22235	A10	22232	C	32221	A8	33217	C4	33239	A19	33261	C	33281	B9	42020	B10	62219	D4	62240	D7	72024	C5
22016	A10	22236	B10	22233	C	32222	A9	33218	C4	33240	A20	33262	C	33282	B9	42021	B10	62220	D4	62241	D7	72025	C5
22017	B10	22237	A11	22234	C	32223	A8	33219	C4	33241	A21	33263	C	33283	B9	42022	B10	62221	D4	62242	D7	72026	C5
22018	A11	22238	B11	22235	C	32224	A9	33220	C4	33242	A22	33264	C	33284	B9	42023	B10	62222	D4	62243	D7	72027	C5
22019	B11	22239	A12	22236	C	32225	A8	33221	C4	33243	A23	33265	C	33285	B9	42024	B10	62223	D4	62244	D7	72028	C5
22020	A12	22240	B12	22237	C	32226	A9	33222	C4	33244	A24	33266	C	33286	B9	42025	B10	62224	D4	62245	D7	72029	C5
22021	B12	22241	A13	22238	C	32227	A8	33223	C4	33245	A25	33267	C	33287	B9	42026	B10	62225	D4	62246	D7	72030	C5
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22027	B15	22247	A16	22244	C	32233	A8	33229	C4	33251	A31	33273	C	33293	B9	42032	B10	62231	D4	62252	D7	72036	C5
22028	A16	22248	B16	22245	C	32234	A9	33230	C4	33252	A32	33274	C	33294	B9	42033	B10	62232	D4	62253	D7	72037	C5
22029	B16	22249	A17	22246	C	32235	A8	33231	C4	33253	A33	33275	C	33295	B9	42034	B10	62233	D4	62254	D7	72038	C5
22030	A17	22250	B17	22247	C	32236	A9	33232	C4	33254	A34	33276	C	33296	B9	42035	B10	62234	D4	62255	D7	72039	C5
22031	B17	22251	A18	22248	C	32237	A8	33233	C4	33255	A35	33277	C	33297	B9	42036	B10	62235	D4	62256	D7	72040	C5
22032	A18	22252	B18	22249	C	32238	A9	33234	C4	33256	A36	33278	C	33298	B9	42037	B10	62236	D4	62257	D7	72041	C5
22033	B18	22253	A19	22250	C	32239	A8	33235	C4	33257	A37	33279	C	33299	B9	42038	B10	62237	D4	62258	D7	72042	C5
22034	A19	22254	B19	22251	C	32240	A9	33236	C4	33258	A38	33280	C	33300	B9	42039	B10	62238	D4	62259	D7	72043	C5
22035	B19	22255	A20	22252	C	32241	A8	33237	C4	33259	A39	33281	C	33301	B9	42040	B10	62239	D4	62260	D7	72044	C5
22036	A20	22256	B20	22253	C	32242	A9	33238	C4	33260	A40	33282	C	33302	B9	42041	B10	62240	D4	62261	D7	72045	C5
22037	B20	22257	A21	22254	C	32243	A8	33239	C4	33261	A41	33283	C	33303	B9	42042	B10	62241	D4	62262	D7	72046	C5
22038	A21	22258	B21	22255	C	32244	A9	33240	C4	33262	A42	33284	C	33304	B9	42043	B10	62242	D4	62263	D7	72047	C5
22039	B21	22259	A22	22256	C	32245	A8	33241	C4	33263	A43	33285	C	33305	B9	42044	B10	62243	D4	62264	D7	72048	C5
22040	A22	22260	B22	22257	C	32246	A9	33242	C4	33264	A44	33286	C	33306	B9	42045	B10	62244	D4	62265	D7	72049	C5
22041	B22	22261	A23	22258	C	32247	A8	33243	C4	33265	A45	33287	C	33307	B9	42046	B10	62245	D4	62266	D7	72050	C5
22042	A23	22262	B23	22259	C	32248	A9	33244	C4	33266	A46	33288	C	33308	B9	42047	B10	62246	D4	62267	D7	72051	C5
22043	B23	22263	A24	22260	C	32249	A8	33245	C4	33267	A47	33289	C	33309	B9	42048	B10	62247	D4	62268	D7	72052	C5
22044	A24	22264	B24	22261	C	32250	A9	33246	C4	33268	A48	33290	C	33310	B9	42049	B10	62248	D4	62269	D7	72053	C5
22045	B24	22265	A25	22262	C	32251	A8	33247	C4	33269	A49	33291	C	33311	B9	42050	B10	62249	D4	62270	D7	72054	C5
22046	A25	22266	B25	22263	C	32252	A9	33248	C4	33270	A50	33292	C	33312	B9	42051	B10	62250	D4	62271	D7	72055	C5
22047	B25	22267	A26	22264	C	32253	A8	33249	C4	33271	A51	33293	C	33313	B9	42052	B10	62251	D4	62272	D7	72056	C5
22048	A26	22268	B26	22265	C	32254	A9	33250	C4	33272	A52	33294	C	33314	B9	42053	B10	62252	D4	62273	D7	72057	C5
22049	B26	22269	A27	22266	C	32255	A8	33251	C4	33273	A53	33295	C	33315	B9	42054	B10	62253	D4	62274	D7	72058	C5
22050	A27	22270	B27	22267	C	32256	A9	33252	C4	33274	A54	33296	C	33316	B9	42055	B10	62254	D4	62275	D7	72059	C5
22051	B27	22271	A28	22268	C	32257	A8	33253	C4	33275	A55	33297	C	33317	B9	42056	B10	62255	D4	62276	D7	72060	C5
22052	A28	22272	B28	22269	C	32258	A9	33254	C4	33276	A56	33298	C	33318	B9	42057	B10	62256	D4	62277	D7	72061	C5
22053	B28	22273	A29	22270	C	32259	A8	33255	C4	33277	A57	33299	C	33319	B9	42058	B10	62257	D4	62278	D7	72062	C5
22054	A29	22274	B29	22271	C	32260	A9	33256	C4	33278	A58	33300	C	33320	B9	42059	B10	62258	D4	62279	D7	72063	C5
22055	B29	22275	A30	22272	C	32261	A8	33257	C4	33279	A59	33301	C	33321	B9	42060	B10	62259	D4	62280	D7	72064	C5
22056	A30	22276	B30	22273	C	32262	A9	33258	C4	33280	A60	33302	C	33322	B9	42061	B10	62260	D4	62281	D7	72065	C5
22057	B30	22277	A31	22274	C	32263	A8	33259	C4	33281	A61	33303	C	33323	B9	420							

Layout Double SCART panel (Only for EU models) (Part 1 Top Side)



1 1



The secondary voltage regulating circuit comprises of optocoupler IC2 (7190), which isolates the error signal from the control IC on the primary side, and a reference component IC3 (7290, TL431).

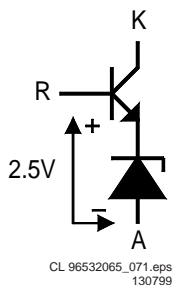


Figure 9-2 TL431

This reference component has two functions:

- A very stable and accurate reference diode
- A high gain amplifier.

When the output voltage increases (due to a reduction in the load), the voltage across $R_{SENSE-2}$ (R3290/R3291) increases to above the internal reference voltage of 2.5V. The TL431 will conduct and the current through the opto-coupler will increase. This results in an increase of the voltage at pin 4 of IC1, which will reduce the 'on' time of Q1 (FET 7125). In the event of an output voltage decrease (due to an increase in the load), the control circuit will operate in the opposite way.

Primary Current Sensing

The current through FET Q1 will result in a voltage drop across $R_{SENSE-1}$ (R3120-23). This line goes to pin 11 of IC7130, which is the current sense input. The higher the input voltage, the more the primary current is limited. In this way, the maximum output power of the power supply is limited.

Under-voltage Protection

If the supply voltage at pin 13 of IC7130 drops below 7.2V (typical), e.g. due to a shorted secondary voltage or excessive load, the drive pulse at pin 12 is disabled and the controller will switch 'off'.

Over-voltage Protection

An internal over-voltage protection circuitry continuously monitors the V_{CC} pin. If, after start-up, this voltage exceeds 40V, the internal latch circuit is triggered, the output buffer is disabled, and the SMPS goes into over-voltage protection. Now a complete restart sequence is required.

Note: If the event of the over-voltage situation remains present, the SMPS will go in sequence of protection, start-up, protection and the cycle repeats. This effect is highly audible.

9.3 Loader/Mono Board (for diagrams see SD4.00SA_CH Service Manual)

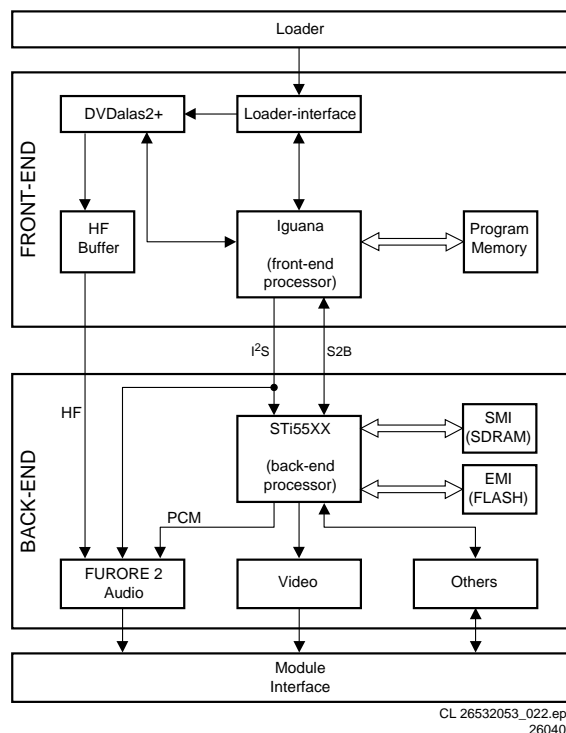


Figure 9-3 Block Diagram Loader/Mono board

The SD4.00_SA_CH (SACHI_4) is the 2nd generation Philips Architectural Standard Design of SACD mono board based on Fureore 2, and will be used in the new generation of SACD players. It is designed in a multi-task way so that it can support the following optional main functions:

- SD4.00_SA_CH: Support SACD player with 5-disc changer.
- SD4.00_SA: Support SACD player with single-disc.
- SD4.00_CH: Support DVD player with 5-disc changer but without SACD playback.
- SD4.00_SA_I2C: Support SACD player with single-disc and I2C slave.

The SD4.00_SA_CH (SACHI_4) module consist of the following key components:

1. **OPU:** Mercury 2 Loader VAL6011/14 (slim type) for a single-disc SACD player, or DVD VAM6001/14 mechanism for a 5-disc SACD changer.
2. **Front-end:** M2 Basic Engine.
3. **Back-end:** DVD Host Processor STi55xx and Fureore 2 SACD DSD/DST decoder.
4. **Power supply:** To convert the PSU voltages to the correct values.
5. **Reset circuit:** This circuit that the booting of the several devices on the mono board takes place in the correct order.

9.3.1 The Optical Pick-up Unit (OPU)

The Mercury 2 Loader has an optical unit consisting of two lasers:

- One for CD with a wavelength of 780 nm.
- One for DVD with a wavelength of 650 nm.

The TZA1033 (item 7105) controls the data from these lasers, and the supply to them.

9.3.2 Front-end: the Servo Part

The front-end consists of:

- The Loader interface

- The Servo Processor/Decoder
- The Interface/Program Memory

The Loader Interface

The TZA1033HL/K2 (or DVDALAS2plus, item 7105) is an analogue pre-processor and laser supply circuit. It contains data amplifiers and several options for radial tracking and focus control.

It is possible to optimise the dynamic range of this pre-amp/processor combination for the LF servo and RF data paths. The gain in both channels is separately programmable. This will guarantee an optimal playability for all kind of discs.

Also, a dual laser supply is implemented, with fully automatic laser control including stabilisation and an ON/OFF switch, plus a separate supply pin for power efficiency.

The Servo Processor/Decoder

In the SD3.0 module, the servo signals were fed to the MACE2 servo processor, while the HF output signal was fed to the SAA7335 decoder. In the new SD4.0SA_CH module, these ICs are combined into one chip: the SAA7812 Iguana.

This chip contains the following blocks: channel decoder, block decoder, servo processor, and microcontroller.

The servo circuit in the SAA7812 (item 7207) takes care of the servo controls.

In a CD system, there are some twelve control loops active. About six of them are needed to adjust the servo error signals that is once per disc rotation. It also adjusts offsets, signal amplitudes, and loop gains (AGCs), to enlarge system robustness and to avoid expensive potentiometer adjustments in production.

The other six loops determine the laser spot position on the disc in the radial, axial (focus), and tangential directions. It also has to take care that the spot accesses a required position as fast as possible. This access system consists of two parts, namely the actuator and the sled, which are (within a certain range) mechanically and electrically independent. Therefore, during an access, the servo has to control as well the actuator as the sled.

The analogue signals, from the diode pre-processor, are converted into a digital representation using A/D converters. For the communication between the host processor (STi55xx) and the servo processor, the S2B bus is used. This bus supports full-duplex asynchronous communication.

The SAA7812 is also a combined CD/DVD compatible decoding device. The device operates with built in hardware for CD/DVD error correction and de-interleaving operations. It decodes EFM or EFM+HF signals directly from the laser pre-amplifier, including analogue front-end, PLL data recovery, demodulation, and error correction.

Its analogue front-end input (the channel decoder), converts the HF input signal to the digital domain via an 8-bit ADC, preceded by an AGC circuit to obtain the optimum performance from the converter. An external resonator clocks this block. This subsystem recovers the data from the channel stream. It corrects asymmetry, performs noise filtering and equalisation, and finally recovers the bit clock and data from the channel using a digital PLL.

The demodulator part detects the frame synchronisation signals and decodes the EFM (14 bit) and EFM+ (16 bit) data and sub-code words into 8-bit symbols. Via the serial output interface, the I²S data (audio and video) go to the DVD decoder STi55xx.

The spindle-motor interface provides both motor control signals from the demodulator and, in addition, contains a tachometer loop that accepts tachometer pulses from the motor unit. They drive the motor IC (BA6665FM, item 7300).

The SAA7812 has two independent microcontroller interfaces. The first is a serial I²C-bus and the second is a standard 8-bit

multiplexed parallel interface. Both of these interfaces provide access to 32 8-bit registers for control and status.

The Interface/Program Memory

The interface between front-end (SAA7812) and back-end (STi55xx) is via:

- I²S bus (BCLK, DATA, WCLK, FLAG, SYNC and V4).
- S2B bus (RXD_S2B, TXD_S2B, CPR_S2B and SUR_S2B).
- Miscellaneous I/O ports (RSTNF= front-end reset, EANF= front-end processor boot select).

Service tip: These lines contain series resistors (47 or 100 Ω) for easy hardware debugging, and for EMC/noise reduction of the high-speed I²S lines.

The front-end processor SAA7812 (Iguana) has two boot modes: normal boot from flash memory, or serial mode. The boot selection is via the EANF pin. The Iguana, samples the EANF signal level once during boot-up. Once boot-up is completed, this pin is no longer used for this purpose. However, in the SD4.0SA_CH circuit, the EANF is also connected to the flash memory. Therefore, when this pin is LOW, the lower 1 Mbits of the memory is accessible. Conversely, when this pin is HIGH, the upper 1 Mbits is accessible.

Under front-end normal operation, the program memory (less than 1 Mbits in size) should reside in the lower bank. Therefore, the EANF pin should be LOW at all times. Since the actual flash memory used is 2 Mbits, the upper 1 Mbits is unused. This area is reserved for possible use by the front-end self-diagnostic software, or flash download application.

9.3.3 Back-end: the Digital Part

The back-end consists of:

- DVD back-end processor
- SACD DSD processor
- Audio output
- Video output
- Clock factory
- Miscellaneous

DVD Back-end Processor

The SD4.0SA_CH is designed for the STi55xx family. Some of the DVD related features of these ICs are:

Processor overview

Function	STi5580	STi5588	STi5519
Basic CD/VCD/DVD decoding	X	X	X
Extra 2-channel of I2S output (PCMDATA3)	X	X	
Karaoke	X	X	
DTS	X	X	
Audio post processing (equalizer, level meter, etc)		X	
DVD audio		X	
Progressive scan at analog video output		X	

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020902

Figure 9-4 Processor overview

The STi5580 has the same architecture as the STi5508 (used in earlier DVD generations), and is pin-to-pin compatible.

It works on 3.3 V (V_{DD}), and comprises the following functions:

- Video decoder, which supports MPEG1 and MPEG2.
- Audio decoder that supports AC-3, MPEG1, MPEG2, DTS, PCM, S/PDIF, and MP3.
- PAL/NTSC video encoder with simultaneously Y/C, CVBS, and RGB/YUV outputs.
- The video encoder supports Closed Caption and allows MacroVision 7.0/6.1.
- Full screen On Screen Display (OSD) generator.
- Three on-chip PLLs to generate all necessary clocks (as reference the 27 MHz video clock is used).

Input

Input data comes from the I²S-bus. The front-end interface of this device, accepts DVD, CD and CD-DA information.

Signal Processing

For video, the input data stream is decoded to the appropriate MPEG, Sub Picture, and OSD data streams, after which they are fed to the PAL/NTSC encoder. This cell will convert the digital MPEG/Sub Picture/OSD stream into a standard base band signal and into RGB components. It handles interlaced and non-interlaced data, can perform CC/TXT encoding, and allows MacroVision copy protection.

For audio, the processing cell is a fully compatible DTS, Dolby Digital (AC-3), MPEG1, MPEG2, PCM decoder, capable of decoding 5.1 and 2 channel streams.

Output

For video, six analogue output pins are available on which CVBS, S-VHS (Y/C), and RGB signals are present. They go, via a buffer, to connector 1703. As an option, a digital YUV output is available at connector 1704.

External Memory

The STi55xx family is capable of accessing external memory via three buses:

- **The enhanced memory interface (EMI).** This interface is configurable and can be used to access Flash, ROM, and various flavours of DRAM.
- **The shared memory interface (SMI).** The SMI is only used to access SDRAM. The SMI is connected to a 64Mbits (4M x 16bit) 7.5ns SDRAM (item 7500). The SDRAM has the following functions:
 - It is used by the MPEG video decoder as a frame buffer,
 - It holds the software and the variables used by it.
- **The I²C bus.** Via this bus, the NVRAM (or EEPROM) is accessible. This memory is used to store user settings, player settings, and region code. As the STi55xx I/O-lines are potentially unable handle 5V inputs, a voltage level shifter is foreseen for all I²C-busses. This circuit will isolate the STi55xx I²C ports (3.3V) from the system I²C bus (5V). See figure below.

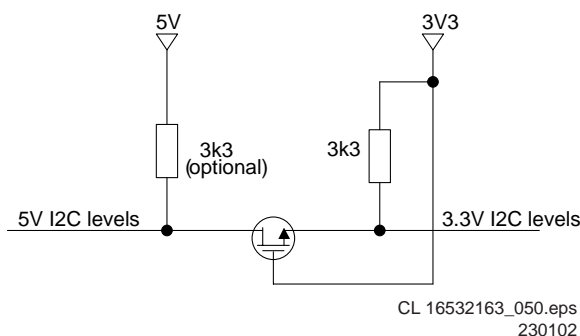


Figure 9-5 I²C voltage level shifter

The SACD DSD processor

The Furore-IC is a one-chip design, containing all the hardware that is required for SACD processing. It is intended to interface with the STixx-family DVD video decoders.

The Furore-IC contains a memory interface to support an external 16 or 64 Mbit SDRAM.

During SACD application, the STi55xx serves as a host, whereby the Furor is controlled via the EMI interface. The Furor processing part is not used during all other play modes. In these modes, the PCM audio signals are fed through the Furor to the appropriate DAC.

Block diagram

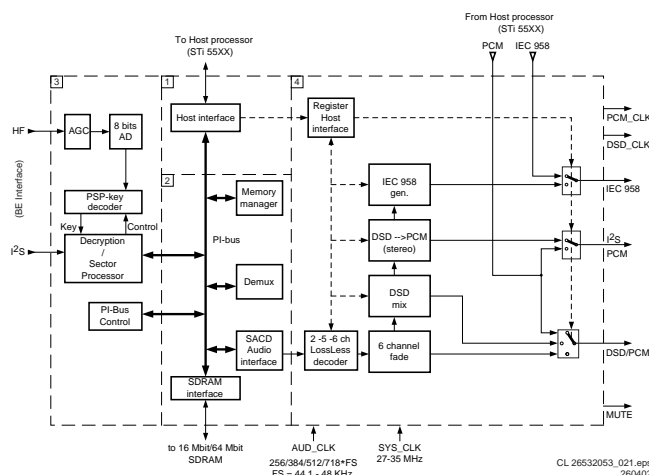


Figure 9-6 Block diagram Furore

We can divide the Furore-IC in four main parts (see block diagram):

1. **Host interface.** This is the link between the host bus and the internal registers and memory bus. It also supplies the general reset signal (HW and SW) and the interrupt signals.
2. **Data processing.** All modules and peripherals in this part are connected to a so-called PI-bus. It is beyond the scope of this manual to go more in detail on this subject.
3. **Copy protection.** On every SACD disc, a PSP-signal is recorded. The player can only play a disc if a valid PSP-signal is detected. This PSP-key is recorded, via a special mechanism, in the EFM-signal on the disc. To detect this key, the analogue HF-signal from the optical pick-up unit is fed directly to the Furore-IC. Via an AGC, the signal is fed to an ADC. The digitised HF signal is then fed to a block where key is encrypted. Control of this process is done via the host interface (sector processor).
4. **DSD decoding and post processing.** In this part, all processing is done to generate a DSD and/or an I²S stream (from the de-multiplexed stream coming from the data processing block), in such a way that it can directly be connected to a DA-converter. All processing is done on 384*FS.

Interfaces

- **Basic Engine Interface:**
 - Data input interface. The Basic Engine Interface (I²S) is connected to the output of the SAA7335 (HD61) high speed CD decoder.
 - Analogue HF input. The analogue HF input, coming from the optical pickup unit (OPU), is also fed to the Furore-IC, to extract the copy-protection information PSP (Pit Signal Processing = invisible data is stored on to disc, which is required to decrypt the encrypted content).
- **SDRAM Interface:** The SDRAM interface forms a glue less interface to one 64 Mbit SDRAM device. The interface takes care for the power-up sequence, mode programming and refreshing of the SDRAM devices. This is hard coded in the interface and does not have to be controlled by the host.
- **Audio data input/output Interface:**
 - DSD/PCM combined data output. DSD_PCM: Output intended for a combined 6-channel DSD (SACD) and PCM (DVD-CDDA) DAC. Switching between the PCM data coming from the STi55xx, and the internal generated DSD signals, is done in the Furore IC.
 - Stereo DSD only output. DSD_stereo: 2-channel DSD output with stereo down mix in the case of 5- and 6-channel, and normal stereo in case of 2-channel DSD mode.

- Stereo PCM data output. Two possible stereo sources can be selected as stereo PCM output:
 1. Stereo PCM coming from the STi55xx via the PCM input on Furore.
 2. Stereo or down-mix-PCM derived via a decimation filter from the SACD-DSD signal.
- Digital audio output interface (IEC958). The IEC958 format is intended to connect the DVD736SA to a digital receiver. No DSD signals are defined for IEC958, therefore the 'DSD-->PCM converted' signal is transmitted. Following two types of signals are possible on the digital interface:
 1. IEC958 data coming from the STi55xx.
 2. IEC958 data (stereo or down-mix-PCM) derived via a decimation filter from the SACD-DSD signal.
- Clock + reset input. Two different processing clocks and a reset pulse are needed:
 1. Sys_clk: System clock for data processing part, frequency can be 27 MHz or 768*FS.
 2. 384*FS: Processing clock for LLD and post processing.
 3. RESETn is an asynchronous reset and should be low for at least 1 period of DSD_CLK.

Memory

- **SDRAM.** The size of the SDRAM is 64 Mbit (items 7500 and 7502). It has the following functions:
 - It is used by the MPEG video decoder as a frame buffer,
 - It holds the software and the variables used by it.
- **Flash-ROM.** Two 2MB Flash-ROMs (items 7402 and 7403) hold the DVD firmware, and are controlled by pin 16 (FLASH_OEN) of the STi55xx. It must be able to perform a download (by disk or OS-link) in a Flash-only system.
- **EEPROM.** User settings, player settings, and region code are stored in a 32 Kb I²C EEPROM.

Audio Output

The audio interfaces available in SD4.0SA_CH are I²S and S/PDIF for digital audio output, and I²S karaoke microphone input.

In SACD player, two types of DACs (that are PCM DAC and high end DSD DAC), are used on AV board.

The audio data path to both DACs is routed via the Furore 2.

I²S audio

The STi55xx is capable of 6-channel I²S output. These channels can be configured to output 5.1 Dolby Digital, DTS, etc.

- PCM_OUT0: Left and Right.
- PCM_OUT1: Centre and LFE (subwoofer).
- PCM_OUT2: Left and Right surround.

Two additional channels (available in STi5580 and STi5588) are capable of providing down-mixed stereo.

S/PDIF

The S/PDIF signal level (pin 57, SPDIF_OUT) is 5V TTL at module interface. To meet the complete S/PDIF specifications, an external de-coupling circuit (item 7720, diagram M7) is implemented.

I²S karaoke (optional)

The STi5580 and STi5588 have built-in karaoke processing. The internal karaoke block accepts I²S signal, acting as the master by generating the required KOKPCMCLK frequency. This frequency is always 1/4 the music sampling frequency. An external analogue-to-digital converter (ADC), acting as slave, is required to convert the microphone signals to I²S signals.

CD-DA/DVD Data Path

The data path for CD-DA and DVD is as follows:

- I²S data from the M2 basic engine enters the STi55xx.

- The STi55xx processes the data, and sends the 6 PCM output channels to Furore 2. The LeRi channels are directly passed to the AV board also.
- The switch matrix of the Furore 2 sends the two incoming stereo PCM channels (LeRi) to the AV board.
- The switch matrix of the Furore 2 sends the six incoming PCM channels to the high end DAC board.
- The mute signal from the STi55xx is directly passed to the AV board. This requires a patch on the mono board.
- The IEC958 output of the STi55xx is fed directly to AV board.

The clock distribution is as follows:

- The master clock 384FS is received from the high end DAC board.
- From this clock the 27 MHz clock for STi5580 and the Furore 2 is derived (Video clock).
- From the 27 MHz clock the audio clock (256FS) is derived. The STi55xx and Furore 2 use this clock. For CD-DA FS amounts to 44.1 kHz, for DVD 48 or 96 kHz.
- In case of CD-DA, the high end DAC uses its internal clock (384FS). In case of DVD, the switch matrix of Furore 2 sends the audio clock (256FS) to the high end DAC on AV board.
- The AV board receives the 256FS clock.

Selection of the audio clock is done in the clock factory. For a description of the clock factory, see paragraph 'Clock Factory'.

SACD Data Path

The data path for DSD/DST is as follows:

- I²S data from the basic engine enters the Furore 2.
- The Furore 2 processes the data. This results in 6 DSD/DST channels.
- The switch matrix of the Furore 2 sends the 6 DSD/DST channels to the high end DAC on AV board.
- The 6 DSD/DST channels are down mixed to a stereo PCM signal.
- The switch matrix of the Furore 2 sends the stereo PCM signal to the AV board.
- The mute signal from the STi55xx is directly passed to the AV board. This requires a patch on the mono board.

The clock distribution is as follows:

- The master clock 384FS is received from the high end DAC on AV board.
- From this clock the 27 MHz clock for STi55xx and Furore 2 is derived (video clock).
- From the 27 MHz clock the audio clock (256FS) is derived. The STi5580 and Furore 2 use this clock.
- The high end DAC on AV board uses its own XTAL clock (384FS). The 256FS clock to the DAC board is switched off, to prevent for interference.
- The AV board receives the 256FS clock.

Selection of the audio clock is done in the clock factory. For a description of the clock factory, see paragraph 'Clock Factory'.

Video Output

Digital video (optional)

Digital YUV output is routed directly from STi55xx ports to a 24-pin connector (item 1704). From the same connector, the HSYNC, VSYNC and 27MHZ_CLK signals are available. The digital YUV connector is the interface to external video processing devices; such as high quality progressive scan codex and high quality video DAC.

Analogue video

The STi55xx is capable of 6-channel analogue video. Three channels (pins 25, 26 and 27) are RGB or YUV format, while the other three channels (pins 32, 33 and 34) are Y, C, and CVBS.

A video output buffer (see diagram M7, e.g. item 7701 for R) is implemented: an 8MHz/16MHz selectable filter stage and a 75Ω drive stage.

Clock Factory

One clock factory is implemented to support all clocks required by the Furore 2. The various master clock, which depends on whether SACD is present, is used for SD4.00_SA_CH. The clock factory of SD4.00_SA_CH is showed in Figure 8-2.

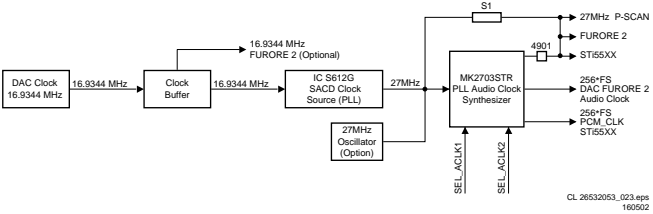


Figure 9-7 Block diagram clock factory

For the SACD player, the clock system is a DAC master clock system. For non-SACD player, the clock system is a mono board master clock system.

The Furore 2 supports clock 256*FS/384*FS/512*FS. The most convenient value in the market is 16.9344 MHz (384*FS, FS=44.1kHz). Therefore, the master clock on the SD4.00_SA_CH mono board is the 384*FS coming from the A/ V board. The 384*FS (16.9344 MHz) from the DAC clock, must always be present. It is buffered before it is sent to the Furore 2 and the rest of the clock factory. The IC S612G delivers a 27 MHz system clock.

The Furore 2 and Sti5580/Sti5588 (Video) use this clock. It is used to derive the PCM audio clocks 256*FS by the MK2703STR. This IC is also used to buffer the incoming 27 MHz clock.

The communication between the Sti55xx and the Furore 2 is asynchronous.

To support non-SACD playback, an on-board 27MHz oscillator delivers the master clock for SD4.00_SA_CH mono board.

Miscellaneous

Most general IO ports are connected directly to the module interface. Compared with the SD3.0 module, some on-board circuits are removed, as it made more sense (and more cost effective) to implement these circuits externally.

SCART Status Signal

The SCART0 and SCART1 signals are directly available at the module interface, where the 0_6_12V signal is generated. See table below:

Table 9-1 0_6_12V SCART status truth table

Function	PIO3_6 (SCART0)	PIO3_7 (SCART1)	0_6_12V (at SCART connector)
TV display	1	1	0V
TV display	0	1	0V
16:9 aspect ratio	1	0	+6V
4:3 aspect ratio	0	0	+12V

Mute

The audio MUTE signal (active 'high') is directly available at the module interface.

Service

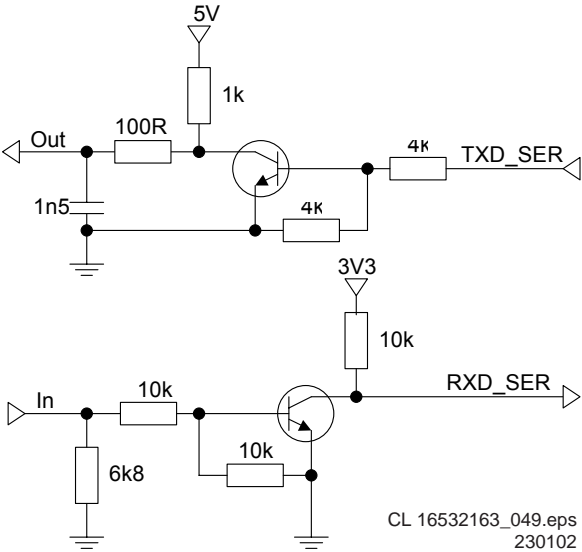


Figure 9-8 Service Port Buffer

The service port (see diagram M5) is simplified to reduce cost. The unused RTS and CTS lines are no longer connected. A transistor buffer (item 7502) is used instead of the Schmitt Trigger buffer (item 7501).

The overall loading and driving capability of the RS-232 emulator port is not greatly changed. However, as a precaution, the Schmitt Trigger circuit remains in the layout as an optional implementation.

This SD4.0SA_CH has the same ComPair connector as in previous DVD generations. Flashing of the application-SW is not possible with the ComPair cable, except with a CD-R disc. For sets with Mask-ROM software, replace it with a programmed Flash (available via your Philips Service organisation).

Power Supply (diagram M7)

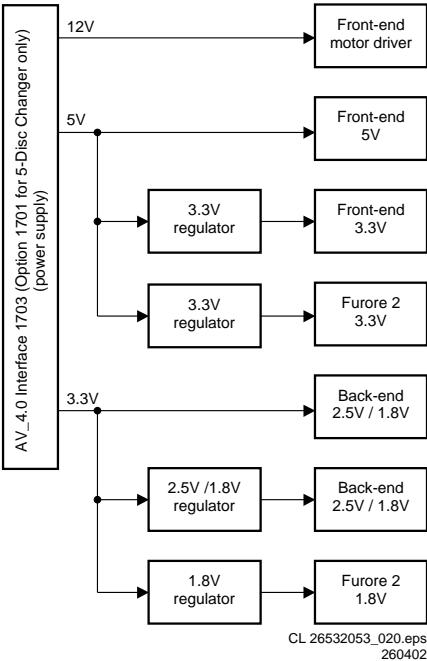


Figure 9-9 Mono Board Power Supply Block Diagram

The main power supplies to the module are 3.3V, 5V, and 12V (input via connector 1703).

The SACD DSD/DST decoder Furore 2 uses 1.8V for its core and analogue portion, and 3.3V for its interface. The on-board 1.8V linear regulator LF18ABDT and 3.3V linear LD1117DT33 are used to generate 1.8V and 3.3V power supply respectively. The back-end section mainly uses the 1.8V or 2.5V and 3.3V, which depend on which back-end processor is used. The on-board linear regulators LF25ABDT or LF18ABDT are used to generate the 2.5V (or 1.8V) required by the STi55xx. The front-end section mainly uses the 5V and 12V. An on-board linear regulator LD1117DT33 can be used to generate the 3.3V required by the front-end. The 12V is used by the motor and servo drivers.

Reset Circuit

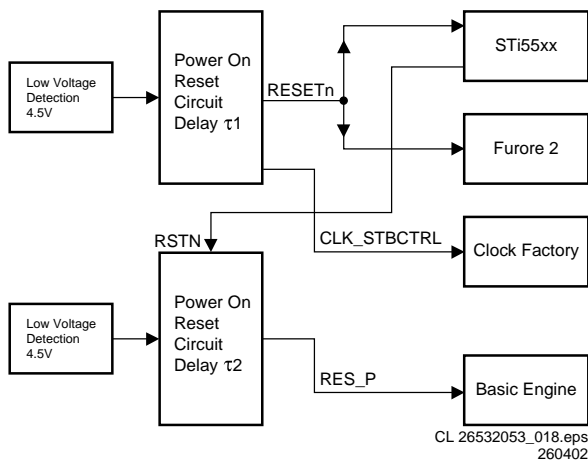


Figure 9-10 Block diagram of reset circuit

This reset circuit takes care that booting the different devices on the mono board takes place in the correct order. The correct reset order is:

1. The Power On Reset circuit (delay τ_1) creates a reset signal 'RESETn' to reset the STi55xx and Furore .
2. In the meantime, the Power On Reset circuit (delay τ_1) creates a reset signal 'CLK_STBCTRL', which is inverted to 'RESETn', to enable the Clock Factory.
3. Then, the Power On Reset circuit (delay τ_2) generates a reset signal 'RES_P' to reset the Basic Engine.
4. The STi55xx can now reset the Basic Engine via 'RSTN'.

9.4 Audio/Video (A/V) Board

This board is the interface panel between the DVD-player and its peripherals.

9.4.1 Block diagram

See Block diagram A/V Board in chapter 6.

9.4.2 Control

The control of the A/V board is done by I²C-decoder IC7107 (see table below):

Table 9-2 Control lines overview IC7107

Description	Pin	Hi	Lo
CLK_SEL	12	External clock (DSD stream)	Internal clock (PCM stream)
UPSAMPLING	11	No upsampling	Upsampling
DAC_RESET	10	Normal	Reset
CENTER_ON	9	Off	On
SPDIF-MUTE	5	Mute	No mute
192k/96k	4	192k PCM	96k PCM

9.4.3 Video

The analogue video signals from the Mono Board are buffered before they are fed to the A/V board. The video outputs from the A/V Board are RGB/YUV, YC, and CVBS. These signals are also fed to the double SCART board.

9.4.4 Audio

The digital audio signals for Surround, Centre and LFE (subwoofer) are fed to a 6-channel DAC CS4362 (item 7300, 48-pin LQFP) for the audio output. This DAC accepts both DSD and PCM data streams.

The digital audio signals for Front and Stereo are fed to a high performance current output DAC AD1955 (item 7301) via a multiplexer (item 7200) which selects either the direct PCM or an upsampled data stream (determined by the UPSAMPLING control line).

The Upsampling is only applicable for CD playback, and the AD1895 sample rate converter (item 7201) converts the 44.1 kHz/16 bit audio to various formats such as 96 kHz/24 bit or 192 kHz/24 bit.

The bit and word clock, required by the upsampling DAC AD1955 (item 7301), are derived from the 24.576 MHz master clock (item7103) via counter (item 7204/7205).

The S/PDIF, only supports digital output upsampling of 96 kHz/ 24 bit and is encoded by the digital audio transmitter AK4103 (item7203).

There is a control line from IC7107, called CENTRE_ON, which is used to switch between the centre channel and front channels for both SACD- and DVD modes.

9.5 Progressive Scan Board

The DVD963SA series offer progressive scan YUV outputs, and the option to select the interlace YUV output by a sliding switch on the board (selectable at the rear of the player).

9.5.1 Block diagram

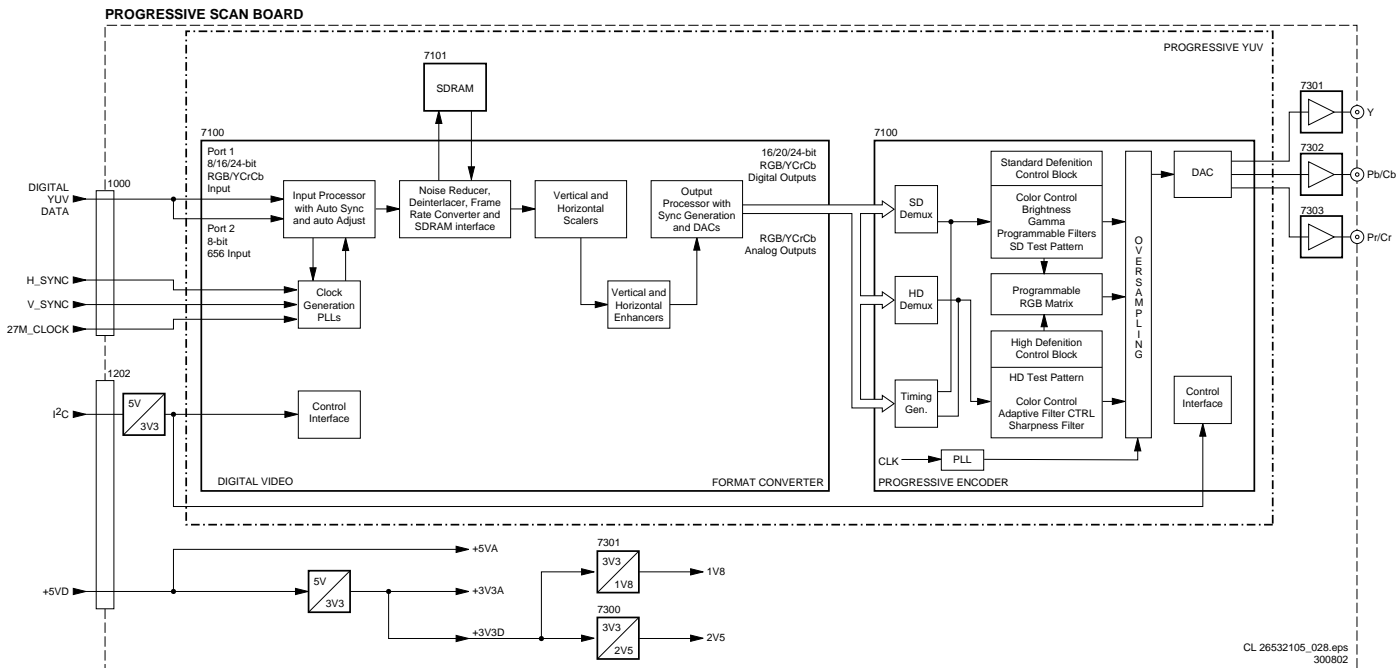


Figure 9-11 Block diagram Progressive Scan Board

9.5.2 Progressive YUV

This creates a picture signal with double the scan lines of a conventional interlaced picture, to create a noticeably sharper and smoother image. It offers higher picture resolution and eliminates virtually all motion artefacts. Even on large screens, the progressive scan lines are barely noticeable and it reduces picture flickering significant.

Two new IC's are used: the Faroudja FLI2310 Digital Video Format Converter, and the Analog Devices ADV7300 Progressive Scan Video Encoder.

This board also offers the Digital Crystal Clear feature, which allow you to fine-tune the following parameters:

- Gamma correction.
- Chroma and Luma delay.
- Sharpness.
- DCDi: to produce a smooth and natural looking image without visible artifacts (like jagged edges).
- True life: brings out details in the picture, producing a more life-like image.

9.6 Double SCART

This board consists of two SCART connectors and a SCART-switch (item 7501, STV6410), which is controlled by I²C. In a P50 player with double SCART connectors, video and audio loop-through must be available. This loop-through is controlled by the P50 commands, which are send via pin 10 of the SCART connector. When the DVD player is in standby, the loop-through is active. During standby, the display (slave) processor is active (takes over the I²C-bus), and will manage the P50 commands.

Note: The loop-through only functions when the DVD-player is in standby (NOT in low power standby)

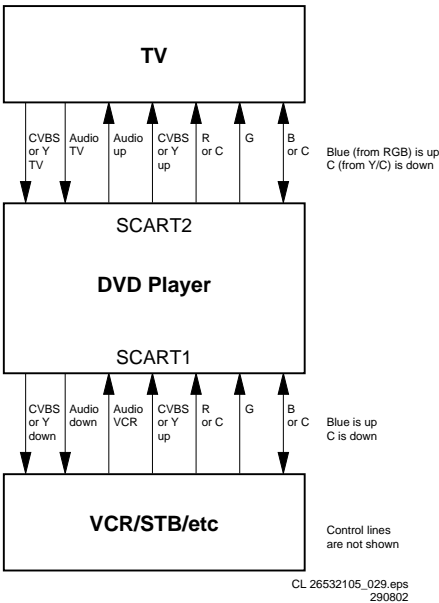


Figure 9-12 Loop-through set-up

The B component (from RGB) and the C component (from Y/ C) share the same pin on the SCART connector (pin 7). Because the B-signal is an "up-stream" signal (towards the TV) and the C-signal is a "down-stream" signal (towards the VCR), this pin 7 must be a bi-directional pin. Because the STV6410

does not support this, an external circuit (two bi-directional switches, named switch "1" and switch "2") is added. The Y- and CVBS-components also share the same pins.

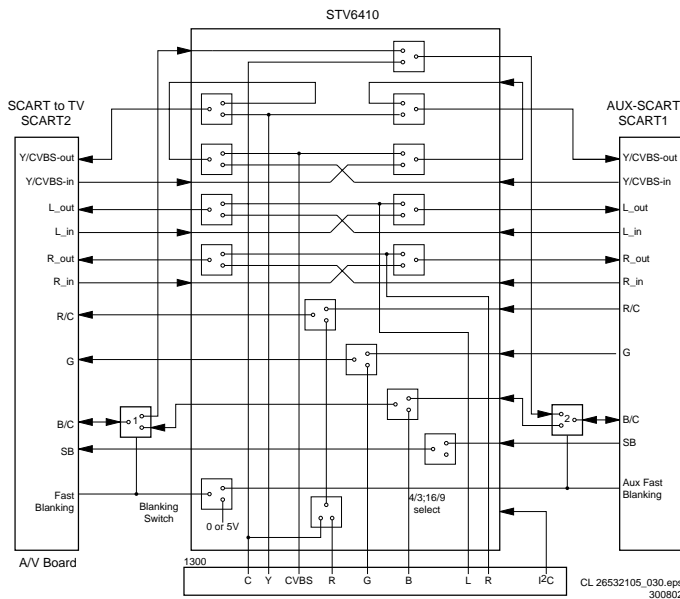


Figure 9-13 SCART switching circuitry

- Switch "1" is controlled by the FB (Fast Blanking) pin (pin 16) of SCART2 (TV SCART):
 - If the FB line is low, C is input on the B/C of SCART2.
 - If the FB line is high, B is output on the B/C of SCART2.
- Switch "2" is controlled by the FB (Fast Blanking) pin (pin 16) of SCART1 (AUX SCART):
 - If the FB line is low, C is output on the B/C of SCART1.
 - If the FB line is high, B is input on the B/C of SCART1.

We can distinguish four situations:

1. DVD player is ON, DVD is active (DVD mode):
NO LOOP-TROUGH.
2. DVD player is ON, DVD is not active (TV mode):
LOOP-TROUGH.
3. DVD player is ON, external source becomes active:
LOOP-TROUGH.
4. DVD player is in STBY:
LOOP-TROUGH.

9.7 Control and Display Panel

9.7.1 Control

Slave processor

The key component on this board is the (slave) microprocessor (item 7401). It runs on an 8 MHz system clock generated with a ceramic resonator (item 1119) and has a reset circuit (item 7105) that is triggered by the +5Vstb voltage.

The start-up sequence is as follows:

1. The required IC voltage is the +5Vstb, which is present during Standby Mode.
2. When the RESET circuit (item 7105) is triggered by the +5Vstb, the slave uP initialises.
3. This will set the STDBY_CONTROL signal to LOW, which will switch on the 12V and subsequently the +3V3.
4. Once these voltages are provided, the host uP (on the mono board) will reset (via reset circuit around item 7412 on the mono board, diagram M4).
5. Now, the host uP will initialise, and indicate the slave uP to activate the Standby Mode (STBY_CONTROL) signal.
6. The player wakes up from the Standby Mode when any button is pressed on the front panel, or when the 'Power' button is pressed on the Remote Control.

Also, when going to Standby, the slave processor will reset the host processor. When the slave processor receives the correct IR (or key) code to leave the Standby mode, it resets the host processor.

Note: The slave uP will not reset successfully, if the 8MHz clock oscillator has not stabilised (check on pin 8 of IC7101).

Other slave processor functions are:

- Generation of a scanning grid for the keys,
- Generation of the display grid and segment scanning,
- Generation of square signal to generate the filament voltage for FTD display,
- Inputs for RC5/6 and P50 (P50 controller is build in).

Standby LED

Transistor 7104 drives the Standby LED. When the STBLEAD signal from the slave processor is 'high' (pin 14), the LED is 'off'.

Key Matrix

When a key on the local keyboard is pressed, the signal at the scanning pins of the microprocessor (pins 34, 35, 36 and 37) go from +5 V to 0 V.

IR Receiver

The IR controller in the slave processor handles both RC5 and RC6 signals (input on pin 22). The logic is +5 V for 'high' and 0 V for 'low'.

Status LEDs

The four status LEDs (SOUND, AUDIO DIRECT, UP-SAMPLING, and P-SCAN) are controlled by the slave processor.

P50 Interface

P50 (or Easylink) is a bi-directional serial interface for communication between video equipment. For European sets, this communication goes via pin 10 of the SCART connector, while for other regions (when present), this is a cinch connector. The slave processor controls the P50 bus (via pins 11 and 20).

9.7.2 Display

Because these DVD players have an AC display (FTD) and a DC supply, an AC voltage is created by the microprocessor. The slave processor has an internal square signal generator (42 kHz, duty cycle 45/55), to generate the AC filament voltage. This square voltage is amplified (by TS7106 and 7109) and decoupled (C2138 and C2143) before it is applied to the display. The necessary voltage of -32 V comes directly from the PSU.

9.8 Abbreviation list

ADC	Analogue to Digital Converter	SRAM	Static RAM
AGC	Automatic Gain Control	STBY	Standby
ASD	Architecture and Standard Design	SVCD	Super Video CD
AM	Amplitude Modulation	SW	Software
BE	Basic Engine	THD	Total Harmonic Distortion
ComPair	Computer aided rePair	TTL	Transistor Transistor Logic (5V logic)
CD-DA	CD Digital Audio	uP	Microprocessor
CS	Chip Select	VAL	Video Audio Loader
CVBS	Composite Video Blanking and Synchronisation	VCD	Video CD
DAC	Digital to Analogue Converter	Y/C	Luminance (Y) and Chrominance (C) signal
DAIO	Digital Audio Input Output	YUV	Component video
DEMUX	De-multiplexer		
DENC	Digital Encoder		
DFU	Direction For Use: description for the end user		
DNR	Dynamic Noise Reduction		
DRAM	Dynamic Random Access Memory		
DSD	Direct Stream Digital		
DSP	Digital Signal Processing		
DST	Direct Stream Transfer (= loss less compressed DSD signal)		
DTS	Digital Theatre Sound		
DVD	Digital Versatile Disc		
EEPROM	Electrically Erasable and Programmable Read Only Memory		
EFM	Eight to Fourteen bit Modulation		
EMI	External Memory Interface (STi55xx)		
FFC	Flat Foil Cable		
FLASH	Flash memory		
HPF	High Pass Filter		
HW	Hardware		
I ² C	Integrated IC bus (signals at 5V level)		
I ² S	Integrated IC Sound bus (signals at 3.3V level)		
IC	Integrated Circuit		
IF	Intermediate Frequency		
IRQ	Interrupt Request		
KOK	Karaoke		
LFE	Low Frequency Effect (= subwoofer)		
LLD	Loss Less Decoder		
LPCM	Linear Pulse Code Modulation		
LRCLK	Left/Right clock		
LVTTL	Low Voltage Transistor Transistor Logic (3.3V logic)		
M2	Mercury 2 Basic Engine		
MACE	Mini All Compact Disc Engine		
MPEG	Motion Pictures Experts Group		
NC	Not Connected		
NVM	Non Volatile Memory (= IC containing TV related data e.g. alignments)		
OC	Open Circuit		
OPU	Optical Pick-up Unit		
PCB	Printed Circuit Board (see PWB)		
PCM	Pulse Code Modulation		
PCM_CLK	Audio system clock for DAC		
PCM_OUTx	Audio serial output data		
PSP	Pit Signal Processing		
PSU	Power Supply Unit		
PWB	Printed Wiring Board (see PCB)		
RAM	Random Access Memory		
RGB	Red, Green and Blue colour space		
ROM	Read Only Memory		
S2B	Serial to Basic Engine (= communication bus between host- and servo processor)		
SCL	Serial Clock I ² C		
SCLK	Audio serial bit clock		
SDA	Serial Data I ² C		
SDRAM	Synchronous DRAM		
SMI	Shared Memory Interface		
S/PDIF	Sony Philips Digital InterFace		

9.9 IC Data

In this paragraph, the internal block diagrams and pinning are given of ICs that are drawn as 'black box' in the electrical diagrams (with the exception of 'memory' and 'logic' ICs).

9.9.1 Diagram Power Supply: TY72011P2 (item 7130)

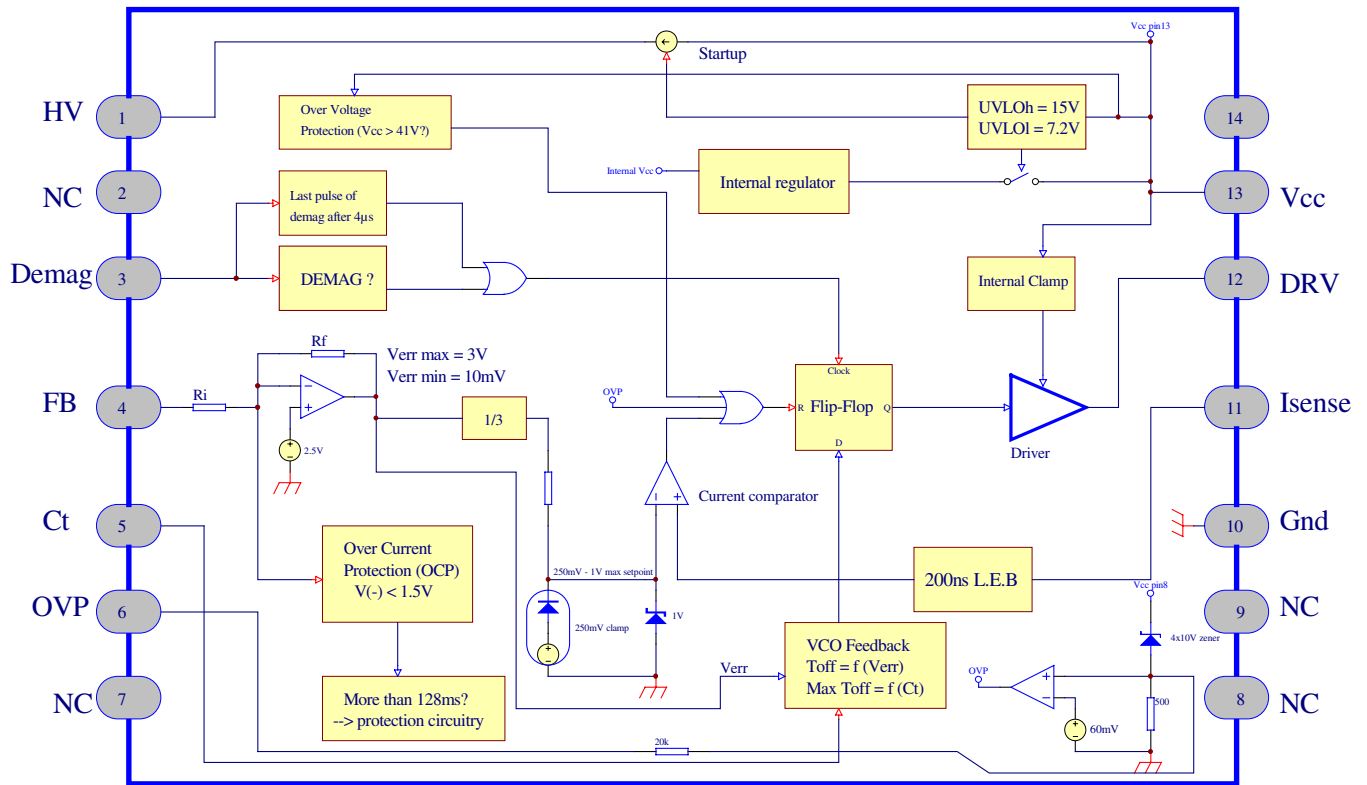


Figure 9-14 Block Diagram

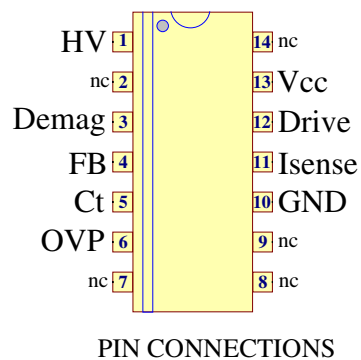
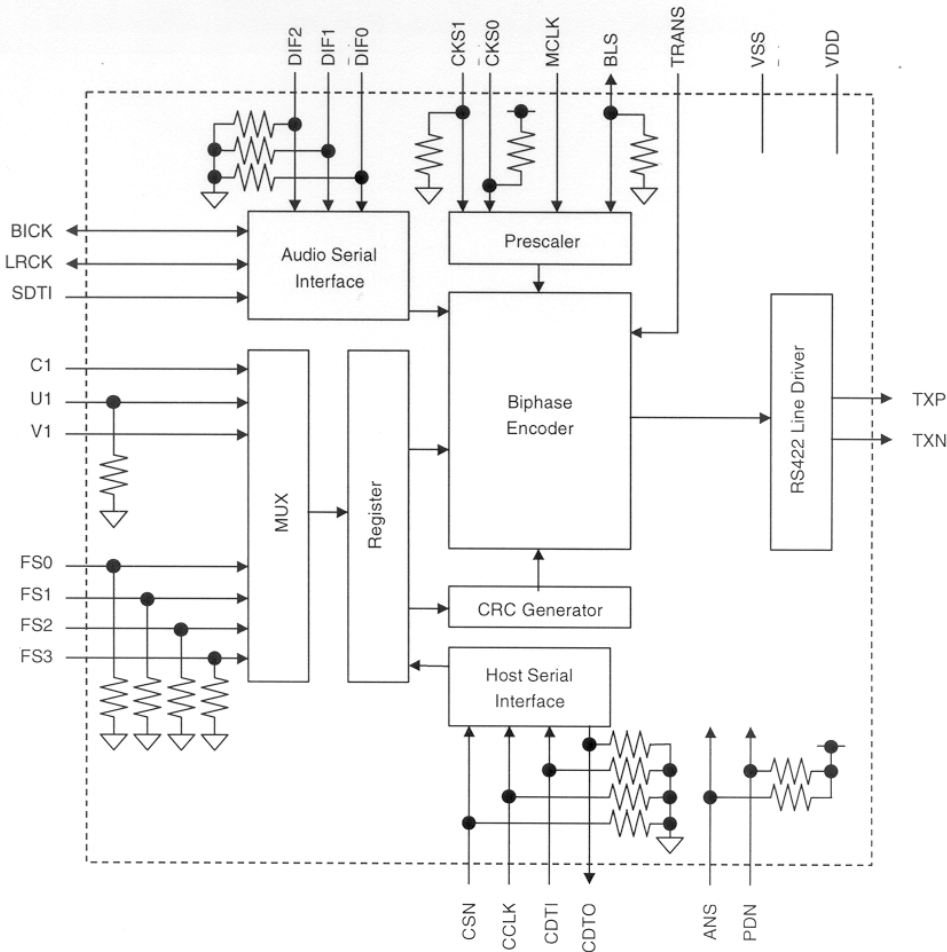


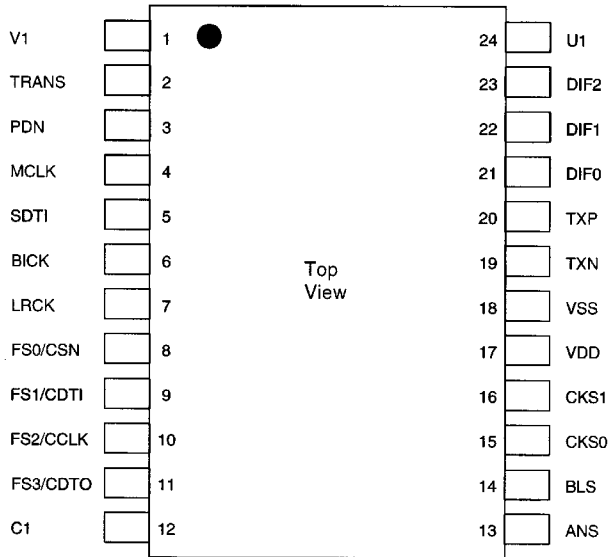
Figure 9-15 Pinning

9.9.2 Diagram A2: AK4103VF (IC7203)

■ Block Diagram



■ Pin Layout



CL 26532105_054.eps
300802

Figure 9-16 AK4103VF (IC7203)

9.9.3 Diagram A3: CS4362 (IC7300)

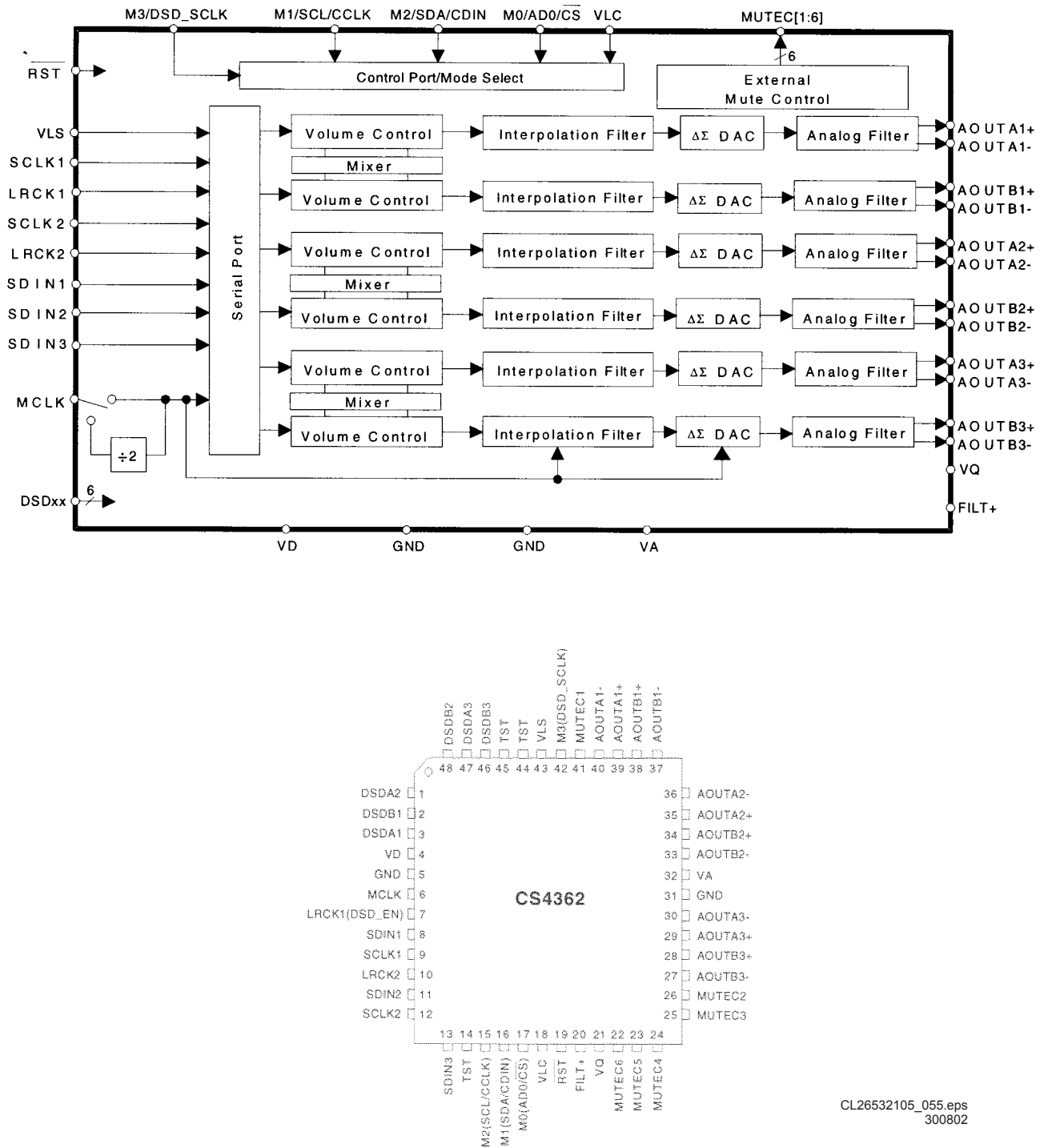


Figure 9-17 CS4362 (IC7300)

9.9.4 Diagram P1: FLI2301 (item 7100)

BLOCK DIAGRAM

Simplified Internal Block Diagram

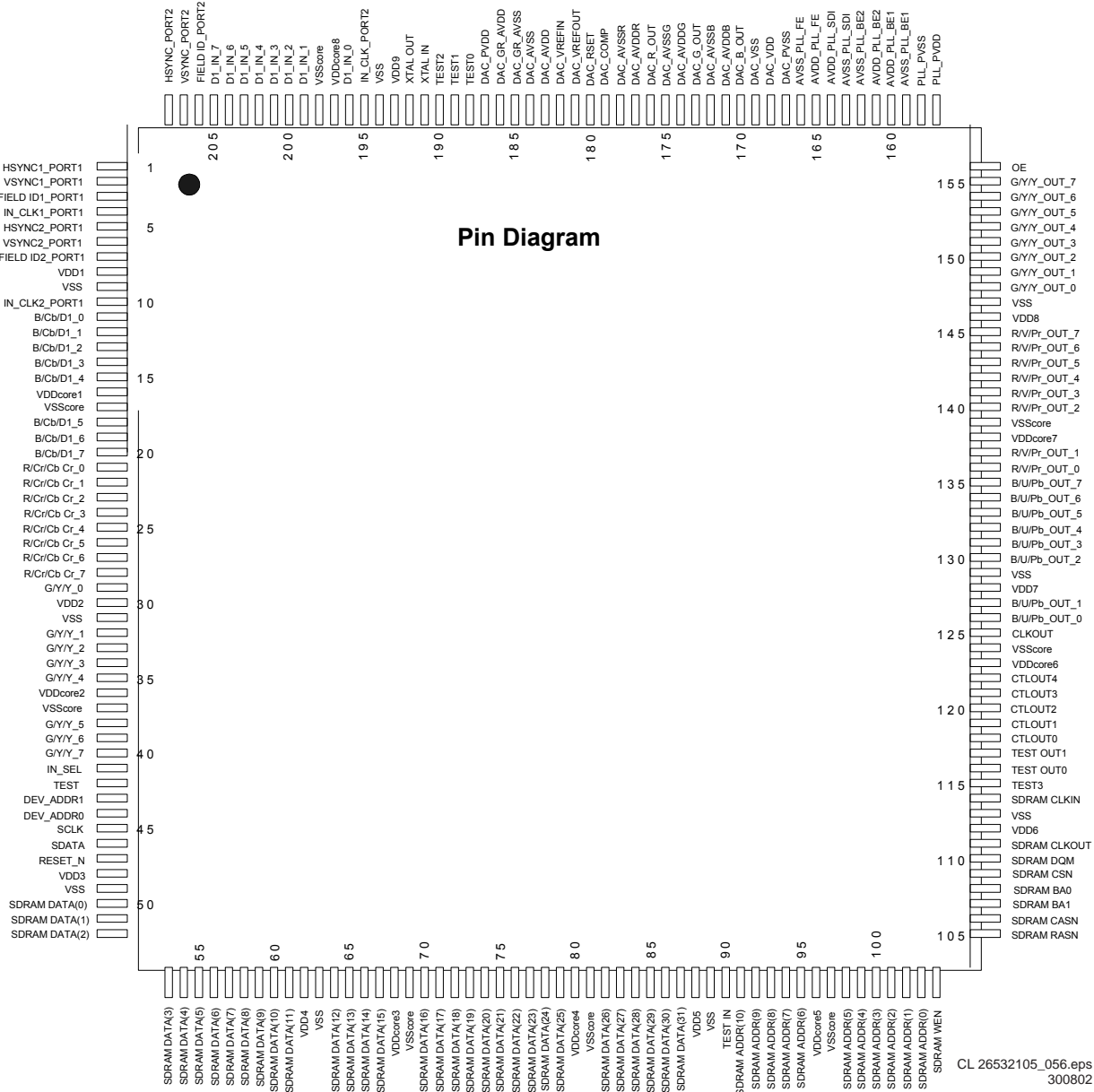
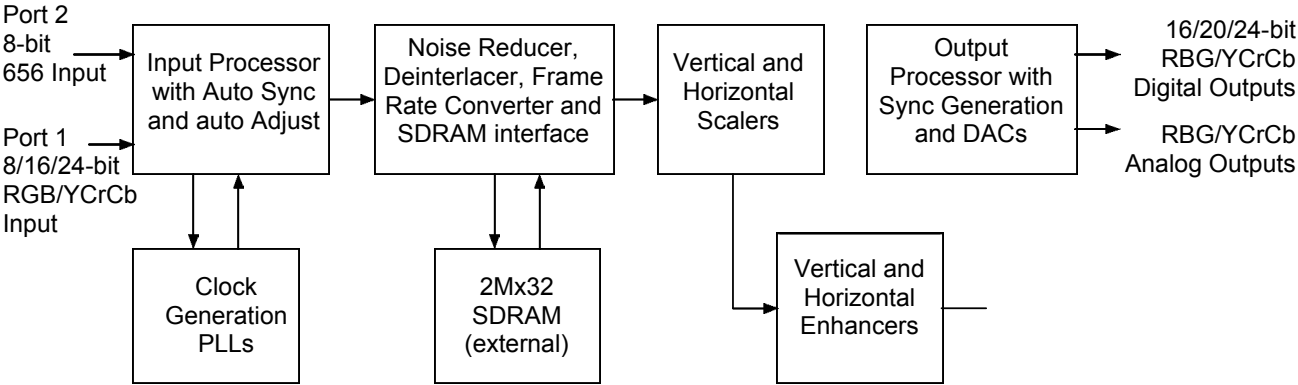


Figure 9-18 FLI2301 (item 7100)

10. Spare Parts List

Mechanical /001

Various

0002	3139 247 56601	FRONT ASSY DVD963SA/00X
0078	3139 243 10161	FILTER FTD SACD900
0150	3139 247 56581	BOTTOM FRAME ASSY DVD963SA/00X
0155	2422 030 00395	SOC POW H 3P M AC IEC B
0250	3139 247 56571	PLATE BACK DVD963SA/00X PPT
0300	3139 247 51691	COVER TOP DVD956K PNT PRT
0350	3139 238 02121	PROD. ASSY RC19237006/01H PKD
0360▲	2422 070 98224	MAINSCORD IEC 10A 1M5 DET BK B
0365	3103 308 92610	CABLE AUDIO 2X2RCA MALE 1.5MTR
0366	4822 321 61579	VIDEO-CABLE
0372	2422 076 00468	CBLE SCART 1M1 SCART 21P BK B
0375	3139 246 11991	IFU DVD963SA/00X
1010▲	3122 427 23261	PSU SUPER AUDIO CD WR HE
1103	3139 110 36071	FFC FOIL 30P/100/30P BD 1MMP
1104	3139 110 36071	FFC FOIL 30P/100/30P BD 1MMP
1108	3139 241 00191	FFC FOIL 24P/180/24P BD1MMP FD
1110	3139 241 00201	FFC FOIL 22P/220/22P BD 1MMP
0374	3139 246 12131	IFU2 DVD963SA/00X

Mechanical /171

Various

0002	3139 247 56601	FRONT ASSY DVD963SA/00X
0078	3139 243 10161	FILTER FTD SACD900
0150	3139 247 56581	BOTTOM FRAME ASSY DVD963SA/00X
0155	2422 030 00395	SOC POW H 3P M AC IEC B
0250	3139 247 56561	PLATE BACK DVD963SA/17X PPT
0300	3139 247 51691	COVER TOP DVD956K PNT PRT
0350	3139 238 02121	PROD. ASSY RC19237006/01H PKD
0360▲	2422 070 98223	MAINSCORDUL 10A 1M5 DET BK B
0365	3103 308 92610	CABLE AUDIO 2X2RCA MALE 1.5MTR
0366	4822 321 61579	VIDEO-CABLE
0375	3139 246 11961	IFU DVD963SA/17X
1010▲	3122 427 23471	PSU SUPER AUDIO CD USA 35PS104
1103	3139 110 36071	FFC FOIL 30P/100/30P BD 1MMP
1104	3139 110 36071	FFC FOIL 30P/100/30P BD 1MMP
1108	3139 241 00191	FFC FOIL 24P/180/24P BD1MMP FD

Mechanical /691

Various

0002	3139 247 56601	FRONT ASSY DVD963SA/00X
0078	3139 243 10161	FILTER FTD SACD900
0150	3139 247 56581	BOTTOM FRAME ASSY DVD963SA/00X
0155	2422 030 00395	SOC POW H 3P M AC IEC B
0250	3139 247 56561	PLATE BACK DVD963SA/17X PPT
0300	3139 247 51691	COVER TOP DVD956K PNT PRT
0350	3139 238 02121	PROD. ASSY RC19237006/01H PKD
0360▲	2422 070 98224	MAINSCORD IEC 10A 1M5 DET BK B

0365	3103 308 92610	CABLE AUDIO 2X2RCA MALE 1.5MTR
0366	4822 321 61579	VIDEO-CABLE
0375	3139 246 11951	IFU DVD963SA/69X
1010▲	3122 427 23261	PSU SUPER AUDIO CD WR HE
1103	3139 110 36071	FFC FOIL 30P/100/30P BD 1MMP
1104	3139 110 36071	FFC FOIL 30P/100/30P BD 1MMP
1108	3139 241 00191	FFC FOIL 24P/180/24P BD1MMP FD

SD4.00SA Module

Various

0001	9305 023 61114	VAL6011/14
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AV PWB

Various

1101	2422 543 01255	RES XTL SM 24MHz 576 30P AT-51R
1102	2422 543 01297	RES XTL SM 33M8688 16P NX6035R
1400	2422 025 17433	CON BM V 30P F 1.00 FFC 0.3 B
1401	2422 026 05345	SOC CINCH H 4P F YEWHYERD Y
1402	2422 026 05088	CON BM CINCH H 6P F B
1403	4822 265 11154	52030-2210 (22P)
1408	2422 026 05188	CON BM MDIN H 3P F TCS7927 B
1410	4822 267 31626	
1420	2422 025 10772	CON BM V 12P M 2.00 PH B
1421	2422 025 17433	CON BM V 30P F 1.00 FFC 0.3 B
1422	2422 025 17467	CON BM V 8P M 2.50 MIS B






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2100	2238 586 59812	0603 50V 100NP80M
2101	4822 122 33761	22pF 5% 50V
2102	4822 126 11785	0603 50V 47P 5%
2103	4822 122 33752	15pF 5% 50V
2104	4822 122 33752	15pF 5% 50V
2105	4822 126 11663	12pF
2106	4822 126 11663	12pF
2107	4822 122 33752	15pF 5% 50V
2108	2238 586 59812	0603 50V 100NP80M
2110	2238 586 59812	0603 50V 100NP80M
2111	2238 586 59812	0603 50V 100NP80M
2112	2238 586 59812	0603 50V 100NP80M
2113	2238 586 59812	0603 50V 100NP80M
2114	2020 552 93645	0603 50V 39P 5%
2115	2020 552 93645	0603 50V 39P 5%
2116	4822 122 33761	22pF 5% 50V
2117	2238 586 59812	0603 50V 100NP80M
2118	4822 126 11663	12pF
2119	4822 126 11663	12pF
2200	2238 586 59812	0603 50V 100NP80M
2201	2238 586 59812	0603 50V 100NP80M
2202	2238 586 59812	0603 50V 100NP80M
2203	2238 586 59812	0603 50V 100NP80M
2204	2238 586 59812	0603 50V 100NP80M
2205	4822 124 40248	10μF 20% 63V
2206	4822 124 40248	10μF 20% 63V
2207	2238 586 59812	0603 50V 100NP80M
2208	2238 586 59812	0603 50V 100NP80M
2209	2238 586 59812	0603 50V 100NP80M
2210	4822 124 40248	10μF 20% 63V
2211	2238 586 59812	0603 50V 100NP80M
2212	4822 124 40248	10μF 20% 63V
2213	4822 124 40248	10μF 20% 63V
2230	2238 586 59812	0603 50V 100NP80M
2301	4822 124 40248	10μF 20% 63V
2302	4822 124 21913	1μF 20% 63V
2303	2238 586 59812	0603 50V 100NP80M
2304	2238 586 59812	0603 50V 100NP80M
2305	4822 124 21913	1μF 20% 63V
2306	2238 586 59812	0603 50V 100NP80M
2307	4822 124 40433	47μF 20% 25V
2308	2238 586 59812	0603 50V 100NP80M
2309	4822 124 21913	1μF 20% 63V

2310	2238 586 59812	0603 50V 100NP80M
2311	4822 124 41584	100μF 20% 10V
2312	4822 124 41584	100μF 20% 10V
2313	2238 586 59812	0603 50V 100NP80M
2314	4822 124 40433	47μF 20% 25V
2315	2238 586 59812	0603 50V 100NP80M
2316	2238 586 59812	0603 50V 100NP80M
2317	4822 124 40248	10μF 20% 63V
2318	2238 586 59812	0603 50V 100NP80M
2319	4822 124 21913	1μF 20% 63V
2321	2020 552 94427	0603 50V 100P 5%
2323	2020 552 94427	0603 50V 100P 5%
2400	2238 586 59812	0603 50V 100NP80M
2401	2238 586 59812	0603 50V 100NP80M
2402	2238 586 59812	0603 50V 100NP80M
2403	2238 586 59812	0603 50V 100NP80M
2404	2238 586 59812	0603 50V 100NP80M
2405	2238 586 59812	0603 50V 100NP80M
2406	2238 586 59812	0603 50V 100NP80M
2407	2238 586 59812	0603 50V 100NP80M
2408	2238 586 59812	0603 50V 100NP80M
2409	2238 586 59812	0603 50V 100NP80M
2410	2238 586 59812	0603 50V 100NP80M
2411	2238 586 59812	0603 50V 100NP80M
2412	4822 124 40207	100μF 20% 25V
2413	4822 124 40207	100μF 20% 25V
2414	2020 552 94427	0603 50V 100P 5%
2415	2020 552 94427	0603 50V 100P 5%
2416	2020 552 94427	0603 50V 100P 5%
2417	3198 016 36810	0603 25V 680P COL R
2418	4822 126 13881	470pF 5% 50V
2419	3198 016 36810	0603 25V 680P COL R
2420	4822 126 13881	470pF 5% 50V
2421	2020 012 93792	EL FG 16V S 100μF PM20 A
2422	2020 552 94427	0603 50V 100P 5%
2423	2020 552 94427	0603 50V 100P 5%
2424	2020 552 94427	0603 50V 100P 5%
2425	3198 016 36810	0603 25V 680P COL R
2426	4822 126 13881	470pF 5% 50V
2427	3198 016 36810	0603 25V 680P COL R
2428	4822 126 13881	470pF 5% 50V
2429	2020 012 93792	EL FG 16V S 100μF PM20 A
2431	3198 016 31020	0603 25V 1nF
2432	3198 016 31020	0603 25V 1nF
2434	3198 016 31020	0603 25V 1nF
2436	3198 016 31020	0603 25V 1nF
2437	3198 016 31020	0603 25V 1nF
2439	3198 016 31020	0603 25V 1nF
2440	3198 016 31020	0603 25V 1nF
2441	3198 016 31020	0603 25V 1nF
2442	4822 124 80195	470μF 20% 10V
2443	4822 124 80195	470μF 20% 10V
2444	4822 124 80195	470μF 20% 10V
2445	4822 124 80195	470μF 20% 10V
2500	2238 586 59812	0603 50V 100NP80M
2501	2238 586 59812	0603 50V 100NP80M
2502	2238 586 59812	0603 50V 100NP80M
2503	2238 586 59812	0603 50V 100NP80M
2510	4822 126 14506	270pF 5% 50V 0603
2511	4822 126 13881	470pF 5% 50V
2512	2020 552 94427	0603 50V 100P 5%
2513	2020 552 94427	0603 50V 100P 5%
2514	4822 126 13881	470pF 5% 50V
2515	4822 126 14506	270pF 5% 50V 0603
2516	4822 126 14506	270pF 5% 50V 0603
2517	2020 552 94427	0603 50V 100P 5%
2518	4822 126 13881	470pF 5% 50V
2519	4822 126 14506	270pF 5% 50V 0603
2520	2020 552 94427	0603 50V 100P 5%
2521	4822 126 13881	470pF 5% 50V
2522	4822 126 14506	270pF 5% 50V 0603
2523	4822 126 13881	470pF 5% 50V
2524	2020 552 94427	0603 50V 100P 5%
2525	2020 552 94427	0603 50V 100P 5%
2526	4822 126 13881	470pF 5% 50V
2527	4822 126 14506	270pF 5% 50V 0603
2528	4822 126 14506	270pF 5% 50V 0603
2529	4822 126 13881	470pF 5% 50V
2530	3198 016 31020	0603 25V 1nF
2531	3198 016 31020	0603 25V 1nF
2532	4822 124 40207	100μF 20% 25V
2533	2020 012 93792	EL FG 16V S 100μF PM20 A
2534	4822 124 40207	100μF 20% 25V
2535	2020 552 94427	0603 50V 100P 5%
2536	4822 124 40207	100μF 20% 25V
2537	2020 552 94427	0603 50V 100P 5%
2538	4822 126 13881	470pF 5% 50V
2539	4822 126 14506	270pF 5% 50V 0603
2540	3198 016 31020	0603 25V 1nF

2541	3198 016 31020	0603 25V 1nF	3305	5322 117 13018	1k0 1% 0.063W 0603 RC22H	3529	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2542	3198 016 31020	0603 25V 1nF	3306	4822 051 30759	75Ω 5% 0.062W	3530	5322 117 13022	22k 1% 0.063W 0603 RC22H
2543	3198 016 31020	0603 25V 1nF	3307	4822 051 30759	75Ω 5% 0.062W	3531	5322 117 13048	3k3 1% 0.063W 0603 RC22H
2544	3198 016 31020	0603 25V 1nF	3308	4822 051 30759	75Ω 5% 0.062W	3532	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2545	3198 016 31020	0603 25V 1nF	3310	4822 051 30103	10k 5% 0.062W	3533	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2546	4822 124 80875	220μF 20% 25V	3311	4822 051 30103	10k 5% 0.062W	3534	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2547	4822 124 80875	220μF 20% 25V	3313	4822 051 30103	10k 5% 0.062W	3535	5322 117 13022	22k 1% 0.063W 0603 RC22H
2600	4822 126 14494	22nF 10% 25V 0603	3314	4822 051 30103	10k 5% 0.062W	3536	5322 117 13048	3k3 1% 0.063W 0603 RC22H
2601	4822 126 13883	220pF 5% 50V	3400	4822 051 30103	10k 5% 0.062W	3537	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2602	4822 126 13883	220pF 5% 50V	3401	4822 051 30273	27k 5% 0.062W	3538	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2603	3198 017 44740	0603 10V 470nF COL	3403	5322 117 13049	470Ω 1% 0.063W 0603 RC22H	3539	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2604	4822 126 13883	220pF 5% 50V	3404	4822 051 30101	100Ω 5% 0.062W	3540	4822 051 30221	220Ω 5% 0.062W
2605	4822 126 13883	220pF 5% 50V	3405	5322 117 13061	180Ω 1% 0.063W 0603 RC22H	3541	4822 051 30102	1k 5% 0.062W
2606	4822 124 40433	47μF 20% 25V	3406	4822 051 30561	560Ω 5% 0.062W	3543	5322 117 13022	22k 1% 0.063W 0603 RC22H
2608	4822 124 40248	10μF 20% 63V	3408	4822 051 30101	100Ω 5% 0.062W	3544	5322 117 13048	3k3 1% 0.063W 0603 RC22H
2609	4822 124 40207	100μF 20% 25V	3409	5322 117 13049	470Ω 1% 0.063W 0603 RC22H	3545	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2610	4822 124 40433	47μF 20% 25V	3410	5322 117 13061	180Ω 1% 0.063W 0603 RC22H	3546	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2612	4822 124 40248	10μF 20% 63V	3411	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3547	4822 117 12706	10k 1% 0.063W CASE0603 RC22H
2613	4822 124 80791	470μF 16V 20% 105C DXH=8X11.5	3412	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3548	5322 117 13022	22k 1% 0.063W 0603 RC22H
2614	4822 124 40207	100μF 20% 25V	3413	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3549	5322 117 13048	3k3 1% 0.063W 0603 RC22H
2616	4822 124 40207	100μF 20% 25V	3414	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3550	4822 051 30221	220Ω 5% 0.062W
2617	2238 586 59812	0603 50V 100NP80M	3415	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3551	4822 051 30102	1k 5% 0.062W
2618	4822 124 40248	10μF 20% 63V	3416	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3552	4822 051 30103	10k 5% 0.062W
2619	4822 124 81144	1000μF 16V	3417	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3553	4822 051 30103	10k 5% 0.062W
2620	4822 124 40248	10μF 20% 63V	3418	4822 051 30101	100Ω 5% 0.062W	3555	4822 051 30471	470Ω 5% 0.062W
2621	2238 586 59812	0603 50V 100NP80M	3419	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3556	4822 051 30102	1k 5% 0.062W
2622	4822 126 11785	0603 50V 47P 5%	3421	5322 117 13049	470Ω 1% 0.063W 0603 RC22H	3558	4822 051 30471	470Ω 5% 0.062W
2623	4822 126 11785	0603 50V 47P 5%	3422	5322 117 13061	180Ω 1% 0.063W 0603 RC22H	3560	4822 051 30102	1k 5% 0.062W
2624	5322 126 11583	10nF 10% 50V 0603	3423	4822 051 30101	100Ω 5% 0.062W	3561	4822 051 30103	10k 5% 0.062W
2625	4822 124 40207	100μF 20% 25V	3424	4822 051 30561	560Ω 5% 0.062W	3562	4822 051 30103	10k 5% 0.062W
2626	4822 124 40207	100μF 20% 25V	3426	4822 051 30101	100Ω 5% 0.062W	3563	4822 051 30221	220Ω 5% 0.062W
2627	4822 124 80875	220μF 20% 25V	3427	5322 117 13049	470Ω 1% 0.063W 0603 RC22H	3564	4822 051 30102	1k 5% 0.062W
2628	4822 124 40433	47μF 20% 25V	3428	5322 117 13061	180Ω 1% 0.063W 0603 RC22H	3566	4822 051 30221	220Ω 5% 0.062W
2633	4822 124 40207	100μF 20% 25V	3429	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3567	4822 051 30102	1k 5% 0.062W
2634	2238 586 59812	0603 50V 100NP80M	3430	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3568	4822 051 30103	10k 5% 0.062W
2635	4822 124 40207	100μF 20% 25V	3431	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3570	4822 051 30103	10k 5% 0.062W
2636	2238 586 59812	0603 50V 100NP80M	3432	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3571	4822 051 30221	220Ω 5% 0.062W
2637	4822 124 40207	100μF 20% 25V	3433	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3572	4822 051 30102	1k 5% 0.062W
2638	2238 586 59812	0603 50V 100NP80M	3434	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3574	4822 051 30221	220Ω 5% 0.062W
2639	2238 586 59812	0603 50V 100NP80M	3435	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3575	4822 051 30102	1k 5% 0.062W
2640	2238 586 59812	0603 50V 100NP80M	3436	4822 051 30101	100Ω 5% 0.062W	3576	4822 051 30103	10k 5% 0.062W
2646	4822 124 40433	47μF 20% 25V	3437	5322 117 13052	2k7 1% 0.063W 0603 RC22H	3577	4822 051 30103	10k 5% 0.062W
-W-			3438	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3579	4822 117 12925	47k 1% 0.063W 0603
3100	4822 051 30101	100Ω 5% 0.062W	3439	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3580	4822 051 30101	100Ω 5% 0.062W
3101	4822 051 30101	100Ω 5% 0.062W	3440	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3581	4822 051 30472	4k7 5% 0.062W
3102	4822 051 30101	100Ω 5% 0.062W	3441	5322 117 13046	1k8 1% 0.063W 0603 RC22H	3582	4822 051 30103	10k 5% 0.062W
3103	4822 051 30101	100Ω 5% 0.062W	3501	4822 051 30109	10Ω 5% 0.062W	3583	4822 051 30103	10k 5% 0.062W
3104	4822 051 30101	100Ω 5% 0.062W	3502	4822 051 30109	10Ω 5% 0.062W	3584	4822 051 30103	10k 5% 0.062W
3105	4822 051 30101	100Ω 5% 0.062W	3504	4822 051 30109	10Ω 5% 0.062W	3590	4822 051 30332	3k3 5% 0.062W
3106	4822 051 30101	100Ω 5% 0.062W	3505	4822 051 30109	10Ω 5% 0.062W	3591	4822 117 12925	47k 1% 0.063W 0603
3107	4822 051 30101	100Ω 5% 0.062W	3506	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3592	4822 117 12925	47k 1% 0.063W 0603
3108	4822 051 30101	100Ω 5% 0.062W	3507	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3600	4822 051 30103	10k 5% 0.062W
3109	4822 051 30472	4k7 5% 0.062W	3508	5322 117 13022	22k 1% 0.063W 0603 RC22H	3601	4822 051 30332	3k3 5% 0.062W
3110	4822 051 30109	10Ω 5% 0.062W	3510	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3602	4822 051 30223	22k 5% 0.062W
3111	4822 051 30105	1M 5% 0.062W	3511	5322 117 13048	3k3 1% 0.063W 0603 RC22H	3603	4822 051 30222	2k2 5% 0.062W
3112	4822 051 30221	220Ω 5% 0.062W	3512	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3604	4822 051 30101	100Ω 5% 0.062W
3113	4822 051 30472	4k7 5% 0.062W	3513	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3605	4822 051 30101	100Ω 5% 0.062W
3114	4822 051 30472	4k7 5% 0.062W	3514	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3606	4822 051 30101	100Ω 5% 0.062W
3115	4822 051 30472	4k7 5% 0.062W	3515	5322 117 13048	3k3 1% 0.063W 0603 RC22H	3607	4822 051 30101	100Ω 5% 0.062W
3116	4822 051 30472	4k7 5% 0.062W	3516	5322 117 13022	22k 1% 0.063W 0603 RC22H	3608	4822 051 30101	100Ω 5% 0.062W
3117	4822 051 30472	4k7 5% 0.062W	3517	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3609	4822 051 30101	100Ω 5% 0.062W
3118	4822 051 30472	4k7 5% 0.062W	3518	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3610	4822 051 30103	10k 5% 0.062W
3119	4822 051 30472	4k7 5% 0.062W	3519	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3611	4822 051 30183	18k 5% 0.062W
3120	4822 051 30479	47Ω 5% 0.062W	3520	5322 117 13022	22k 1% 0.063W 0603 RC22H	3612	4822 051 30223	22k 5% 0.062W
3123	4822 051 30479	47Ω 5% 0.062W	3521	5322 117 13048	3k3 1% 0.063W 0603 RC22H	3614	4822 051 30569	56Ω 5% 0.062W
3124	4822 051 30332	3k3 5% 0.062W	3522	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3615	4822 051 30569	56Ω 5% 0.062W
3125	4822 051 30332	3k3 5% 0.062W	3523	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3616	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3126	4822 051 30332	3k3 5% 0.062W	3524	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3617	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3127	4822 051 30332	3k3 5% 0.062W	3525	5322 117 13022	22k 1% 0.063W 0603 RC22H	3618	4822 051 30569	56Ω 5% 0.063W
3128	4822 051 30479	47Ω 5% 0.062W	3526	5322 117 13048	3k3 1% 0.063W 0603 RC22H	3619	4822 051 30471	470Ω 5% 0.062W
3129	4822 051 30105	1M 5% 0.062W	3527	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3620	4822 051 30221	220Ω 5% 0.062W
3130	4822 051 30221	220Ω 5% 0.062W	3528	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3621	4822 051 30153	15k 5% 0.062W
3200	4822 051 30273	27k 5% 0.062W	3529	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3622	4822 051 30222	2k2 5% 0.062W
3201	4822 117 11151	1Ω 5%	3530	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3623	4822 051 30759	75Ω 5% 0.062W
3202	4822 117 11151	1Ω 5%	3531	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3624	4822 051 30222	2k2 5% 0.062W
3203	4822 051 30273	27k 5% 0.062W	3532	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3625	4822 051 30759	75Ω 5% 0.062W
3204	4822 051 30472	4k7 5% 0.062W	3533	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3626	4822 051 30222	2k2 5% 0.062W
3205	4822 051 30222	2k2 5% 0.062W	3534	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3627	4822 051 30759	75Ω 5% 0.062W
3206	4822 051 30222	2k2 5% 0.062W	3535	5322 117 13022	22k 1% 0.063W 0603 RC22H	3628	4822 051 30222	2k2 5% 0.062W
3207	4822 051 30222	2k2 5% 0.062W	3536	5322 117 13048	3k3 1% 0.063W 0603 RC22H	3629	4822 051 30759	75Ω 5% 0.062W
3208	4822 051 30331	330Ω 5% 0.062W	3537	4822 117 12706	10k 1% 0.063W CASE0603 RC22H	3630	4822 051 30222	2k2 5% 0.062W
3210	4822 051 302							

3632	4822 051 30222	2k2 5% 0.062W	7204	9352 105 20118	IC SM 74LVC161DB (PHSE) R	— —
3633	4822 051 30759	75Ω 5% 0.062W	7205	9352 105 20118	IC SM 74LVC161DB (PHSE) R	2102 3198 024 44730 47nF 50V 0603
3634	4822 051 30103	10k 5% 0.062W	7206	9352 424 10118	IC SM 74LVC04ADB (PHSE) R	2103 4822 124 41584 100μF 20% 10V
3635	4822 051 30103	10k 5% 0.062W	7207	9322 186 80668	IC SM 74AC00M (ST00) R	2104 2020 552 94427 0603 50V 100P 5%
3636	4822 051 30103	10k 5% 0.062W	7220	5322 130 60159	BC846B	2105 2020 552 94427 0603 50V 100P 5%
3637	4822 117 12902	8k2 1% 0.063W 0603	7221	5322 130 60159	BC846B	2106 2020 552 94427 0603 50V 100P 5%
3638	4822 051 30183	18k 5% 0.062W	7222	5322 130 60159	BC846B	2107 4822 124 81151 22μF 50V
3639	4822 051 30101	100Ω 5% 0.062W	7300	9322 177 92671	IC SM CS4362-KQ (CILO) Y	2108 5322 126 11578 1nF 10% 50V 0603
3640	4822 051 30222	2k2 5% 0.062W	7301	9322 178 69668	IC SM AD1955ARS (ANA0) R	2122 4822 126 13883 220pF 5% 50V
3641	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM	7400	5322 209 14481	HEF4053BT	2123 4822 126 13883 220pF 5% 50V
3642	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM	7401	4822 209 30095	LM833D	2124 4822 126 14549 33nF 16V 0603
3643	4822 051 30101	100Ω 5% 0.062W	7402	4822 209 30095	LM833D	2127 4822 124 40207 100μF 20% 25V
3644	4822 051 30101	100Ω 5% 0.062W	7403	9322 184 52668	IC SM AD8022AR (ANA0) R	2129 3198 024 44730 47nF 50V 0603
3645	4822 051 30222	2k2 5% 0.062W	7404	9322 184 52668	IC SM AD8022AR (ANA0) R	2131 3198 024 44730 47nF 50V 0603
3646	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM	7405	9322 190 07685	IC SM AD8032AR (ANA0) R	2135 4822 126 14549 33nF 16V 0603
3647	4822 051 30222	2k2 5% 0.062W	7420	4822 130 42804	BC817-25	2136 4822 124 40433 47μF 20% 25V
3648	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM	7501	9352 202 10118	IC SM NE5532AD8 (PHSE) R	2137 4822 126 11578 1nF 10% 50V 0603
3652	4822 051 30562	5k6 5% 0.063W 0603 RC21 RST SM	7502	9352 202 10118	IC SM NE5532AD8 (PHSE) R	2138 4822 124 81151 22μF 50V
3653	4822 051 30103	10k 5% 0.062W	7521	4822 130 42804	BC817-25	2139 4822 124 40769 4.7μF 20% 100V
3655	4822 051 30562	5k6 5% 0.063W 0603 RC21 RST SM	7522	4822 130 60373	BC856B	2140 4822 126 14549 33nF 16V 0603
3659	4822 117 11151	1Ω 5%	7523	4822 130 42804	BC817-25	2141 4822 126 14549 33nF 16V 0603
3661	4822 117 11152	407 5%	7525	4822 130 42804	BC817-25	2142 5322 126 11578 1nF 10% 50V 0603
3664	4822 117 11151	1Ω 5%	7527	4822 130 42804	BC817-25	2143 4822 124 81151 22μF 50V
3665	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM	7529	4822 130 42804	BC817-25	—WW—
3666	4822 117 11151	1Ω 5%	7531	4822 130 42804	BC817-25	3100 4822 051 30223 22k 5% 0.062W
3667	4822 117 11151	1Ω 5%	7533	4822 130 42804	BC817-25	3101 4822 117 12864 82k 5% 0.6V
3669	4822 051 30101	100Ω 5% 0.062W	7535	4822 130 42804	BC817-25	3102 4822 117 12864 82k 5% 0.6V
3670	4822 051 30101	100Ω 5% 0.062W	7536	4822 130 60373	BC856B	3103 4822 051 30273 27k 5% 0.062W
3671	4822 051 30472	4k7 5% 0.062W	7537	5322 130 60159	BC846B	3104 4822 117 12864 82k 5% 0.6V
3672	4822 117 12903	1k8 1% 0.063W 0603	7538	4822 130 60373	BC856B	3105 4822 051 30221 220Ω 5% 0.062W
3673	4822 051 30569	56Ω 5% 0.062W	7541	4822 130 60373	BC856B	3106 4822 117 12925 47k 1% 0.063W 0603
3674	4822 051 30332	3k3 5% 0.062W	7542	4822 130 60373	BC856B	3107 4822 117 13632 100k 1% 0603 0.62W
3675	4822 051 30332	3k3 5% 0.062W	7543	4822 130 60373	BC856B	3108 4822 051 30221 220Ω 5% 0.062W
3676	4822 051 30332	3k3 5% 0.062W	7600	4822 209 17398	LD1117DT33	3109 4822 051 30472 4k7 5% 0.062W
3677	4822 051 30471	470Ω 5% 0.062W	7601	9322 134 86668	LF80C	3110 4822 051 30472 4k7 5% 0.062W
3678	4822 051 30222	2k2 5% 0.062W	7602	9322 163 71668	IC SM MC79M08BDT (ONSE) R	3111 4822 051 30472 4k7 5% 0.062W
3679	4822 051 30221	220Ω 5% 0.062W	7603	9322 140 81668	IC SM LF50CDT (ST00) R	3112 4822 051 30472 4k7 5% 0.062W
3680	4822 051 30684	680k 5% 0.062W	7604	9322 155 28667	OPT FIB CON GP1FA550TZ (SRPJ)L	3113 4822 051 30472 4k7 5% 0.062W
3681	4822 051 30333	33k 5% 0.062W	7620	5322 130 60159	BC846B	3114 4822 051 30472 4k7 5% 0.062W
3682	4822 051 30562	5k6 5% 0.063W 0603 RC21 RST SM	7621	4822 130 42804	BC817-25	3115 4822 051 30103 10k 5% 0.062W
3683	4822 051 30103	10k 5% 0.062W	7622	4822 130 42804	BC817-25	3116 4822 051 30472 4k7 5% 0.062W
3684	4822 051 30103	10k 5% 0.062W	7623	4822 130 42804	BC817-25	3117 4822 051 30472 4k7 5% 0.062W
—			7624	4822 130 42804	BC817-25	3118 4822 051 30472 4k7 5% 0.062W
—			7625	4822 130 42804	BC817-25	3119 4822 051 30103 10k 5% 0.062W
5300	4822 157 10586	2.2μH 10% 0805	7626	4822 130 42804	BC817-25	3120 4822 051 30472 4k7 5% 0.062W
5301	4822 157 10586	2.2μH 10% 0805	7627	5322 130 60159	BC846B	3121 4822 051 30331 330Ω 5% 0.062W
5601	2422 535 94092	IND FXD SM 0805 33U PM10 R	7628	5322 130 60159	BC846B	3123 4822 051 30109 10Ω 5% 0.062W
5602	2422 535 94092	IND FXD SM 0805 33U PM10 R	7629	5322 130 60159	BC846B	3125 4822 051 30109 10Ω 5% 0.062W
5603	2422 535 94092	IND FXD SM 0805 33U PM10 R	7630	5322 130 60159	BC846B	3126 4822 051 30109 10Ω 5% 0.062W
5606	4822 157 11074	100μH	7631	4822 130 42804	BC817-25	3127 4822 117 11152 4Ω7 5%
5607	4822 157 70601	100μH (920927085A)	7632	4822 130 42804	BC817-25	3128 4822 117 11152 4Ω7 5%
—			7633	4822 130 42804	BC817-25	3129 4822 051 30331 330Ω 5% 0.062W
—			7636	4822 130 60373	BC856B	3130 4822 051 30331 330Ω 5% 0.062W
6500	4822 130 11397	BAS316	7638	5322 130 60159	BC846B	3131 4822 051 30472 4k7 5% 0.062W
6602	4822 130 11522	UDZ15B	7641	9322 163 53685	FET POW SM IRLML2502 (INR0) R	3133 4822 051 30471 470Ω 5% 0.062W
6603	4822 130 11522	UDZ15B	7642	5322 130 60159	BC846B	3134 4822 051 30109 10Ω 5% 0.062W
6604	4822 130 11522	UDZ15B	7643	5322 130 60159	BC846B	3135 4822 051 30102 1k 5% 0.062W
6606	4822 130 11397	BAS316	Front PWB			3138 4822 051 30103 10k 5% 0.062W
6607	4822 130 11522	UDZ15B	Various			3139 4822 051 30471 470Ω 5% 0.062W
—			0002	3139 244 00440	FTD HOLDER DVD711	3140 4822 051 30103 10k 5% 0.062W
—			1100	4822 267 10565	4P	3141 4822 051 30103 10k 5% 0.062W
—			1101	4822 276 13775	SWITCH	3142 4822 051 30109 10Ω 5% 0.062W
—			1102	4822 276 13775	SWITCH	3146 4822 051 30471 470Ω 5% 0.062W
—			1103	4822 276 13775	SWITCH	3147 4822 051 30471 470Ω 5% 0.062W
—			1104	4822 276 13775	SWITCH	3148 4822 051 30103 10k 5% 0.062W
—			1105	4822 276 13775	SWITCH	3149 4822 051 30103 10k 5% 0.062W
—			1106	4822 276 13775	SWITCH	3150 4822 051 30331 330Ω 5% 0.062W
—			1108	4822 267 10574	CON BM V 8P 2.50	3151 4822 051 30331 330Ω 5% 0.062W
—			1111	3139 240 50231	VFD 15-MT-70GNK (FTBO) Y	3152 4822 051 30471 470Ω 5% 0.062W
—			1112	4822 276 13775	SWITCH	3153 4822 051 30471 470Ω 5% 0.062W
—			1113	4822 276 13775	SWITCH	3154 4822 051 30103 10k 5% 0.062W
—			1114	4822 276 13775	SWITCH	3155 4822 051 30103 10k 5% 0.062W
—			1119	4822 242 72066	CST8,00MT	3156 4822 051 30331 330Ω 5% 0.062W
—			1201	4822 276 13775	SWITCH	3157 4822 051 30331 330Ω 5% 0.062W
—			1205	4822 267 10567	4P	3158 4822 051 30221 220Ω 5% 0.062W
7100	9965 000 06673	TC7WHU04FU	—			3200 4822 051 30101 100Ω 5% 0.062W
7101	9965 000 06673	TC7WHU04FU	—			—
7102	4822 209 17441	74LVC74AD	6100	9340 255 30135	BAS216	—
7103	9965 000 06673	TC7WHU04FU	6101	9965 000 04709	UDZ6.2BTE-17	—
7105	9352 499 60118	IC SM 74LVC00AD (PHSE) R	6103	9340 255 30135	BAS216	—
7106	9352 499 60118	IC SM 74LVC00AD (PHSE) R	6104	9322 185 23676	LED VS LTL1CHTDK3 (LITO) A	—
7107	5322 209 11578	PCF8574T	6105	9322 185 23676	LED VS LTL1CHTDK3 (LITO) A	—
7121	9965 000 04199	BSN20	6106	9322 185 23676	LED VS LTL1CHTDK3 (LITO) A	—
7122	9965 000 04199	BSN20				
7200	4822 209 17455	74LVC157AD				
7201	9322 177 89668	IC SM AD1895YRS (ANA0) R				
7202	9352 115 40118	IC SM 74LVC245APW (PHSE) R				
7203	9322 178 30669	IC SM AK4103VF (AKM0) R				

6107	9322 185 23676	LED VS LTL1CHTDK3 (LITO) A	2213	2238 586 59812	0603 50V 100NP80M	3161	4822 117 12139	22Ω 5% 0.062W
6200	9322 190 53676	LED VS LTL42HCWK5P (LITO) A	2214	2238 586 59812	0603 50V 100NP80M	3162	4822 117 12139	22Ω 5% 0.062W
			2215	2238 586 59812	0603 50V 100NP80M	3163	4822 117 12139	22Ω 5% 0.062W
7100	5322 130 60159	BC846B	2216	2238 586 59812	0603 50V 100NP80M	3164	4822 117 12139	22Ω 5% 0.062W
7101	5322 130 60159	BC846B	2217	2238 586 59812	0603 50V 100NP80M	3165	4822 117 12139	22Ω 5% 0.062W
7102	5322 130 60159	BC846B	2218	2238 586 59812	0603 50V 100NP80M	3166	4822 117 12139	22Ω 5% 0.062W
7103	5322 130 60159	BC846B	2220	2238 586 59812	0603 50V 100NP80M	3170	4822 051 30008	0Ω jumper
7104	4822 130 60373	BC856B	2221	5322 126 11579	3.3nF 10% 63V	3172	4822 051 30008	0Ω jumper
7105	5322 130 60159	BC846B	2222	3198 016 38210	0603 25V 820P COL	3173	4822 117 12139	22Ω 5% 0.062W
7106	4822 130 40981	BC337-25	2223	4822 124 40769	4.7μF 20% 100V	3174	4822 117 12139	22Ω 5% 0.062W
7108	5322 130 60159	BC846B	2300	2238 586 59812	0603 50V 100NP80M	3175	4822 117 12139	22Ω 5% 0.062W
7109	4822 130 40854	BC327	2301	4822 122 33761	22pF 5% 50V	3176	4822 117 12139	22Ω 5% 0.062W
7111	4822 130 60373	BC856B	2302	4822 126 11785	0603 50V 47P 5%	3177	4822 117 12139	22Ω 5% 0.062W
7112	4822 130 60373	BC856B	2304	4822 124 40184	1000μF 20% 10V	3178	4822 117 12139	22Ω 5% 0.062W
7113	4822 130 60373	BC856B	2305	2238 586 59812	0603 50V 100NP80M	3179	4822 117 12139	22Ω 5% 0.062W
7114	4822 130 60373	BC856B	2306	2238 586 59812	0603 50V 100NP80M	3180	4822 117 12139	22Ω 5% 0.062W
7401	3139 240 50221	TMP87CH74F-3NB2 DVDV2.21	2307	3198 017 44740	0603 10V 470nF COL	3181	4822 117 12139	22Ω 5% 0.062W
7402	9322 155 82667	IR RECEIVER TSOP2236	2308	4822 124 81286	47μF 20% 16V	3182	4822 117 12139	22Ω 5% 0.062W
P Scan PWB			2309	2238 586 59812	0603 50V 100NP80M	3183	4822 117 12139	22Ω 5% 0.062W
Various			2310	4822 122 33761	22pF 5% 50V	3201	4822 051 30472	4k7 5% 0.062W
1000	2422 025 17599	CON BM H 24P F 1.00 FFC 0.3 R	2311	4822 126 11785	0603 50V 47P 5%	3203	4822 051 30152	1k5 5% 0.062W
1003	2422 026 05191	CON BM CINCH H 3P F RDBUGN B	2313	2238 586 59812	0603 50V 100NP80M	3204	4822 117 12139	22Ω 5% 0.062W
1203	2422 127 00548	SWI SLID 1P 2POS SM SSSS21 R	2314	4822 124 40184	1000μF 20% 10V	3205	4822 051 30152	1k5 5% 0.062W
			2315	4822 124 81286	47μF 20% 16V	3206	4822 117 12139	22Ω 5% 0.062W
2100	4822 124 81286	47μF 20% 16V	2316	2238 586 59812	0603 50V 100NP80M	3207	4822 051 30472	4k7 5% 0.062W
2101	2238 586 59812	0603 50V 100NP80M	2317	4822 122 33761	22pF 5% 50V	3208	4822 051 30472	4k7 5% 0.062W
2102	3198 017 44740	0603 10V 470nF COL	2318	4822 126 11785	0603 50V 47P 5%	3209	4822 051 30101	100Ω 5% 0.062W
2103	4822 124 81286	47μF 20% 16V	2320	4822 124 81286	47μF 20% 16V	3210	4822 051 30103	10k 5% 0.062W
2105	2238 586 59812	0603 50V 100NP80M	2321	4822 124 81286	47μF 20% 16V	3211	4822 051 30101	100Ω 5% 0.062W
2106	2238 586 59812	0603 50V 100NP80M	2322	4822 124 81286	47μF 20% 16V	3212	4822 051 30103	10k 5% 0.062W
2107	4822 124 81286	47μF 20% 16V	2323	4822 124 81286	47μF 20% 16V	3213	4822 051 30101	100Ω 5% 0.062W
2108	4822 124 11947	10μF 20% 16V				3214	4822 051 30101	100Ω 5% 0.062W
2109	2238 586 59812	0603 50V 100NP80M	3100	4822 051 30109	10Ω 5% 0.062W	3215	4822 051 30472	4k7 5% 0.062W
2110	2238 586 59812	0603 50V 100NP80M	3101	4822 051 30151	150Ω 5% 0.062W	3216	4822 051 30681	680Ω 5% 0.062W
2111	2238 586 59812	0603 50V 100NP80M	3102	4822 117 12139	22Ω 5% 0.062W	3300	4822 051 30759	75Ω 5% 0.062W
2112	2238 586 59812	0603 50V 100NP80M	3103	4822 117 12139	22Ω 5% 0.062W	3302	2322 704 63001	300R 1% 0.063W 0603
2113	2238 586 59812	0603 50V 100NP80M	3104	4822 117 12139	22Ω 5% 0.062W	3303	2322 704 63001	300R 1% 0.063W 0603
2114	2238 586 59812	0603 50V 100NP80M	3107	4822 051 30101	100Ω 5% 0.062W	3305	4822 051 30102	1k 5% 0.062W
2115	2238 586 59812	0603 50V 100NP80M	3108	4822 051 30101	100Ω 5% 0.062W	3306	2322 704 63002	3k 1% 0.063W 0603
2116	2238 586 59812	0603 50V 100NP80M	3109	4822 117 12139	22Ω 5% 0.062W	3307	4822 051 30759	75Ω 5% 0.062W
2117	2238 586 59812	0603 50V 100NP80M	3110	4822 117 12139	22Ω 5% 0.062W	3309	2322 704 63001	300R 1% 0.063W 0603
2118	2238 586 59812	0603 50V 100NP80M	3111	4822 117 12139	22Ω 5% 0.062W	3310	2322 704 63001	300R 1% 0.063W 0603
2119	2238 586 59812	0603 50V 100NP80M	3112	4822 117 12139	22Ω 5% 0.062W	3312	4822 051 30102	1k 5% 0.062W
2120	2238 586 59812	0603 50V 100NP80M	3113	4822 117 12139	22Ω 5% 0.062W	3313	2322 704 63002	3k 1% 0.063W 0603
2121	2238 586 59812	0603 50V 100NP80M	3114	4822 117 12139	22Ω 5% 0.062W	3314	4822 117 11152	4Ω 7 5%
2122	2238 586 59812	0603 50V 100NP80M	3115	4822 117 12139	22Ω 5% 0.062W	3316	2322 704 63001	300R 1% 0.063W 0603
2123	2238 586 59812	0603 50V 100NP80M	3116	4822 117 12139	22Ω 5% 0.062W	3317	2322 704 63001	300R 1% 0.063W 0603
2124	2238 586 59812	0603 50V 100NP80M	3117	4822 117 12139	22Ω 5% 0.062W	3319	4822 051 30759	75Ω 5% 0.062W
2125	2238 586 59812	0603 50V 100NP80M	3118	4822 117 12139	22Ω 5% 0.062W	3320	4822 051 30102	1k 5% 0.062W
2126	2238 586 59812	0603 50V 100NP80M	3119	4822 117 12139	22Ω 5% 0.062W	3321	2322 704 63002	3k 1% 0.063W 0603
2127	2238 586 59812	0603 50V 100NP80M	3120	4822 117 12139	22Ω 5% 0.062W			
2128	2238 586 59812	0603 50V 100NP80M	3121	4822 117 12139	22Ω 5% 0.062W	5100	4822 051 30008	0 ohm jumper
2129	2238 586 59812	0603 50V 100NP80M	3122	4822 117 12139	22Ω 5% 0.062W	5101	4822 157 71593	10μH 10%
2130	4822 126 13879	220nF 20% 16V	3123	4822 117 12139	22Ω 5% 0.062W	5102	4822 157 71593	10μH 10%
2131	4822 126 13879	220nF 20% 16V	3124	4822 117 12139	22Ω 5% 0.062W	5201	4822 157 71593	10μH 10%
2132	4822 126 13879	220nF 20% 16V	3125	4822 117 12139	22Ω 5% 0.062W	5202	4822 157 11716	BLM21P300SPT
2133	4822 126 13879	220nF 20% 16V	3126	4822 117 12139	22Ω 5% 0.062W	5203	4822 157 11716	BLM21P300SPT
2134	2238 586 59812	0603 50V 100NP80M	3127	4822 117 12139	22Ω 5% 0.062W	5300	4822 157 10977	4.7μH 10%
2135	2238 586 59812	0603 50V 100NP80M	3128	4822 117 12139	22Ω 5% 0.062W	5301	3198 018 31580	FXDIND SM 0805 1U5 PM10 COL R
2136	2238 586 59812	0603 50V 100NP80M	3129	4822 117 12139	22Ω 5% 0.062W	5302	4822 051 30008	0R00 JUMPER
2137	2238 586 59812	0603 50V 100NP80M	3130	4822 117 12139	22Ω 5% 0.062W	5305	4822 157 10977	4.7μH 10%
2138	2238 586 59812	0603 50V 100NP80M	3131	4822 117 12139	22Ω 5% 0.062W	5306	3198 018 31580	FXDIND SM 0805 1U5 PM10 COL R
2139	2238 586 59812	0603 50V 100NP80M	3132	4822 117 12139	22Ω 5% 0.062W	5307	4822 157 10977	4.7μH 10%
2140	4822 126 13879	220nF 20% 16V	3133	4822 117 12139	22Ω 5% 0.062W	5308	3198 018 31580	FXDIND SM 0805 1U5 PM10 COL R
2141	4822 126 13879	220nF 20% 16V	3134	4822 117 12139	22Ω 5% 0.062W	5309	4822 157 71593	10μH 10%
2142	4822 126 13879	220nF 20% 16V	3135	4822 117 12139	22Ω 5% 0.062W	5310	4822 157 71593	10μH 10%
2143	4822 126 13879	220nF 20% 16V	3136	4822 117 12139	22Ω 5% 0.062W	5311	4822 157 71593	10μH 10%
2144	4822 126 13879	220nF 20% 16V	3137	4822 117 12139	22Ω 5% 0.062W	5312	4822 157 71593	10μH 10%
2145	4822 126 13879	220nF 20% 16V	3138	4822 117 12139	22Ω 5% 0.062W			
2146	4822 126 13879	220nF 20% 16V	3139	4822 117 12139	22Ω 5% 0.062W	7100	9322 189 94671	IC SM FLI2310
2147	4822 126 13879	220nF 20% 16V	3140	4822 117 12139	22Ω 5% 0.062W	7101	9322 166 71668	IC SM MT48LC2M32B2TG-6 (MRN0)R
2148	2238 586 59812	0603 50V 100NP80M	3141	4822 117 12139	22Ω 5% 0.062W	7103	9322 179 78668	IC SM LF18ABDT (ST00) R
2149	2238 586 59812	0603 50V 100NP80M	3142	4822 117 12139	22Ω 5% 0.062W	7200	9352 092 60118	IC SM 74LVT74DB (PHSE) R
2150	4822 122 33741	10pF 10% 50V	3143	4822 117 12139	22Ω 5% 0.062W	7201	5322 209 11578	PCF8574T
2209	2238 586 59812	0603 50V 100NP80M	3144	4822 117 12139	22Ω 5% 0.062W	7202	9322 178 70671	IC SM ADV7300ST (ANA0) Y
2210	2238 586 59812	0603 50V 100NP80M	3145	4822 117 12139	22Ω 5% 0.062W	7203	9352 092 90118	IC SM 74LVT86DB (PHSE) R
2211	2238 586 59812	0603 50V 100NP80M	3146	4822 117 12139	22Ω 5% 0.062W	7300	9322 142 88668	IC SM LF25CDT (ST00) R
2212	2238 586 59812	0603 50V 100NP80M	3147	4822 117 12139	22Ω 5% 0.062W	7301	9322 167 49685	IC SM AD8061ART (ANA0) R
			3148	4822 117 12139	22Ω 5% 0.062W	7302	9322 167 49685	IC SM AD8061ART (ANA0) R
			3149	4822 117 12139	22Ω 5% 0.062W	7303	9322 167 49685	IC SM AD8061ART (ANA0) R
			3150	4822 051 30472	4k7 5% 0.062W			
			3151	4822 117 12139	22Ω 5% 0.062W			
			3152	4822 117 12139	22Ω 5% 0.062W			
			3153	4822 117 12139	22Ω 5% 0.062W			
			3154	4822 117 12139	22Ω 5% 0.062W			
			3155	4822 117 12139	22Ω 5% 0.062W			
			3156	4822 117 12139	22Ω 5% 0.062W			
			3157	4822 117 12139	22Ω 5% 0.062W			
			3158	4822 117 12139	22Ω 5% 0.062W			
			3159	4822 117 12139	22Ω 5% 0.062W			
			3160	4822 117 12139	22Ω 5% 0.062W			

SCART PWB**Various**

1000	2422 025 12352	CON BM EURO H 21P F BK GRND-L
1001	2422 025 12352	CON BM EURO H 21P F BK GRND-L
1300	2422 025 16526	CON BM V 22P F 1.00 FFC 0.3 R

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2100	4822 124 11947	10μF 20% 16V
2101	4822 124 40207	100μF 20% 25V
2102	4822 126 14305	100nF 10% 16V 0603
2103	4822 126 14305	100nF 10% 16V 0603
2104	4822 126 14305	100nF 10% 16V 0603
2105	4822 122 33777	47pF 5% 63V
2106	4822 122 33777	47pF 5% 63V
2107	4822 126 14305	100nF 10% 16V 0603
2108	4822 126 14305	100nF 10% 16V 0603
2109	4822 126 14305	100nF 10% 16V 0603
2110	4822 124 41584	100μF 20% 10V
2111	4822 126 14494	22nF 10% 25V 0603
2112	4822 126 14305	100nF 10% 16V 0603
2113	4822 126 14305	100nF 10% 16V 0603
2114	3198 017 44740	0603 10V 470nF COL
2115	3198 017 44740	0603 10V 470nF COL
2116	3198 017 44740	0603 10V 470nF COL
2117	4822 124 11947	10μF 20% 16V
2118	4822 124 11947	10μF 20% 16V
2119	4822 124 21732	10μF 20% 25V
2120	3198 017 44740	0603 10V 470nF COL
2121	4822 124 40207	100μF 20% 25V
2122	4822 126 14305	100nF 10% 16V 0603
2200	4822 124 12032	4.7μF 20% 50V
2201	4822 126 13883	220pF 5% 50V
2202	4822 124 12032	4.7μF 20% 50V
2203	4822 124 12032	4.7μF 20% 50V
2204	4822 124 12032	4.7μF 20% 50V
2205	4822 122 31765	100pF 2% 63V 1206
2206	4822 126 13883	220pF 5% 50V
2207	4822 126 13883	220pF 5% 50V
2208	4822 126 13883	220pF 5% 50V
2209	4822 126 13883	220pF 5% 50V
2210	4822 126 13883	220pF 5% 50V
2211	4822 124 21732	10μF 20% 25V
2212	4822 124 12032	4.7μF 20% 50V
2213	4822 124 12032	4.7μF 20% 50V
2214	4822 124 12032	4.7μF 20% 50V
2215	4822 124 12032	4.7μF 20% 50V
2216	4822 126 13883	220pF 5% 50V
2217	4822 126 13883	220pF 5% 50V
2218	4822 122 31765	100pF 2% 63V 1206
2219	4822 124 11947	10μF 20% 16V
2220	4822 126 13883	220pF 5% 50V
2221	4822 124 11947	10μF 20% 16V
2222	4822 126 13883	220pF 5% 50V
2223	4822 126 13883	220pF 5% 50V
2224	4822 126 13883	220pF 5% 50V
2225	4822 124 21732	10μF 20% 25V
2226	4822 126 13883	220pF 5% 50V

—WW—

3100	4822 117 11152	4Ω 7 5%
3102	4822 051 30103	10k 5% 0.062W
3104	4822 051 30472	4k7 5% 0.062W
3105	4822 051 30103	10k 5% 0.062W
3106	4822 051 30103	10k 5% 0.062W
3107	4822 051 30101	100Ω 5% 0.062W
3108	4822 051 30101	100Ω 5% 0.062W
3109	4822 051 30472	4k7 5% 0.062W
3110	4822 051 30103	10k 5% 0.062W
3111	4822 051 30102	1k 5% 0.062W
3112	4822 051 30472	4k7 5% 0.062W
3113	4822 051 30103	10k 5% 0.062W
3114	4822 051 30471	470Ω 5% 0.062W
3115	4822 051 30471	470Ω 5% 0.062W
3116	4822 051 30103	10k 5% 0.062W
3117	4822 051 30472	4k7 5% 0.062W
3200	4822 051 30221	220Ω 5% 0.062W
3201	4822 117 12925	47k 1% 0.063W 0603
3202	4822 051 30471	470Ω 5% 0.062W
3203	4822 051 30273	27k 5% 0.062W
3204	4822 117 12925	47k 1% 0.063W 0603
3205	4822 051 30221	220Ω 5% 0.062W
3206	4822 117 12925	47k 1% 0.063W 0603
3207	4822 051 30273	27k 5% 0.062W
3208	4822 117 12925	47k 1% 0.063W 0603

3209	4822 051 30471	470Ω 5% 0.062W
3210	4822 051 30471	470Ω 5% 0.062W
3211	4822 051 30101	100Ω 5% 0.062W
3212	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3213	4822 051 30471	470Ω 5% 0.062W
3214	4822 051 30471	470Ω 5% 0.062W
3215	4822 051 30101	100Ω 5% 0.062W
3216	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3217	4822 051 30151	150Ω 5% 0.062W
3218	4822 051 30101	100Ω 5% 0.062W
3219	4822 051 30472	4k7 5% 0.062W
3220	4822 051 30472	4k7 5% 0.062W
3221	4822 051 30472	4k7 5% 0.062W
3222	4822 117 13632	100k 1% 0603 0.62W
3223	4822 051 30471	470Ω 5% 0.062W
3224	4822 051 30151	150Ω 5% 0.062W
3225	4822 051 30471	470Ω 5% 0.062W
3226	4822 051 30472	4k7 5% 0.062W
3227	4822 051 30471	470Ω 5% 0.062W
3228	4822 051 30222	2k2 5% 0.062W
3229	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3230	4822 051 30759	75Ω 5% 0.062W
3231	4822 051 30561	560Ω 5% 0.062W
3232	4822 051 30101	100Ω 5% 0.062W
3233	4822 051 30471	470Ω 5% 0.062W
3234	4822 051 30339	33Ω 5% 0.062W
3235	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3236	4822 051 30472	4k7 5% 0.062W
3237	4822 051 30562	5k6 5% 0.063W 0603 RC21 RST SM
3238	4822 051 30479	47Ω 5% 0.062W
3239	4822 051 30472	4k7 5% 0.062W
3240	4822 051 30102	1k 5% 0.062W
3241	4822 117 12925	47k 1% 0.063W 0603
3242	4822 051 30471	470Ω 5% 0.062W
3243	4822 117 12925	47k 1% 0.063W 0603
3244	4822 051 30272	2k7 5% 0.062W
3245	4822 051 30102	1k 5% 0.062W
3246	4822 117 12925	47k 1% 0.063W 0603
3247	4822 051 30471	470Ω 5% 0.062W
3248	4822 117 12925	47k 1% 0.063W 0603
3249	4822 051 30102	1k 5% 0.062W
3250	4822 117 12925	47k 1% 0.063W 0603
3251	4822 051 30471	470Ω 5% 0.062W
3252	4822 051 30471	470Ω 5% 0.062W
3253	4822 051 30472	4k7 5% 0.062W
3254	4822 051 30759	75Ω 5% 0.062W
3255	4822 051 30472	4k7 5% 0.062W
3257	4822 051 30759	75Ω 5% 0.062W
3258	4822 051 30471	470Ω 5% 0.062W
3259	4822 051 30339	33Ω 5% 0.062W
3260	4822 051 30561	560Ω 5% 0.062W
3261	4822 051 30472	4k7 5% 0.062W
3262	4822 051 30759	75Ω 5% 0.062W
3263	4822 051 30472	4k7 5% 0.062W
3264	4822 051 30479	47Ω 5% 0.062W
3266	4822 051 30759	75Ω 5% 0.062W
3267	4822 051 30472	4k7 5% 0.062W
3268	4822 051 30101	100Ω 5% 0.062W
3269	4822 051 30689	68Ω 5% 0.063W 0603 RC21 RST SM
3270	4822 051 30471	470Ω 5% 0.062W
3271	4822 051 30759	75Ω 5% 0.062W
3272	4822 051 30273	27k 5% 0.062W
3273	4822 117 12925	47k 1% 0.063W 0603
3275	4822 051 30272	2k7 5% 0.062W
3276	4822 051 30472	4k7 5% 0.062W
3277	4822 051 30561	560Ω 5% 0.062W
3278	4822 051 30561	560Ω 5% 0.062W

—D—

6208 9340 548 63115 PDZ15B



7102	4822 130 60511	BC847B
7103	4822 130 60511	BC847B
7104	4822 130 60511	BC847B
7105	4822 130 60511	BC847B
7106	4822 130 40854	BC327
7200	4822 130 42804	BC817-25
7201	4822 130 42804	BC817-25
7202	4822 130 60511	BC847B
7203	4822 130 60511	BC847B
7204	4822 130 60511	BC847B
7205	4822 130 60373	BC856B
7206	4822 130 60511	BC847B

7207	4822 130 60511	BC847B
7208	4822 130 60511	BC847B
7209	4822 130 60511	BC847B
7210	4822 130 42804	BC817-25
7211	4822 130 42804	BC817-25
7212	4822 130 60511	BC847B
7213	4822 130 60373	BC856B
7214	4822 130 60511	BC847B
7215	4822 130 42804	BC817-25
7216	4822 130 42804	BC817-25
7217	4822 130 60511	BC847B
7218	4822 130 60511	BC847B
7219	4822 130 42804	BC817-25
7220	4822 130 42804	BC817-25
7500	9322 134 86668	LF80C
7501	9322 135 59671	STV6411AD

11. Errata

11.1 Revision List

11.1.1 Manual 3122 785 12331

From production start, the new print layout of the Pscan board has been introduced.

- In this service manual the circuit diagrams, print layout and the belonging parts list are added.

In the manual 3122 785 12330, the Double Scart circuit diagrams and print layout was left out.

- In this version of the service manual, the circuit diagrams and print layout are added.

In this version, the Layout of Menu script, Audio nuclei table 5.8.1, Video nuclei table 5.8.7 and Furore nuclei table 5.8.8 are updated.

In this version, the value of C2102, C2129 and C2131 in the parts list of Front board has been changed to 470nF 50V.